Technische
Hochschule
Rosenheim
Technical University of Applied Sciences

Prof. Dr. Florian Künzner

Technical University of Applied Sciences Rosenheim, Computer Science

CA 1 – Intro

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Question

What is the second most important tool of a computer scientist?

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Question

What is the most important tool of a computer scientist?

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Goal

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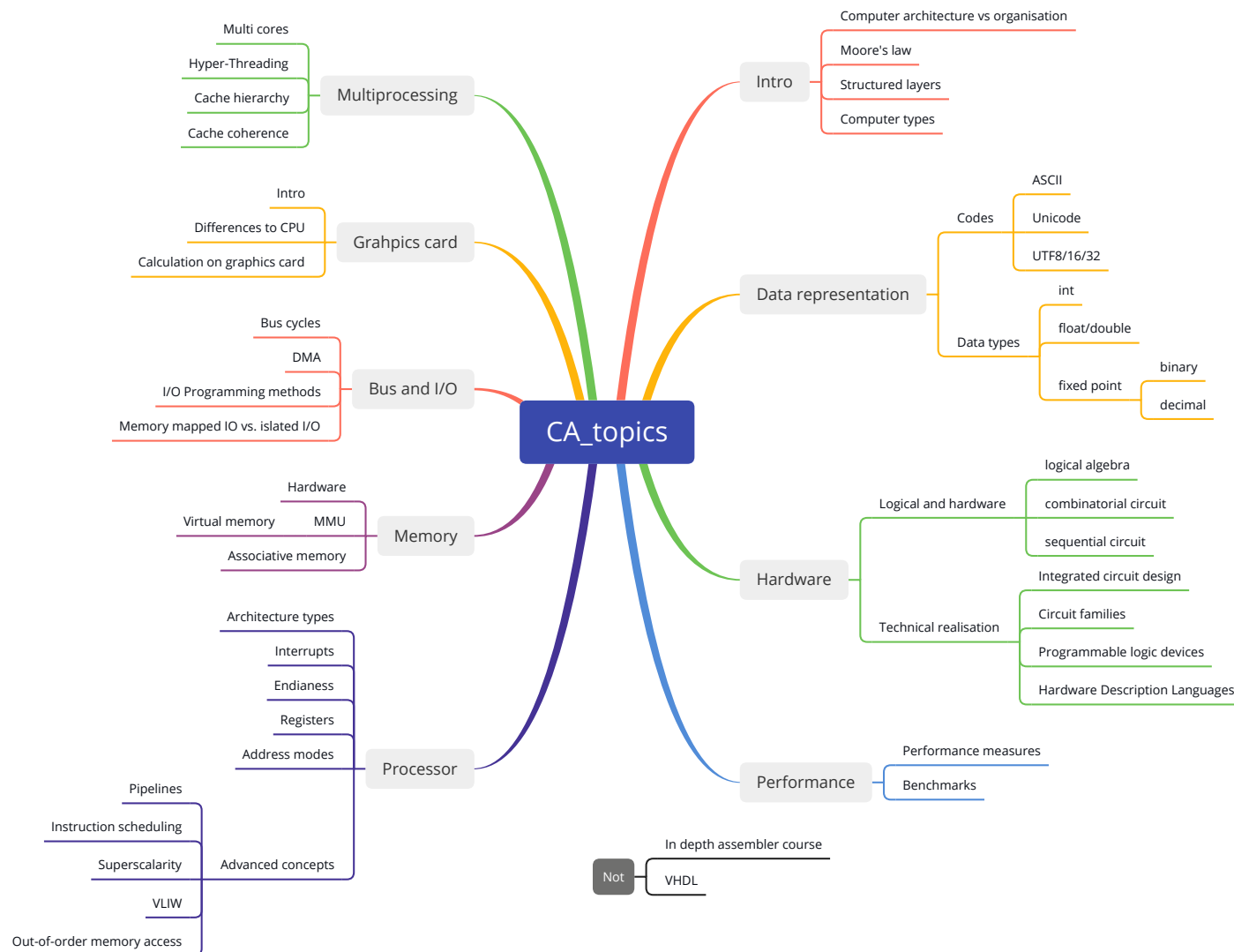
Goal

CA::Intro

- Motivation: Know why it is worth learning CA
- Computer architecture vs organisation
- Moore's law
- Structured layers
- Computer types
- Analogue vs digital

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Motivation: Why should you learn it?

Some reasons why it is worth studying:
- You should know the second most important tool
- Able to buy/specify hardware
- Optimise for hardware (hardware instructions)
- Write better software (algorithms)
- Find bugs or bottlenecks faster
- Embedded systems design and programming
- Real-time systems design and programming
- High performance computing programming
- Do understand computers now and in 5 (…) years

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Material

Material for lecture and exercises:

`https://inf-git.th-rosenheim.de/Lectures/CA_exercises.git`

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Time and date

Event	Day	Time	Room
Lecture	Wednesday	08:00 - 09:30 o'clock	A2.08
Exercise 1	Wednesday	11:45 - 13:15 o'clock	A2.08
Exercise 2	Wednesday	13:45 - 15:15 o'clock	A2.08
Exercise 3	Wednesday	15:30 - 17:00 o'clock	A2.08

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Lecture

- Presentation of concepts

- Discussion of concepts

- Mostly an introduction into concepts

- Reality is very complex

- Hardware evolves very quickly

- There is a large variety of different hardware for different purposes

Best learning experience: discuss with me!

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Exercise

Exercise content:

- Theoretical tasks
- C, C++, Java, and assembler coding
- Some microcontroller programming
- **Homework may also be necessary!!!**

Equipment:

- Updated repository with new exercise sheets
- You should have a PC (notebook) with a Linux and/or the virtual machine for CA (or virtual machine from OS)
- There are some notebooks to borrow (up to 8)

Question

What are the components of a computer?

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What is computer architecture?
DEFINITION OF ARCHITECTURE USED BY IBM

In IBM 370 Principles of Operations, the **architecture** of a computer is defined as "**its attributes as seen by the programmer**; that is, the **conceptual structure and functional behavior** as distinct from the **organization** of the **data flow**, the **logical design**, the **physical design**, and the **performance** of any particular implementation.

Several dissimilar machine implementations may conform to a single architecture. When programs running on different machine implementations produce the results that are defined by a single architecture, the implementations are considered to be compatible."

[source: Prasad: IBM Mainframes. McGraw-Hill 1989]

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What is computer architecture?

COMPUTER ARCHITECTURE vs COMPUTER ORGANIZATION

Computer **architecture** is a description (definition) of the **attributes** of a computing system as seen by a **machine language programmer** or a **compiler writer**. Writable control stores for modifying microcode during computer operation are not considered available to the normal machine language programmer.

Computer **organization** pertains to the various methods that can be used to **implement a specific computer architecture**.

[source: Hintz/Tabak: Microcontrollers. McGraw-Hill 1992.]

Computer architecture vs organisation
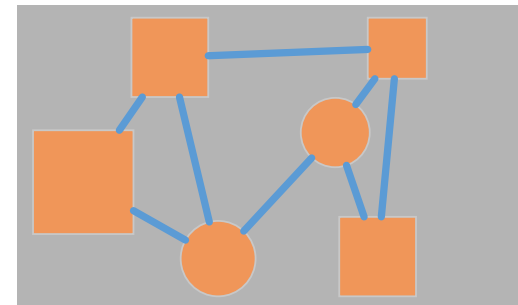
Computer architecture

> ADD R1,R2,R3
> MOV R1, (R1)
> TAS

logical (interface)

Machine language programmer, compiler writer:

- Conceptual structure
- Functional behaviour

Computer organisation

physical (implementation)

- Data flow
- Logical design
- Physical design
- Performance

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In this lecture

We consider both:

- Computer architecture
- Computer organisation

But the focus is more on: **Computer architecture**.

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Literature (1)

Rechnerarchitektur
Von der digitalen Logik zum Parallelrechner

Author(s)   Andrew S. Tanenbaum, Todd Austin
Date   1. March 2014
Edition   6. edition
Language   German
ISBN   978-3868942385
Reference   [1]

[source: https://www.pearson-studium.de]

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Literature (2)

Computer Organization and Design RISC-V Edition
The Hardware Software Interface

Author(s) David A. Patterson, John L. Hennessy
Date   22. May 2017
Edition   RISC-V ed
Language   English
ISBN   978-0128122754
Reference   [2]

[source: https://www.amazon.de]

Literature (3)

Grundlagen der Technischen Informatik

Author(s)	Dirk W. Hoffmann
Date	5. September 2016
Edition	5. edition
Language	German
ISBN	978-3446448674
Reference	[3]

[source: https://www.hanser-fachbuch.de]

Moore's Law: The number of transistors on microchips doubles every two years

Our World in Data

Moore's law describes the empirical regularity that the number of transistors on integrated circuits doubles approximately every two years.
This advancement is important for other aspects of technological progress in computing – such as processing speed or the price of computers.

Data source: Wikipedia (wikipedia.org/wiki/Transistor_count)
OurWorldinData.org – Research and data to make progress against the world's largest problems.
Licensed under CC-BY by the authors Hannah Ritchie and Max Roser.
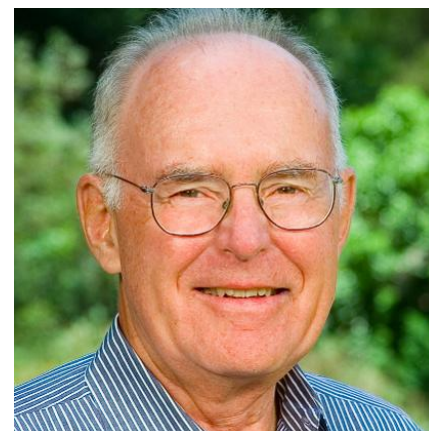
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Moore's law

Observation

- **Number of transistors** in a dense integrated circuit **doubles about every two years** (18 month)
- Exponential growth rate
- Named after **Gordon Moore**
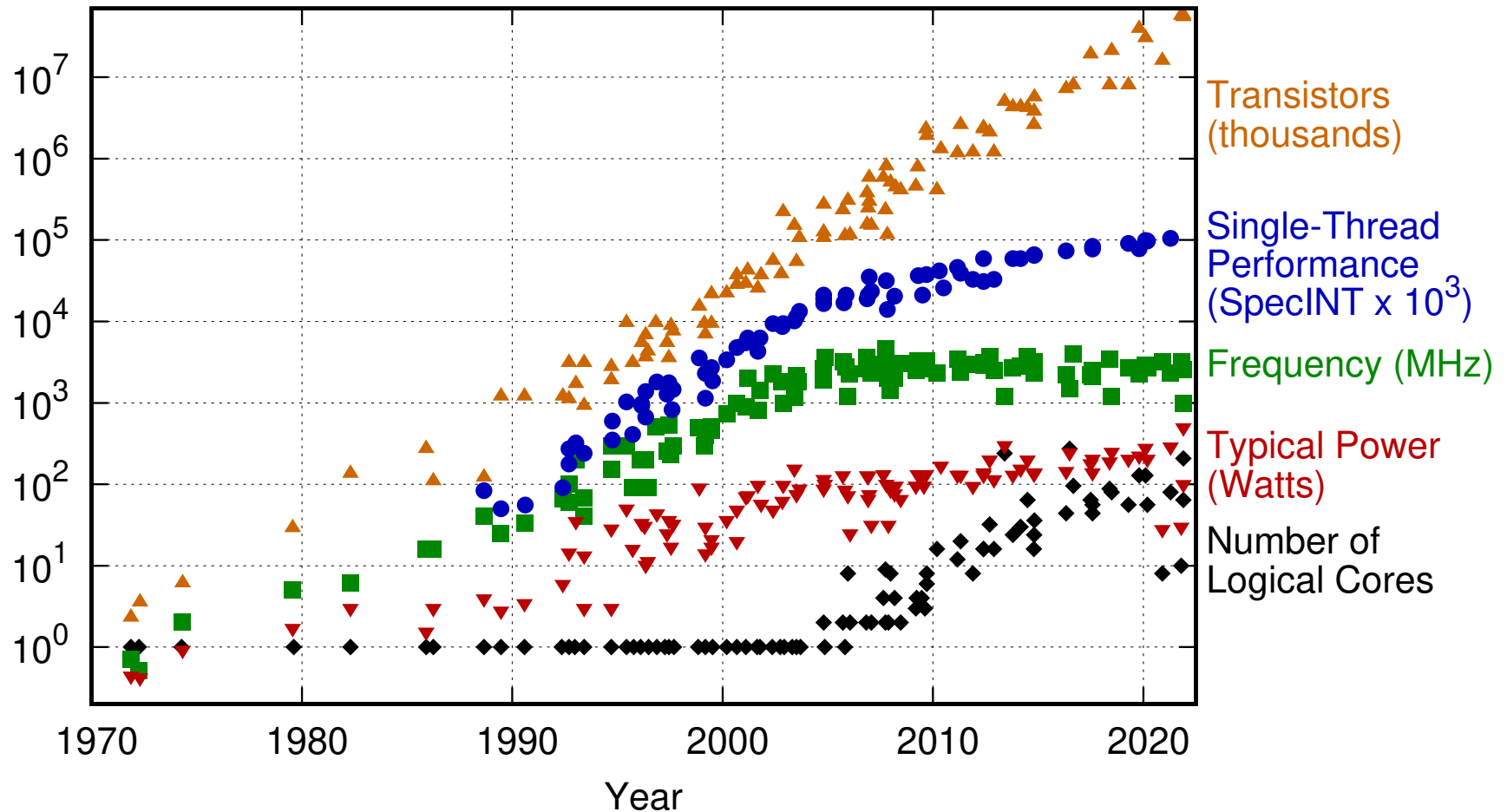- Co-founder of Intel

Gordon Moore

[source: forbes.com]

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Moore's law

Does it hold in the future?

50 Years of Microprocessor Trend Data

Original data up to the year 2010 collected and plotted by M. Horowitz, F. Labonte, O. Shacham, K. Olukotun, L. Hammond, and C. Batten
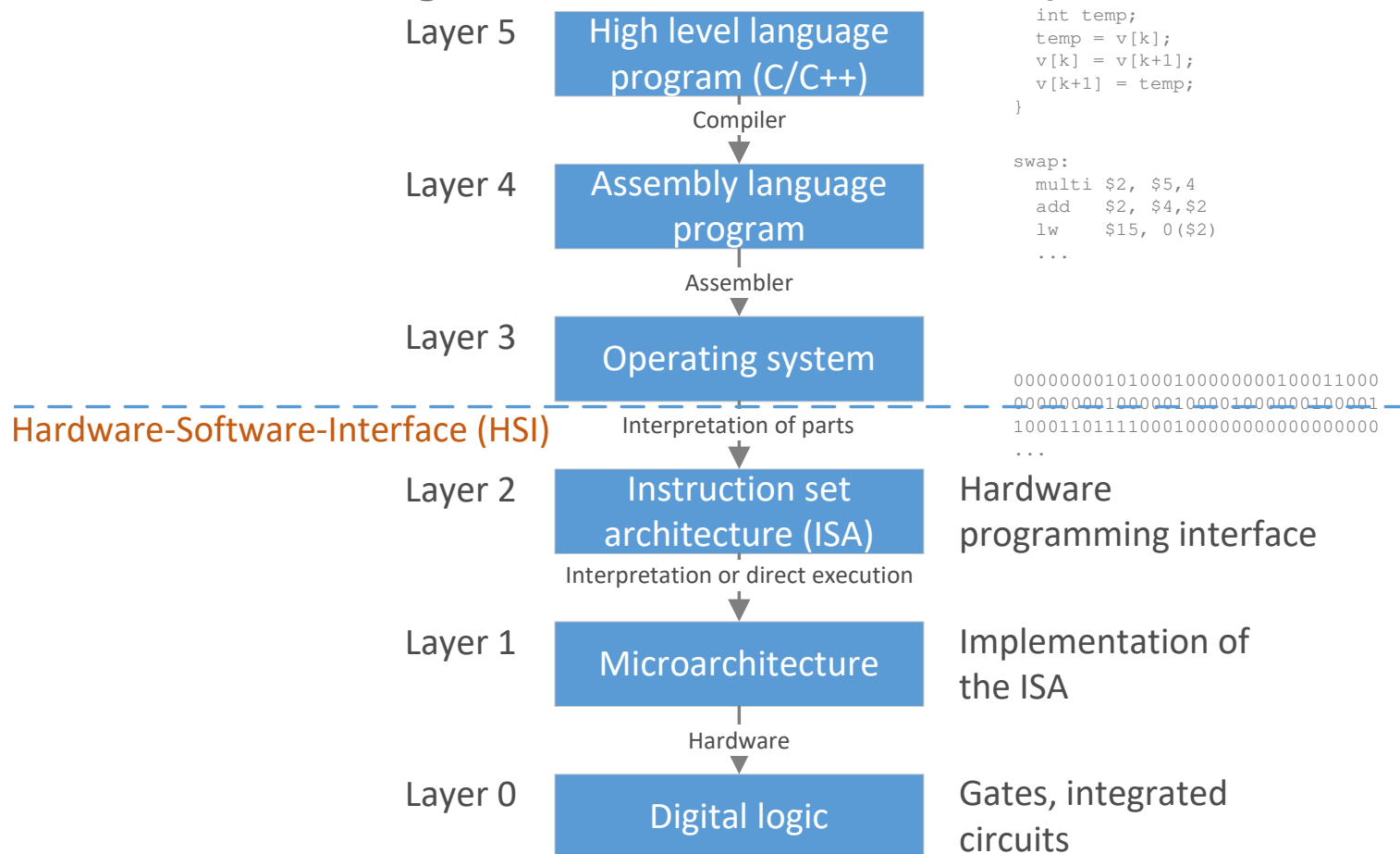New plot and data collected for 2010-2021 by K. Rupp

[source: https://github.com/karlrupp/microprocessor-trend-data]

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Structured layers

From **SOFTWARE** to **HARDWARE**

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Structured layers

Layer 5	High level language program (C/C++)	
	↓ Compiler	
Layer 4	Assembly language program	
	↓ Assembler	
Layer 3	Operating system	
Hardware-Software-Interface (HSI)	↓ Interpretation of parts	
Layer 2	Instruction set architecture (ISA)	Hardware programming interface
	↓ Interpretation or direct execution	
Layer 1	Microarchitecture	Implementation of the ISA
	↓ Hardware	
Layer 0	Digital logic	Gates, integrated circuits

```
swap(int v[], int k){
  int temp;
  temp = v[k];
  v[k] = v[k+1];
  v[k+1] = temp;
}


swap:
  multi $2, $5,4
  add   $2, $4,$2
  lw    $15, 0($2)
  ...
```

```
00000000101000100000000100011000
00000000100001000010000001000001
10001101111000100000000000000000
...
```

[cmp: [1, P. 24], [2, P. 15]]

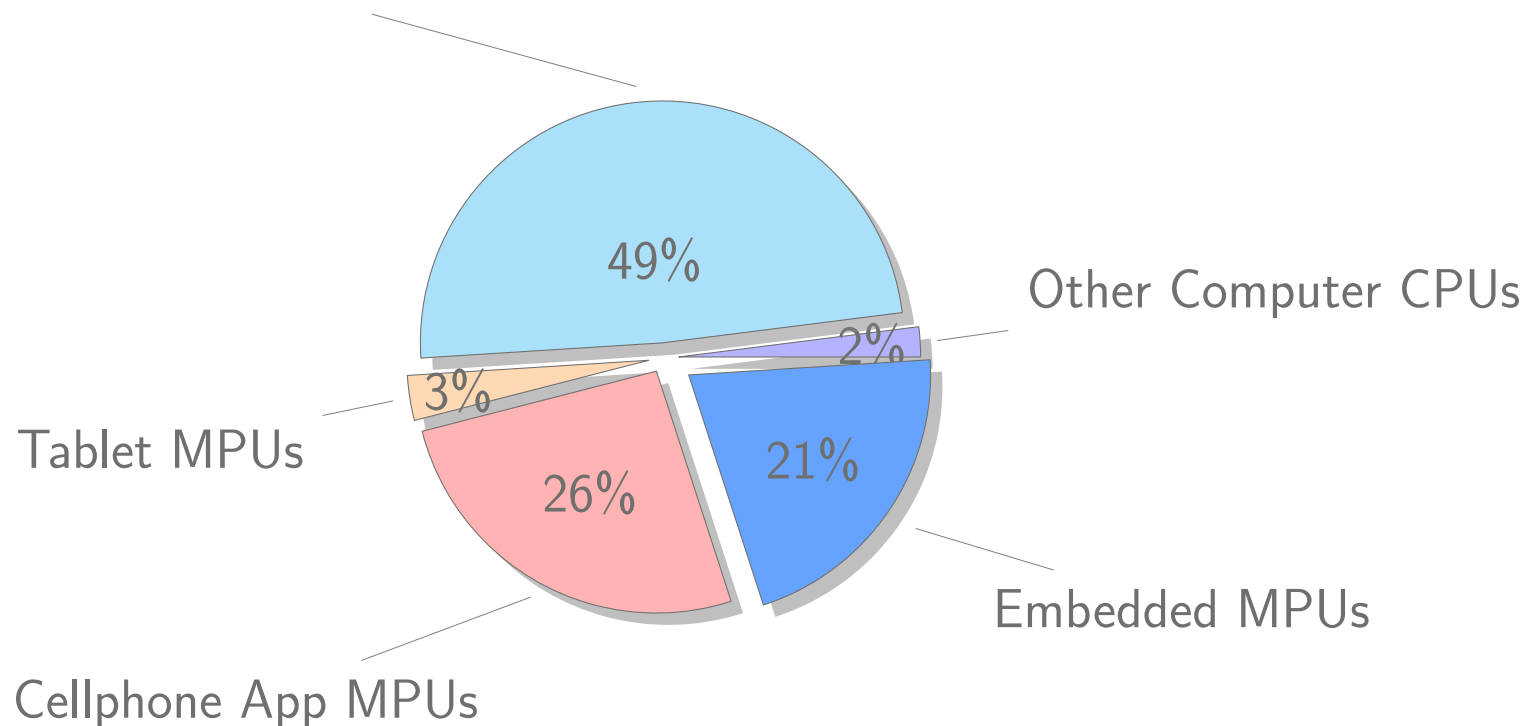**CAMPUS Rosenheim**
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Assembler

The study of computer architecture is always a study of the instruction set architecture (ISA).

- Knowledge from lecture „IT-Systeme" is assumed
- You don't have to write a lot of assembler code
- But: You have to **interpret** it and **understand** its basic operation mode

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Computer types

2020 MPU (microprocessor processor unit) sales by application:
x86 CPUs: Server;Notebook;Desktop

49%

Other Computer CPUs

2%

Tablet MPUs

3%

26%

21%

Embedded MPUs

Cellphone App MPUs

[source: design-reuse.com]

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Computer types

[source: https://www.canalys.com/newsroom/global-pc-market-Q4-2021]
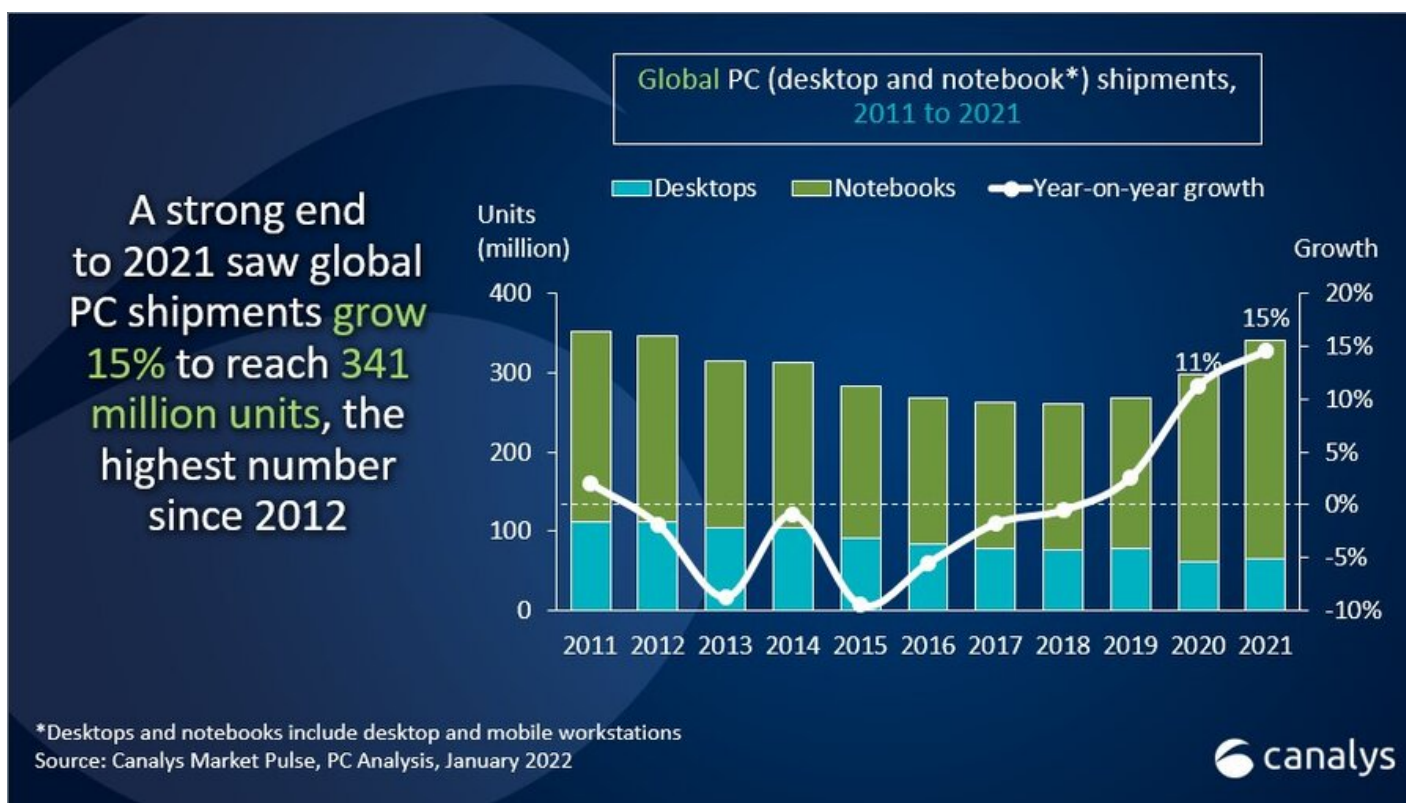
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Computer types

- **Microcontroller**: embedded systems, smartphones, vehicles, robots, …
- **PCs**: workstations, notebooks
- **Server**: grid of workstations, cloud
- **Mainframes**: (high I/O throughput, e.g. e-commerce or banking transactions)
- **Supercomputer**: high performance computing systems

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Analogue vs digital

General meaning

Analogue $\approx$ Corresponding, similar, analogous, applicable

An „analogue" is a similar or corresponding „thing".

Digital $\approx$ With numbers (lat. digitus $=$ „Finger (for counting)")
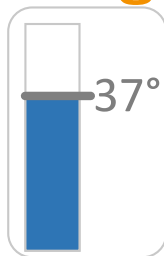
Extended meaning in IT (electronics)

Analogue $\approx$ continuous, steady, constantly

Digital $\approx$ stepwise, discrete

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Analogue vs digital

Analogue

37°

Digital

37°

analogue thermometer

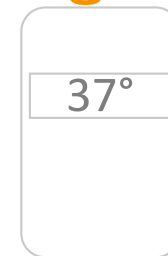**digital** (thermometer)

Temperature

- Indirectly via a physical analogue
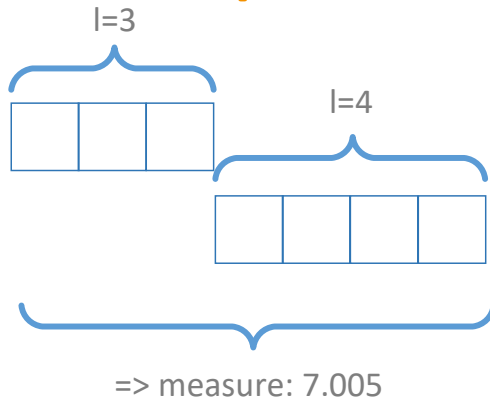- Height of liquid
- Digitalisation through people (read)

Temperature

- Numerical display
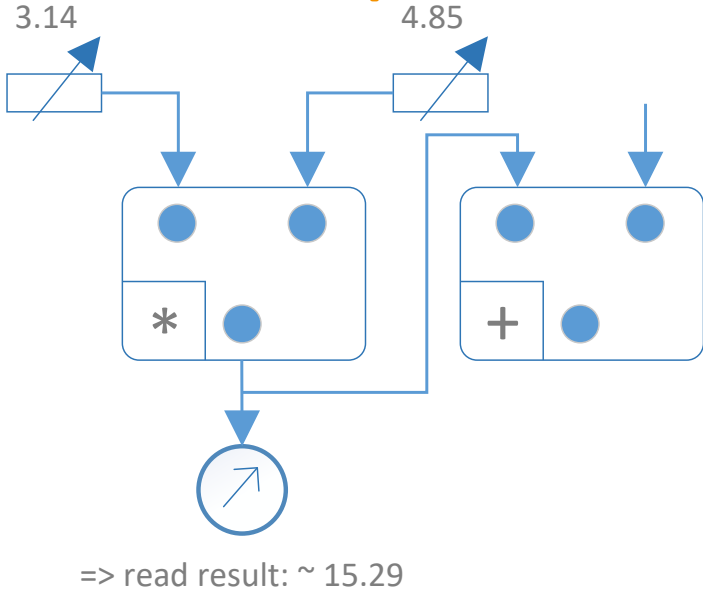- Internal: measure of physical analogue (resistor)
- Automatic digitalisation

Analogue computer

Example 1

l=3

l=4

=> measure: 7.005

Example 2

3.14   4.85

*   +

=> read result: ~ 15.29

analogue add   **analogue** multiply

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Analogue computer: historical

Calculation by creation of a physical analogue.

Properties

- Very fast operations
- Not very accurate

Applications

- Solution of differential equations
- Real-time simulations

Keywords: Operational amplifier (Operationsverstärker)

Nowadays, analogue computers are not used very often.
In the following, **only digital computers** will be discussed.

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Summary and outlook

Summary

- Computer architecture vs organisation

- Moore's law

- Structured layers

- Computer types

- Analogue vs digital

Outlook

- Data representation

- Unicode and UTF

- Data types