

Please submit your project to elec402project@gmail.com by Sep. 29, 2021, 11:59pm

System Verilog FSM Project

Introduction:

This project will be relatively simple, but we would like you to get a clear idea of what you would like to build for your FSM. If you plan ahead now and make it **scalable**, it would keep things easier as the semester progresses.

In this project, you would build a Finite State Machine (FSM) using System Verilog.

Project Description/Requirements:

- Code your FSM in Behavioral System Verilog. Be creative in your FSM but **at least 10 states** are required.
- Choose your FSM for a **practical application**. Examples include Elevator operation, Vending Machines, Bank ATMs, Traffic controller, Convolutional Encoders, Surveillance drones, etc. Be creative; pick something interesting. By not specifying which FSM to use in this project, we are hoping to see many different implementations in the class. And we know that you are smart enough not to copy your code from web (Your code will be submitted to a plagiarism checker)!
- **One test bench** and **at least one module for your FSM**
- **Clear comments** to your code

Report Layout

1. Name, student number and project title on the first page of your project report.
2. A paragraph on the **general description** of your FSM [10].
3. A test bench and at least one module for your FSM. You can have more than two modules if your FSM requires it. Define the input and output of each module and a purpose/description of each state for each module. In your test bench, please **put comments on how this will test your FSM**. [20]
4. **A block diagram** of FSM module(s). Please label all inputs and outputs. [10]
5. **A block diagram of how the module(s) and test bench are connected**. [10]
6. A state diagram of your FSM with data flow. [10]
7. A copy of your code (in Font Size 8) [10] [as a separate file]
8. Simulation waveform results [30].

FAQ:

1. Start early on this project to make sure that you have access to the software. If you don't, fixing it may take up to 48 hours. So don't wait till the last date to work on your project. If you get the fatal license error "Unable to checkout a

viewer license necessary for use of the ModelSim GUI. Vsim is closing”, you are having an access issue.

2. You will find the following resources useful **(available on Canvas)**

Tutorials 0

Tutorial 1, and associated files

Lecture_2_3 - Intro to SystemV