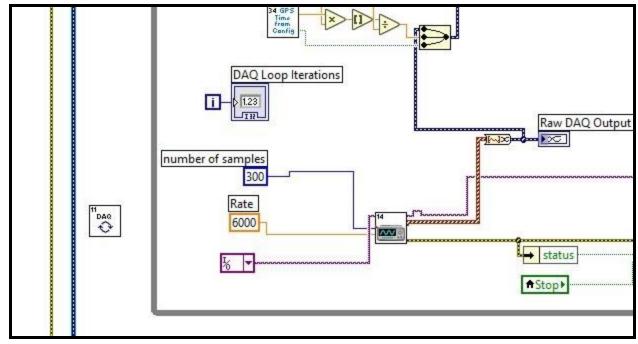The VI 00 Main Simulated Signal has the same front panel as 00 Main (single phase). The difference is in the block diagram, as shown below. The purpose of the VI is to make it easier to obtain simulated data, and so that you don't have to modify the actual 00 Main VI.

Instead of using the sub VI 10 DAQ Config (PFI0) and the Analog 1D Wfm DAQmx functions, 00 Main Simulated Signal uses the sub VI 14 Function Generator Signal, which takes in the number of samples, the rate of sampling and the task name. This sub VI then outputs the desired task out, the data, and any errors. Refer to the VI document for more information on how it works.

The second simulated VI, created for the same purposes as the first, is 00 Main Simulated Triple Signal. The VI operates in the same way as the single signal VI, but sends its data and information to the SUB VI 15 Triple Phase Signal Generation.