



## 1. Description

### 1.1. Project

Project Name	7-SEGMENT DISPLAY COMMON ANODE
Board Name	custom
Generated with:	STM32CubeMX 6.2.0
Date	04/02/2021

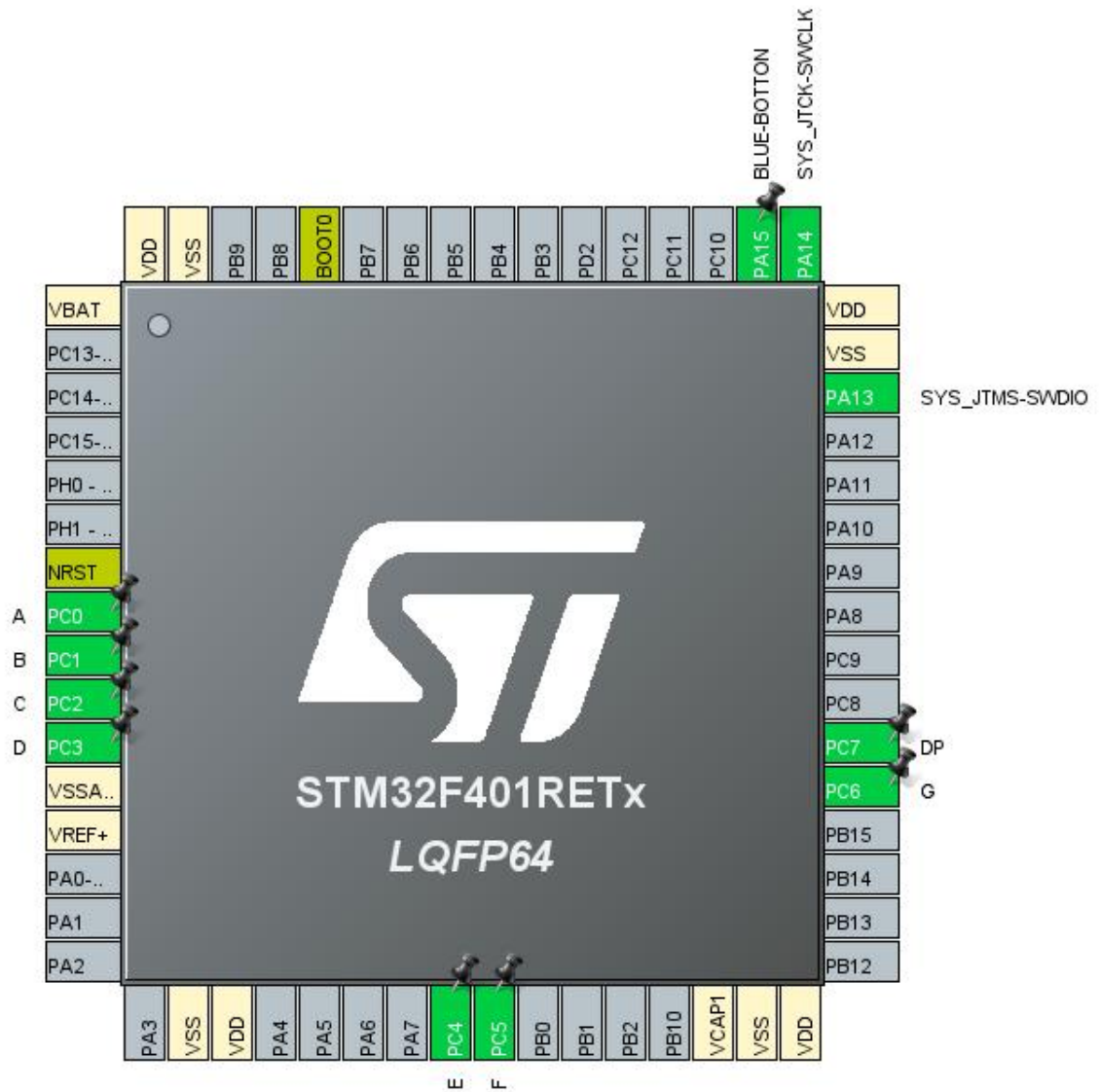
### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F401
MCU name	STM32F401RETx
MCU Package	LQFP64
MCU Pin number	64

### 1.3. Core(s) information

Core(s)	Arm Cortex-M4
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## 2. Pinout Configuration



### 3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
7	NRST	Reset		
8	PC0 *	I/O	GPIO_Output	A
9	PC1 *	I/O	GPIO_Output	B
10	PC2 *	I/O	GPIO_Output	C
11	PC3 *	I/O	GPIO_Output	D
12	VSSA/VREF-	Power		
13	VREF+	Power		
18	VSS	Power		
19	VDD	Power		
24	PC4 *	I/O	GPIO_Output	E
25	PC5 *	I/O	GPIO_Output	F
30	VCAP1	Power		
31	VSS	Power		
32	VDD	Power		
37	PC6 *	I/O	GPIO_Output	G
38	PC7 *	I/O	GPIO_Output	DP
46	PA13	I/O	SYS_JTMS-SWDIO	
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	
50	PA15 *	I/O	GPIO_Input	BLUE-BOTTON
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		

\* The pin is affected with an I/O function



## 5. Software Project

### 5.1. Project Settings

Name	Value
Project Name	7-SEGMENT DISPLAY COMMON ANODE
Project Folder	C:\Users\LUIS ARRES\STM32CubeIDE\workspace_1.6.0\PROJECTS MOOC
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.26.0
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

### 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	MX_GPIO_Init	GPIO
2	SystemClock_Config	RCC

## 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32F4
Line	STM32F401
MCU	STM32F401RETx
Datasheet	DS10086_Rev3

### 6.2. Parameter Selection

Temperature	25
Vdd	3.3

### 6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

#### 6.4. Sequence

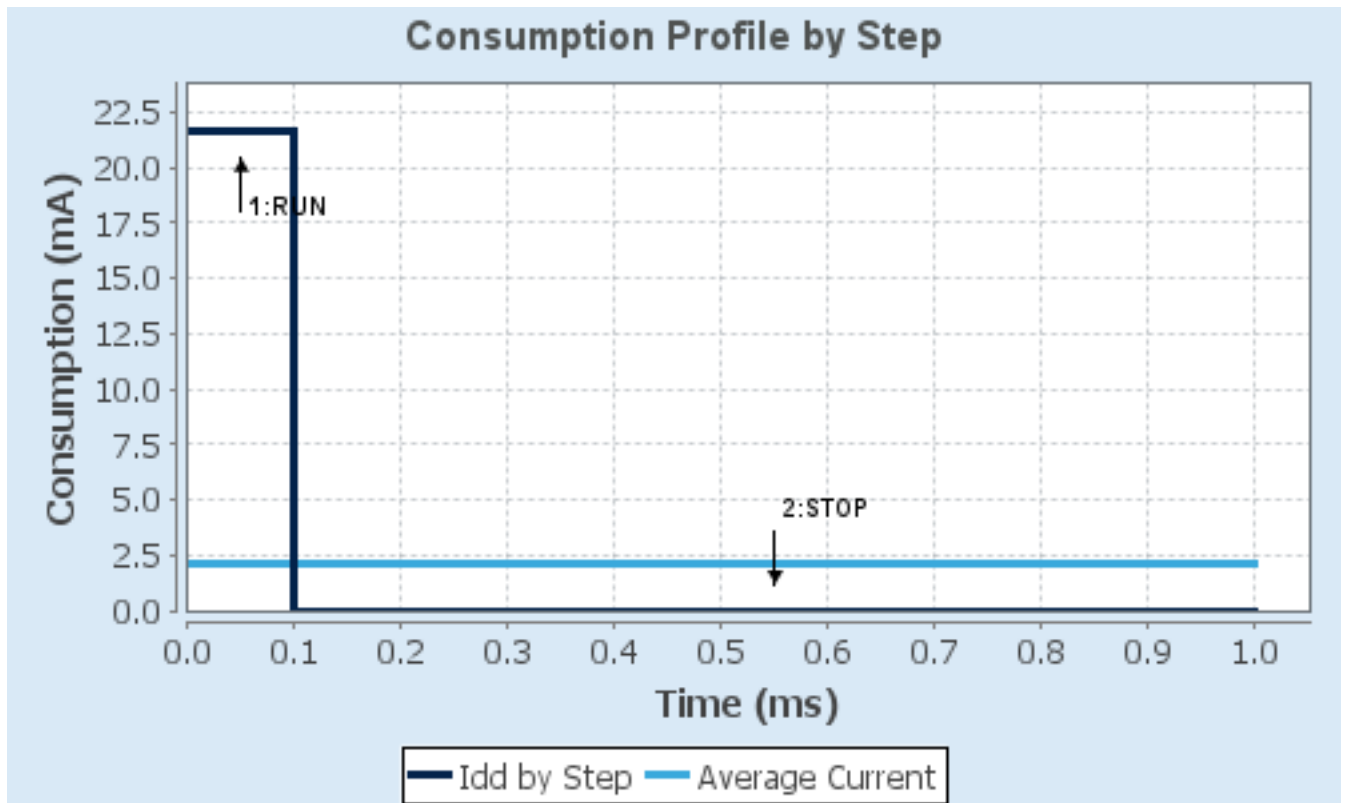
<b>Step</b>	Step1	Step2
<b>Mode</b>	RUN	STOP
<b>Vdd</b>	3.3	3.3
<b>Voltage Source</b>	Battery	Battery
<b>Range</b>	Scale2-Medium	No Scale
<b>Fetch Type</b>	FLASH/ART/PREFETCH	n/a
<b>CPU Frequency</b>	84 MHz	0 Hz
<b>Clock Configuration</b>	HSE PLL	Regulator_LPLV Flash-PwrDwn
<b>Clock Source Frequency</b>	4 MHz	0 Hz
<b>Peripherals</b>		
<b>Additional Cons.</b>	0 mA	0 mA
<b>Average Current</b>	21.6 mA	10 $\mu$ A
<b>Duration</b>	0.1 ms	0.9 ms
<b>DMIPS</b>	105.0	0.0
<b>Ta Max</b>	101.44	105
<b>Category</b>	In DS Table	In DS Table

#### 6.5. Results

Sequence Time	1 ms	Average Current	2.17 mA
Battery Life	2 months, 4 days, 8 hours	Average DMIPS	105.0 DMIPS

#### 6.6. Chart





## 7. *Peripherals and Middlewares Configuration*

### 7.1. RCC

#### 7.1.1. Parameter Settings:

##### **System Parameters:**

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	0 WS (1 CPU cycle)

##### **RCC Parameters:**

HSI Calibration Value	16
TIM Prescaler Selection	Disabled
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

##### **Power Parameters:**

Power Regulator Voltage Scale	Power Regulator Voltage Scale 2
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### 7.2. SYS

**Debug: Serial Wire**

**Timebase Source: SysTick**

**\* User modified value**

## 8. System Configuration

### 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
GPIO	PC0	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Low	A
	PC1	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Low	B
	PC2	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Low	C
	PC3	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Low	D
	PC4	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Low	E
	PC5	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Low	F
	PC6	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Low	G
	PC7	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Low	DP
	PA15	GPIO_Input	Input mode	<b>Pull-up *</b>	n/a	BLUE-BOTTOM

### 8.2. DMA configuration

nothing configured in DMA service

### 8.3. NVIC configuration

#### 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
FPU global interrupt	unused		

#### 8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

\* User modified value

## 9. System Views

### 9.1. Category view

#### 9.1.1. Current

Middleware
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System Core	Analog	Timers	Connectivity	Multimedia	Computing
DMA					
GPIO					
IIVIC					
RCC					
SYS					

## 10. Docs & Resources

Type	Link
Datasheet	<a href="http://www.st.com/resource/en/datasheet/DM00102166.pdf">http://www.st.com/resource/en/datasheet/DM00102166.pdf</a>
Reference manual	<a href="http://www.st.com/resource/en/reference_manual/DM00096844.pdf">http://www.st.com/resource/en/reference_manual/DM00096844.pdf</a>
Programming manual	<a href="http://www.st.com/resource/en/programming_manual/DM00046982.pdf">http://www.st.com/resource/en/programming_manual/DM00046982.pdf</a>
Errata sheet	<a href="http://www.st.com/resource/en/errata_sheet/DM00158624.pdf">http://www.st.com/resource/en/errata_sheet/DM00158624.pdf</a>
Application note	<a href="http://www.st.com/resource/en/application_note/CD00167594.pdf">http://www.st.com/resource/en/application_note/CD00167594.pdf</a>
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Application note	<a href="http://www.st.com/resource/en/application_note/CD00264379.pdf">http://www.st.com/resource/en/application_note/CD00264379.pdf</a>
Application note	<a href="http://www.st.com/resource/en/application_note/DM00024853.pdf">http://www.st.com/resource/en/application_note/DM00024853.pdf</a>
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Application note	<a href="http://www.st.com/resource/en/application_note/DM00042534.pdf">http://www.st.com/resource/en/application_note/DM00042534.pdf</a>
Application note	<a href="http://www.st.com/resource/en/application_note/DM00046011.pdf">http://www.st.com/resource/en/application_note/DM00046011.pdf</a>
Application note	<a href="http://www.st.com/resource/en/application_note/DM00072315.pdf">http://www.st.com/resource/en/application_note/DM00072315.pdf</a>
Application note	<a href="http://www.st.com/resource/en/application_note/DM00073742.pdf">http://www.st.com/resource/en/application_note/DM00073742.pdf</a>
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