#### T2-9.7 MAXIMUM POWER TRANSFER THEOREM

#### VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

B.E: Electronics & Communication Engineering / B.E: Electronics & Telecommunication Engineering NEP, Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

(Effective from the academic year 2021 – 22)

#### **IV Semester**

Circuits & Controls							
Course Code	21EC43	CIE Marks	50				
Teaching Hours/Week (L: T: P: S)	(3:0:2:0)	SEE Marks	50				
Total Hours of Pedagogy	40 hours Theory + 13 Lab slots	Total Marks	100				
Credits	04	Exam Hours	03				

module-1	M	o	d	ul	e-	1
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### Basic concepts and network theorems

Types of Sources, Loop analysis, Nodal analysis with independent DC and AC Excitations.

(Textbook 1: 2.3, 4.1, 4.2, 4.3, 4.4, 10.6)

 $Super position \ theorem, \ The venin's \ theorem, \ Norton's \ Theorem, \ Maximum \ Power \ transfer \ Theorem.$ 

(Textbook 2: 9.2, 9.4, 9.5, 9.7)

Teaching-Learning Process Chalk and Talk, YouTube videos, Demonstrate the concepts using circuits

ing Process | RBT Level: L1, L2, L3

### Module-2

**Two port networks**: Short- circuit Admittance parameters, Open- circuit Impedance parameters, Transmission parameters, Hybrid parameters (Textbook 3: 11.1, 11.2, 11.3, 11.4, 11.5)

**Laplace transform and its Applications**: Step Ramp, Impulse, Solution of networks using Laplace transform, Initial value and final value theorem (Textbook 3: 7.1, 7.2, 7.4, 7.7, 8.4)

TeachingLearning Process Chalk and Talk
RBT Level: L1, L2, L3

Module-3

### Basic Concepts and representation:

Types of control systems, effect of feedback systems, differential equation of physical systems (only electrical systems), Introduction to block diagrams, transfer functions, Signal Flow Graphs (Textbook 4: Chapter 1.1, 2.2, 2.4, 2.5, 2.6)

Teaching-Learning Chalk and Talk, YouTube videos
Process RBT Level: L1, L2, L3

Module-4				
<b>Time Response analysis</b> : Time response of first order systems. Time response of second order systems, time response specifications of second order systems (Textbook 4: Chapter 5.3, 5.4)				
<b>Stability Analysis:</b> Concepts of stability necessary condition for stability, Routh stability criterion, relative stability Analysis (Textbook 4: Chapter 5.3, 5.4, 6.1, 6.2, 6.4, 6.5)				
Teaching-Learning	Chalk and Talk, Any software tool to show time response			
Process	RBT Level: L1, L2, L3			
Module-5				
Root locus: Introduction the root locus concepts, construction of root loci (Textbook 4: 7.1, 7.2, 7.3)				
<b>Frequency Domain analysis and stability</b> : Correlation between time and frequency response and Bode plots (Textbook 4: 8.1, 8.2, 8.4)				
<b>State Variable Analysis:</b> Introduction to state variable analysis: Concepts of state, state variable and state models. State model for Linear continuous –Time systems, solution of state equations.				
(Textbook 4: 12.2, 12.3, 12.6)				
Teaching-Learning	Chalk and Talk, Any software tool to plot Root locus, Bode plot			
Process	RBT Level: L1, L2, L3			

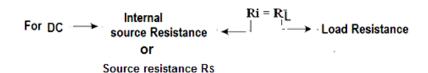
### **Suggested Learning Resources:**

### Text Books

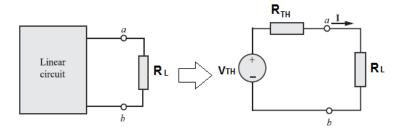
- Engineering circuit analysis, William H Hayt, Jr, Jack E Kemmerly, Steven M Durbin, Mc Graw Hill Education, Indian Edition 8e.
- 2. Networks and Systems, D Roy Choudhury, New age international Publishers, second edition.
- 3. Network Analysis, M E Van Valkenburg, Pearson, 3e.
- 4. Control Systems Engineering, I J Nagrath, M. Gopal, New age international Publishers, Fifth edition.

### **T2-9.7 MAXIMUM POWER TRANSFER THEOREM**

**Maximum power transfer theorem** states that in a linear bidirectional two terminal network Maximum power will be transferred from source to the load when the internal resistance of the source is equal to the load resistance



Source inpedance Zs



According to Maximum Power Transfer Theorem, for maximum power transfer from the network to the load resistance, RL must be equal to the source resistance i.e. Network's Thevenin equivalent resistance RTH. I.e. RL = RTH

The load current **I** in the circuit shown above is given by,

$$I = \frac{V_{TH}}{R_{TH} + R_L}$$

The power delivered by the circuit to the load:

$$P = I^2 R = \frac{V_{TH}^2}{(R_{TH} + R_L)^2} R_L$$

$$P = V_{TH}^2 \left\{ \frac{R_L}{(R_{TH} + R_L)^2} \right\}$$

The condition for maximum power transfer can be obtained by differentiating the above expression for power delivered with respect to the load resistance and equating it to zero

$$\frac{dP}{dR_L} = 0$$

$$= V_{TH}^2 \left[ \frac{(R_{TH} + R_L)^2 (1) - R_L \times 2 (R_{TH} + R_L) (1)}{(R_{TH} + R_L)^4} \right] = 0$$

$$\frac{dU/V}{dt} = \frac{V \cdot \frac{dU}{dt} - U \cdot \frac{dV}{dt}}{V^2}$$

$$(R_{TH} + R_L)^2 - R_L \times 2 (R_{TH} + R_L) = 0$$

$$(R_{TH} + R_L) [(R_{TH} + R_L) - 2R_L] = 0$$

$$(R_{TH} + R_L) (R_{TH} - R_L) = 0$$

Source Resistance = Load Resistance

 $(R_{TH}-R_L)=0$ 

This is the condition for max. power transfer

The value of Maximum Power Transfer

$$P = I^2 R = \frac{V_{TH}^2}{(R_{TH} + R_L)^2} R_L$$

For maximum power transfer  $R_{TH} = R_L$ 

$$P_{MAX} = \frac{V_{TH}^2}{(R_L + R_L)^2} \times R_L = \frac{V_{TH}^2}{4R_L}$$

Under the condition of maximum power transfer, the power delivered to the load is given by

$$P_{MAX}=rac{V_{TH}^{2}}{4R_{L}}$$
 Watts

# Efficiency at maximum power transfer

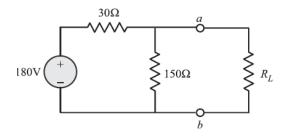
$$P_{OUT} = Output power at max. Power transfer = P_{MAX} = \frac{V_{TH}^2}{4R_L}$$
 Watts

Input power = 
$$V \mid = V_{TH} \mid X \mid \frac{V_{TH}}{R_{TH} + R_L}$$
 Watts

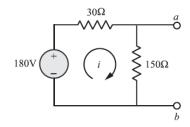
For maximum power transfer  $R_{TH} = R_L$ 

Therefore efficiency is 50% at maximum power transfer

1)Find the load RL that will result in maximum power delivered to the load for the circuit of Fig. Also determine the maximum power Pmax.



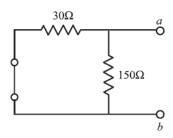
First step Disconnect the load resistor RL and find VTH

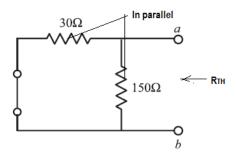


$$i = \frac{180}{150 + 30} = 1A$$

$$V_{oc} = V_{\mathrm{TH}} = 150 \times i = 150 \; \mathrm{V}$$

# To find RTH deactivate the independent source

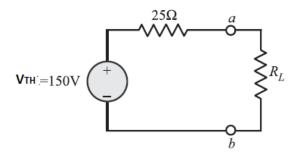




$$\begin{split} R_{_{\mathrm{TH}}} &= R_{ab} = 30 \; \Omega || 150 \; \Omega \\ &= \frac{30 \times 150}{30 + 150} = 25 \; \Omega \end{split}$$

Maximum power transfer is obtained when

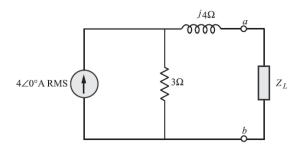
$$R_L = R_{\rm th} = 25 \ \Omega.$$



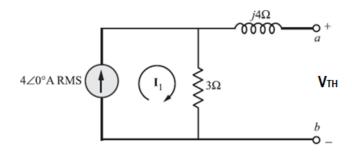
Then the maximum power is

$$P_{\text{max}} = \frac{V_{\text{TH}}^2}{4R_L} = \frac{(150)^2}{4 \times 25}$$
  
= 2.25 Watts

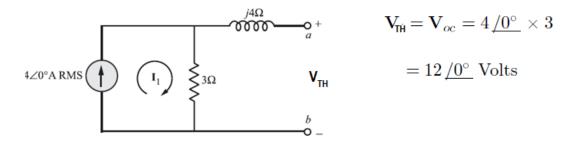
2) Find the load impedance that transfers the maximum average power to the load and determine the maximum average power transferred to the load ZL shown in Fig.



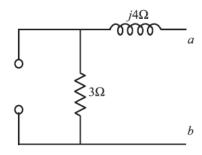
# First step disconnect the load resistance



# Find the VTH



# To find ZTH, deactivate all the



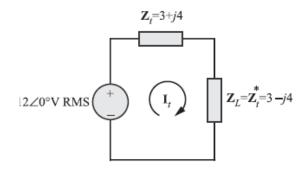
$$\mathbf{Z}_{\mathsf{TH}} = 3 + j4 \,\Omega$$

For maximum power transfer ZTH = ZL

Therefore **ZL** = 
$$^{=3} + j4 \Omega$$

For maximum average power transfer to the load, ZTH =  $\mathbf{Z}_L^* = 3 - j4$ .

For maximum average power transfer, the load impedance  $Z_L$  must be equal to the complex conjugate of the Thevenin impedance  $Z_{Th}$ .

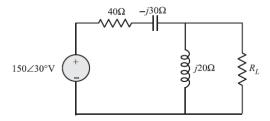


... 
$$I_{TH} = \frac{12 / 0^{\circ}}{3 + i4 + 3 - i4} = 2 / 0^{\circ}$$
 A

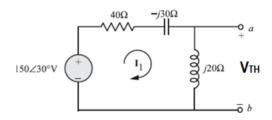
Hence, maximum average power delivered to the load is

$$P = |I_t|^2 R_L = 4(3) = 12 \text{ W}$$

3) Refer the circuit given in Fig. Find the value of RL that will absorb the maximum average power.



## Disconnecting the load resistor RL

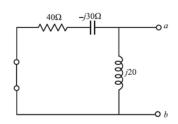


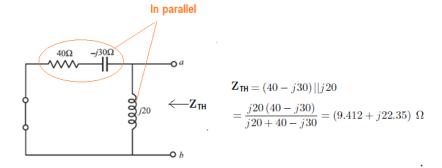
$$\mathbf{V}_{\text{TH}} = \mathbf{V}_{oc} = \mathbf{I}_1 \ (j20)$$

$$= \frac{150 \ /30^{\circ} \times j20}{(40 - j30 + j20)}$$

$$= 72.76 \ /134^{\circ} \ \text{Volts.}$$

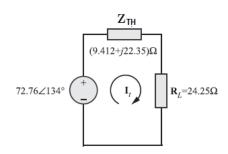
## To find ZTH deactivate the independent source





The Value of RL that will absorb the maximum average power is

$$R_L = |\mathbf{Z}_t| = \sqrt{(9.412)^2 + (22.35)^2}$$
  
= 24.25 \Omega



$$\mathbf{I_{^{1}TH}} = \frac{72.76\, \underline{/134^{\circ}}}{(9.412+j22.35+24.25)} \, = 1.8\, \underline{/100.2^{\circ}} \; \mathbf{A}$$

Maximum average power absorbed by RL is

$$P_{
m max} = rac{1}{2} |I_{
m TH}|^2 R_L$$
  $P_{
m max} = rac{1}{2} (1.8)^2 imes 24.25$   $= 39.29 \ 
m W$ 

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