

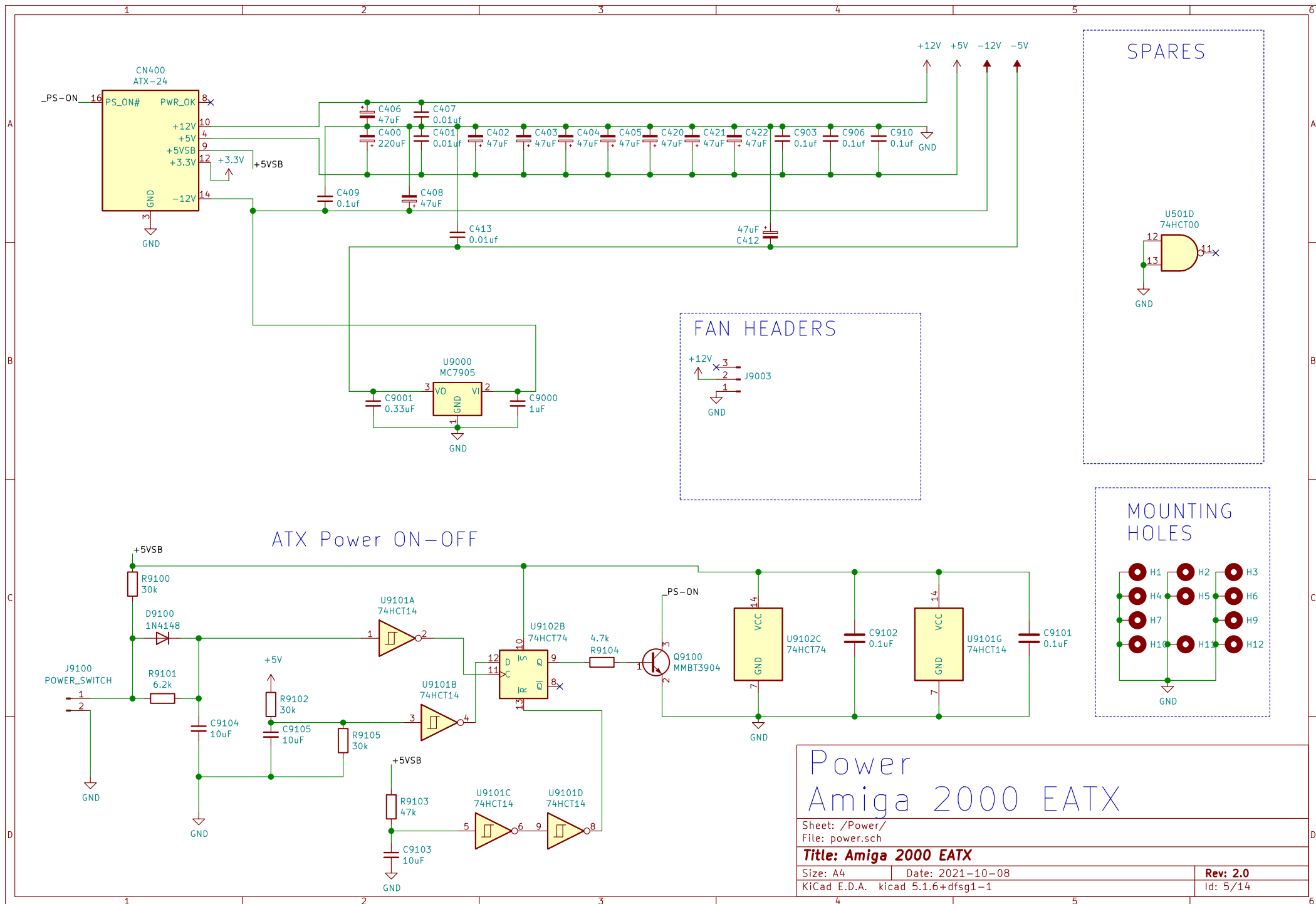
Coprocessor Amiga 2000 EATX

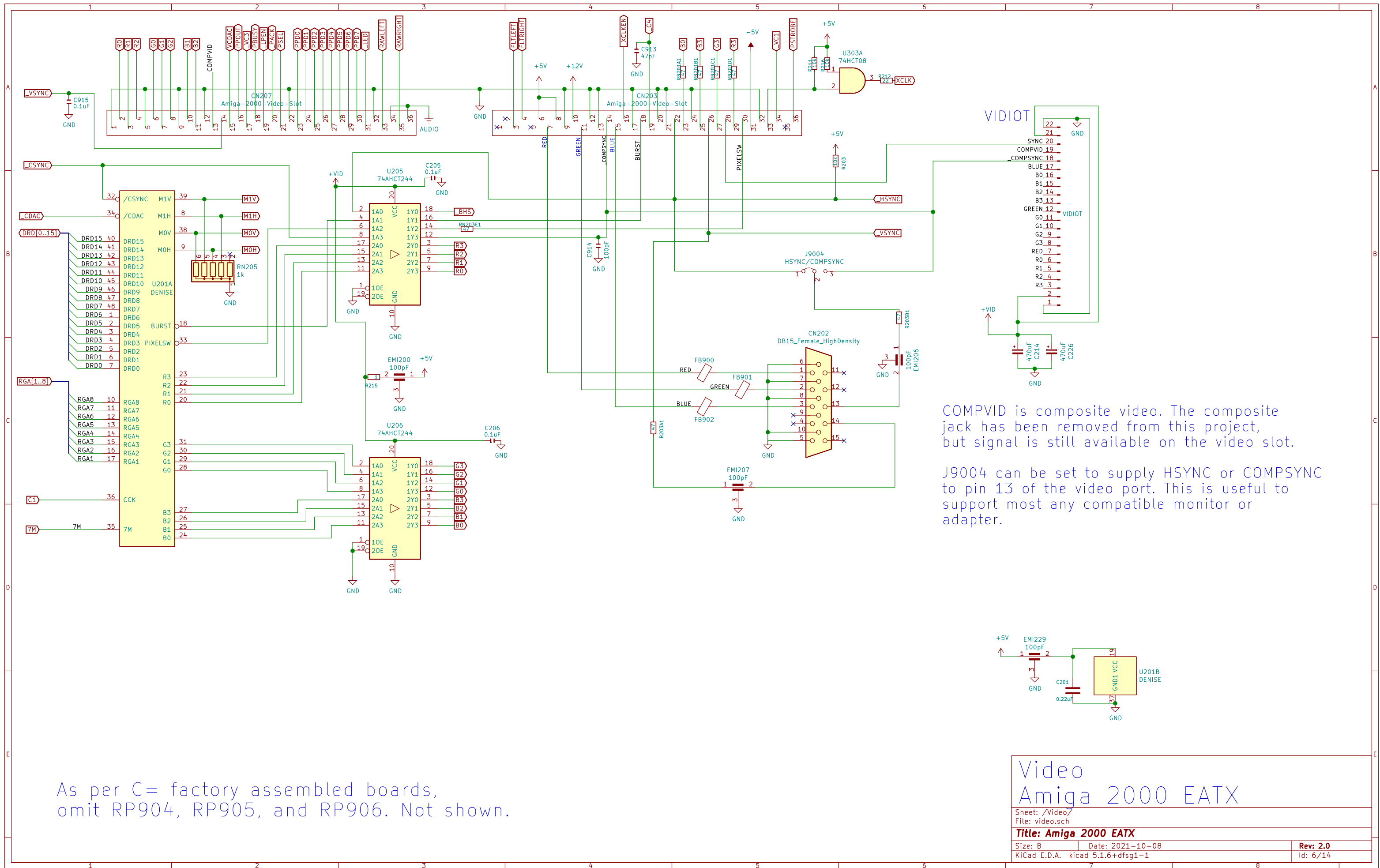
Sheet: /Coprocessor/
File: coprocessor.sch

Title:

Size: A4
Date: 2021-10-08
KiCad E.D.A. kicad 5.1.6+dfsg1-1

Rev:
Id: 4/14





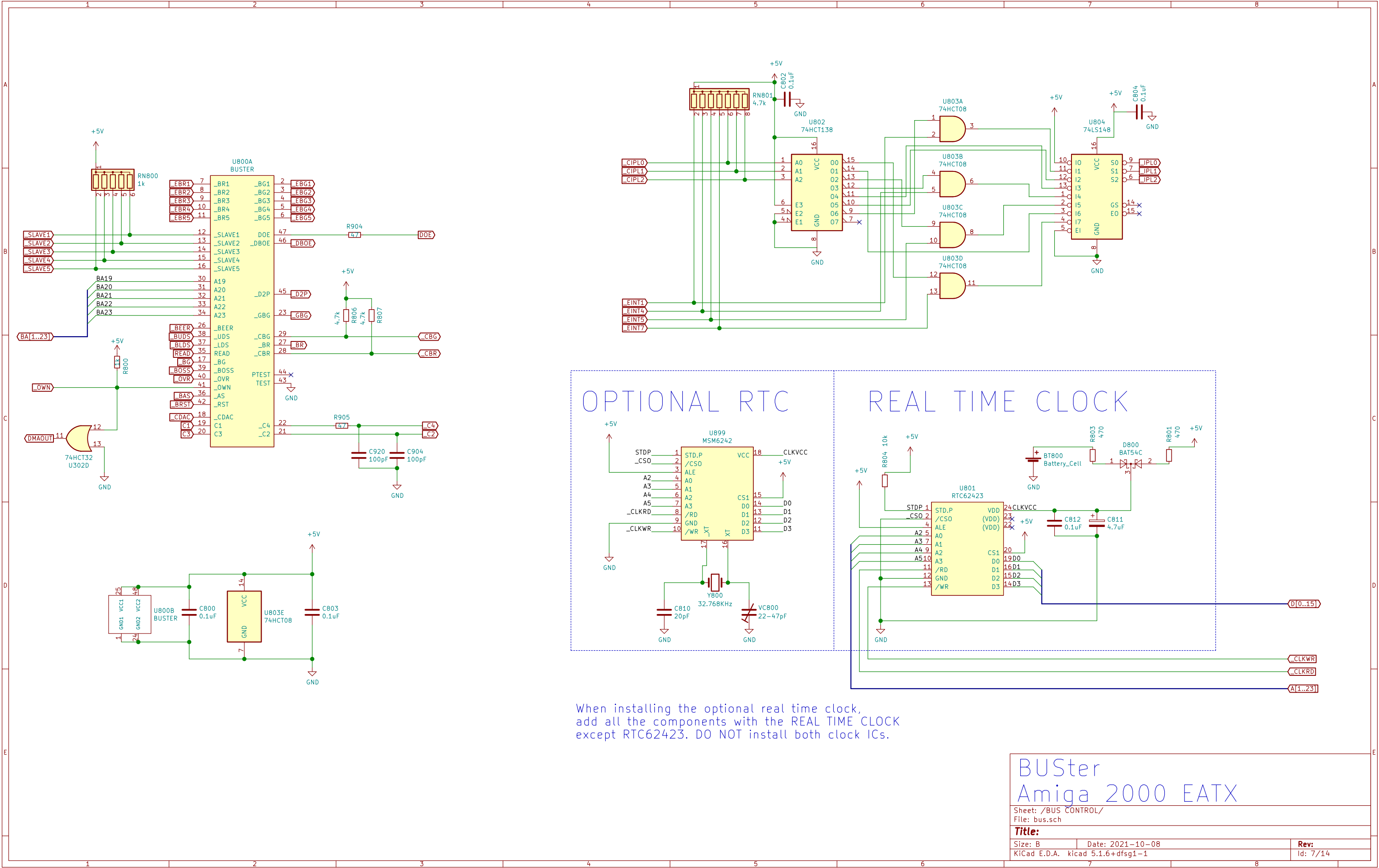
As per C= factory assembled boards,
omit RP904, RP905, and RP906. Not shown.

COMPVID is composite video. The composite
jack has been removed from this project,
but signal is still available on the video slot.

J9004 can be set to supply HSYNC or COMPSYNC
to pin 13 of the video port. This is useful to
support most any compatible monitor or
adapter.

Video Amiga 2000 EATX

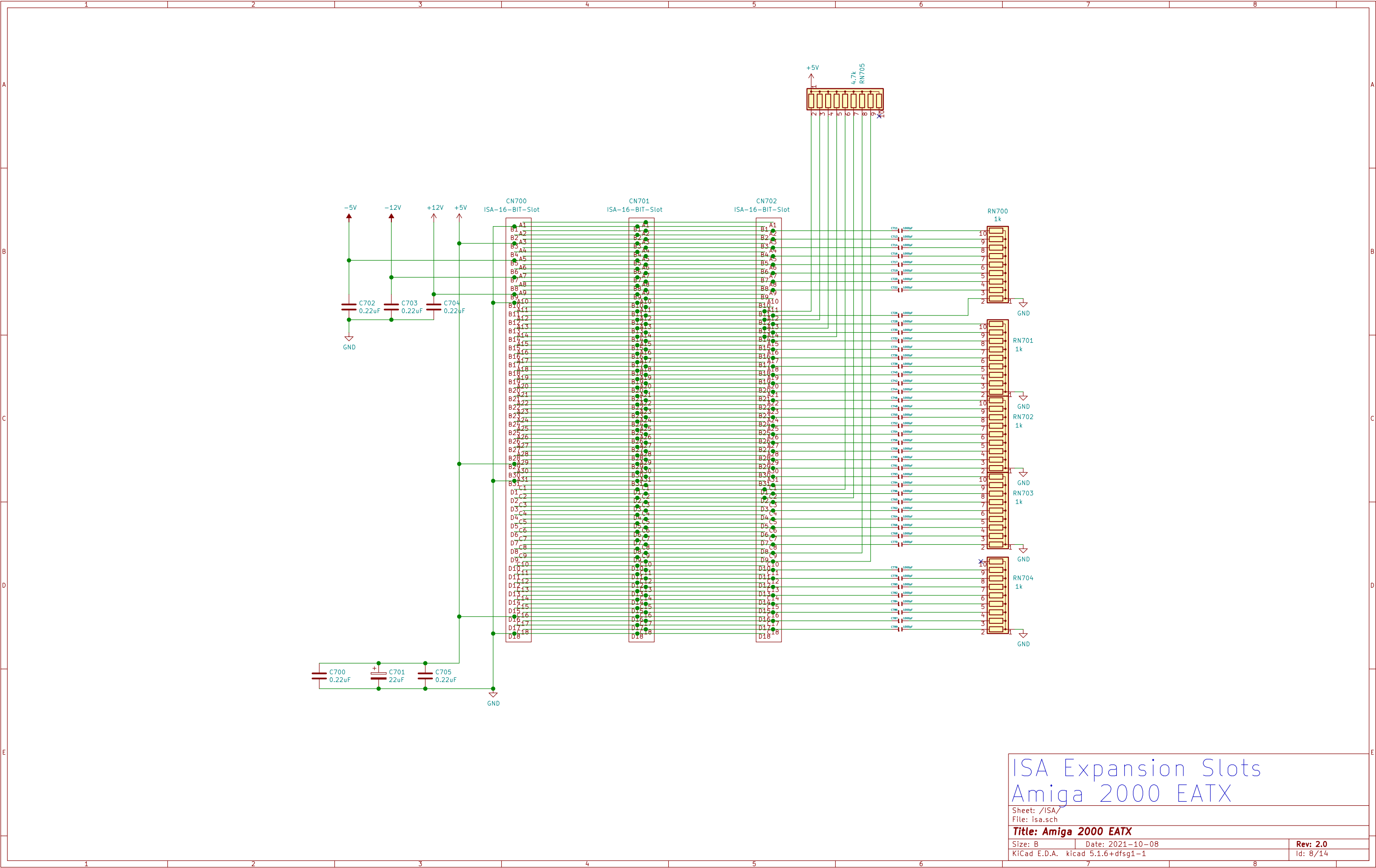
| | | |
|-----------------------------------|------------------|----------|
| Sheet: /Video/ File: video.sch | | |
| Title: Amiga 2000 EATX | | |
| Size: B | Date: 2021-10-08 | Rev: 2.0 |
| KiCad E.D.A. kicad 5.1.6+dfsg1-1 | | Id: 6/14 |

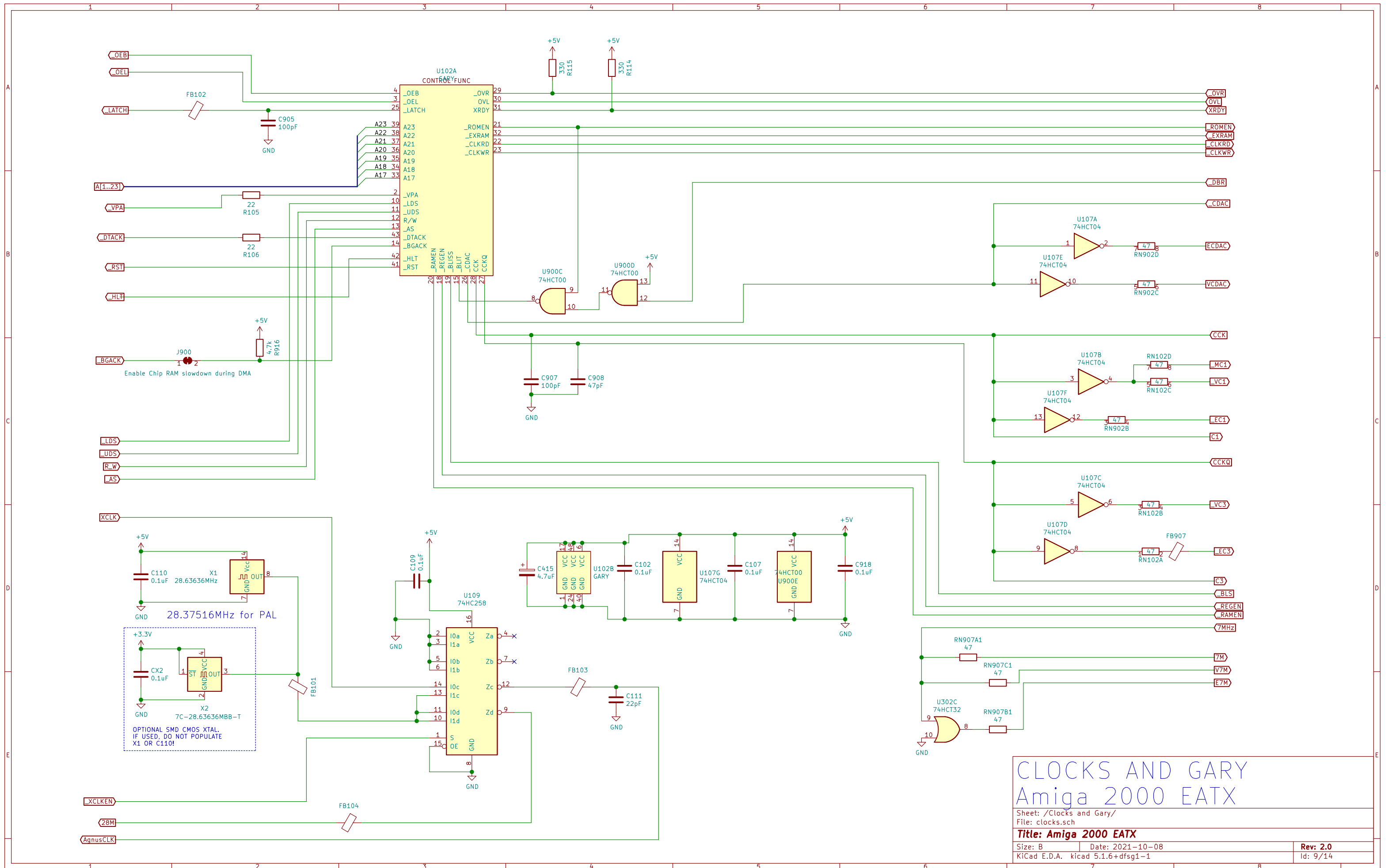


When installing the optional real time clock,
add all the components with the REAL TIME CLOCK
except RTC62423. DO NOT install both clock ICs.

BUSTar Amiga 2000 EATX

| | | |
|---------------------------------------|------------------|----------|
| Sheet: /BUS CONTROL/ File: bus.sch | | |
| Title: | | |
| Size: B | Date: 2021-10-08 | Rev: |
| KiCad E.D.A. kicad 5.1.6+dfsg1-1 | | Id: 7/14 |

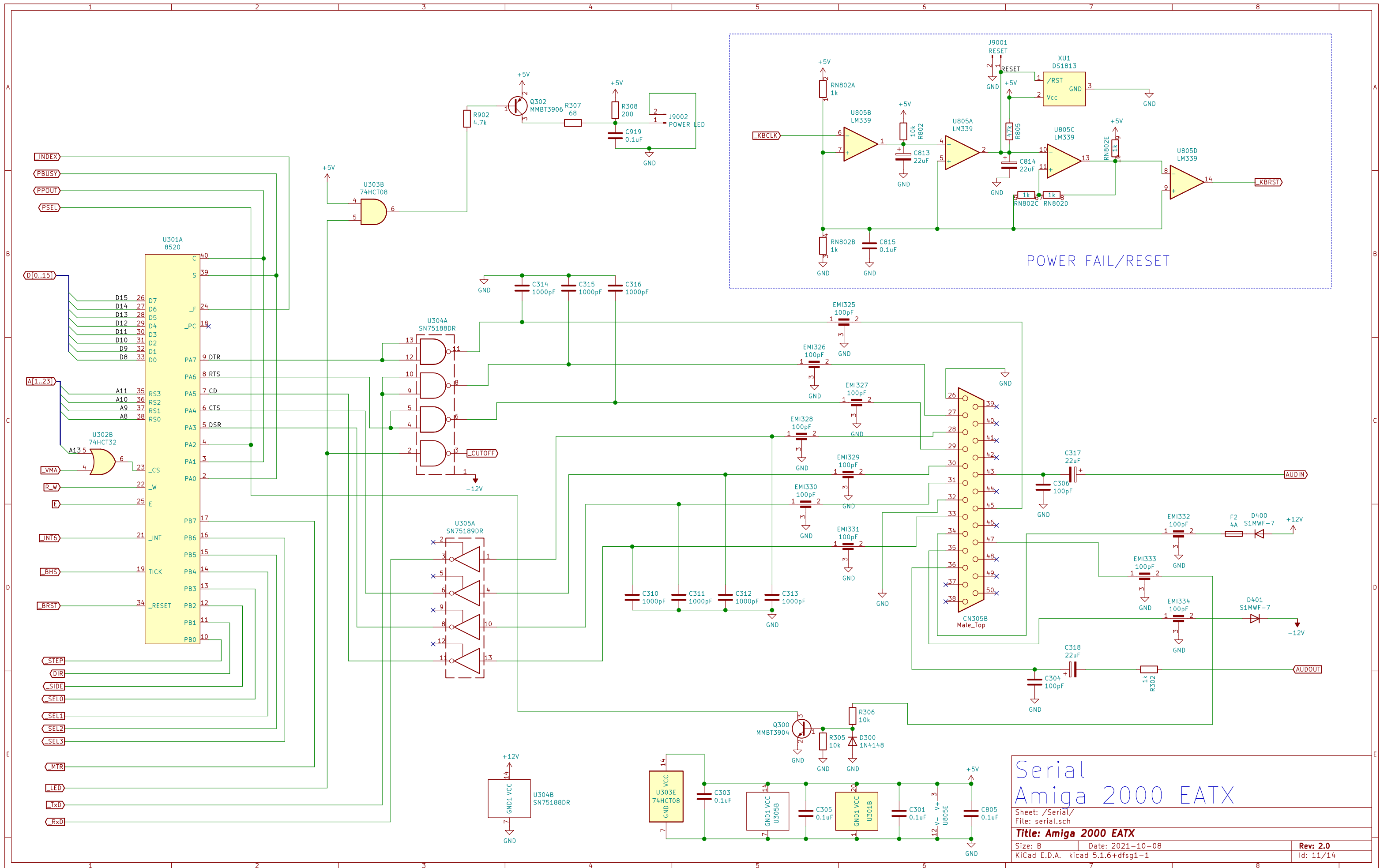






KiCad E.D.A. kicad 5.1.6+dfsg1-1 Id: 10/14

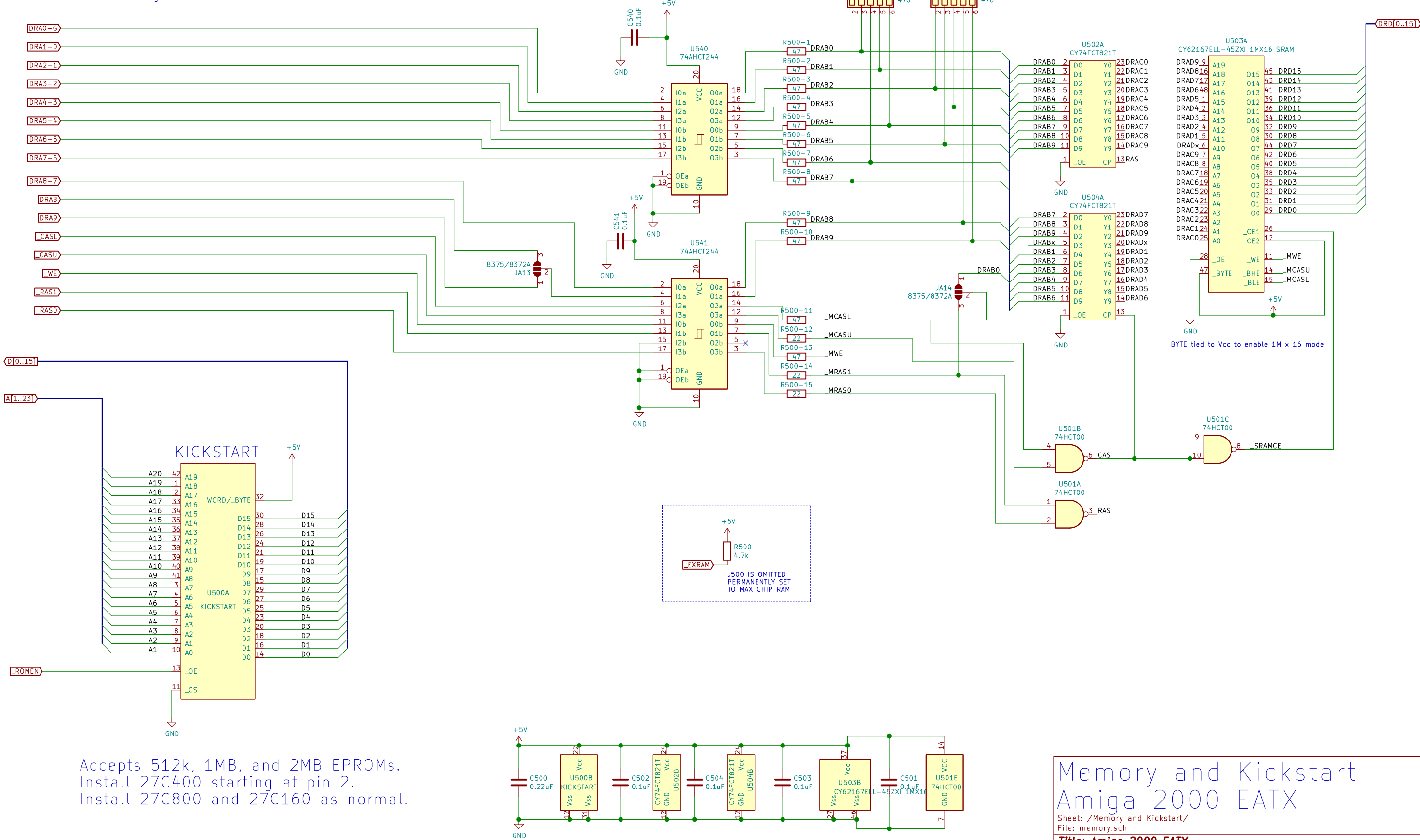
Id: 10/14



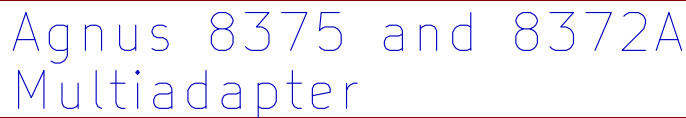


JUMPERS MUST BE CONFIGURED BY AGNUS PART NUMBER

DRA SIGNAL Description
8375 Left – 8372A Right



JUMPERS MUST BE CONFIGURED BY AGNUS PART NUMBER



| | | |
|----------------------------------|------------------|-----------|
| Sheet: /Agnus Multi Adapter/ | | |
| File: Agnus-Multi.sch | | |
| Title: Amiga 2000 EATX | | |
| Size: B | Date: 2021-10-08 | Rev: 2.0 |
| KiCad E.D.A. kicad 5.1.6+dfsg1-1 | | Id: 14/14 |