

\* Generated for: PrimeSim  
\* Design library name: AMIT  
\* Design cell name: Neuron\_activation\_function\_tb  
\* Design view name: schematic

.lib 'saed32nm.lib' TT

\*Custom Compiler Version S-2021.09

\*Fri Feb 25 04:11:15 2022

.global gnd!

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\* Library : AMIT  
\* Cell : Neuron\_activation\_function  
\* View : schematic  
\* View Search List : hspice hspiceD schematic spice verilog  
\* View Stop List : hspice hspiceD

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.subckt neuron\_activation\_function gnd\_1 inp out reference supply  
xm1 net9 supply supply supply p105 w=0.1u l=0.03u nf=1 m=1  
xm0 out supply supply supply p105 w=0.1u l=0.03u nf=1 m=1  
xm4 net16 reference gnd\_1 gnd\_1 n105 w=0.1u l=0.03u nf=1 m=1  
xm3 out gnd\_1 net16 net16 n105 w=0.1u l=0.03u nf=1 m=1  
xm2 net9 inp net16 net16 n105 w=0.1u l=0.03u nf=1 m=1  
.ends neuron\_activation\_function

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xi0 gnd! net9 output net11 net8 neuron\_activation\_function

v5 net11 gnd! dc=0.2

v1 net8 gnd! dc=1.8

v3 net9 gnd! dc=0 sin ( 0 100m 0.5meg 0 0 0 )

.tran '0.1u' '400n' name=tran

.option primesim\_remove\_probe\_prefix = 0

.probe v(\*) i(\*) level=1

.probe tran v(output)

.temp 25

.option primesim\_output=wdf

.option parhier = LOCAL

.end