Nvshare

A Kubernetes Use Case

George Alexopoulos <giorgosalexo0@gmail.com>













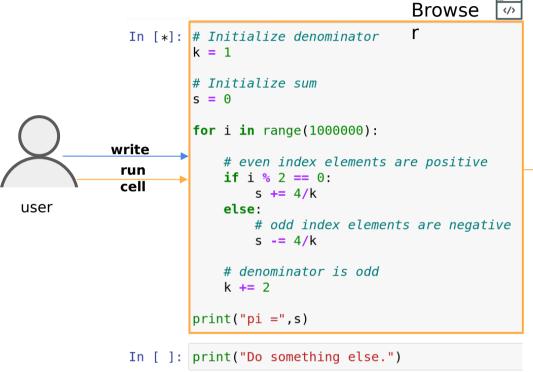
```
Browse
              In [ ]: # Initialize denominator
                      k = 1
                      # Initialize sum
                      s = 0
                      for i in range(1000000):
           write
                          # even index elements are positive
                          if i % 2 == 0:
                              s += 4/k
user
                          else:
                              # odd index elements are negative
                              s = 4/k
                          # denominator is odd
                          k += 2
                      print("pi =",s)
              In [ ]: print("Do something else.")
```

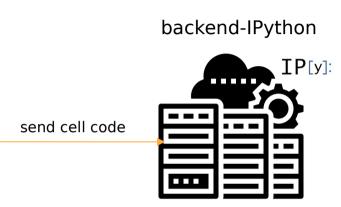


Jupyter Interactive environment

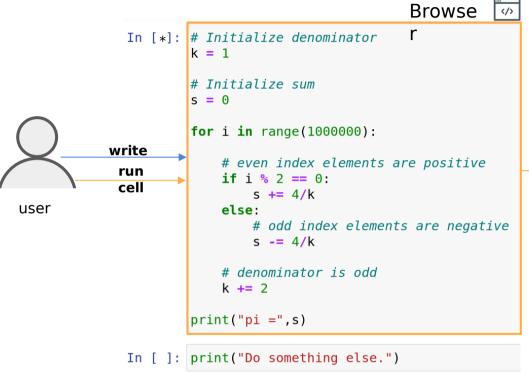
```
Browse
              In [ ]: # Initialize denominator
                      k = 1
                      # Initialize sum
                      s = 0
                      for i in range(1000000):
           write
                          # even index elements are positive
            run
                          if i % 2 == 0:
            cell
                              s += 4/k
user
                          else:
                              # odd index elements are negative
                              s = 4/k
                          # denominator is odd
                          k += 2
                      print("pi =",s)
              In [ ]: print("Do something else.")
```

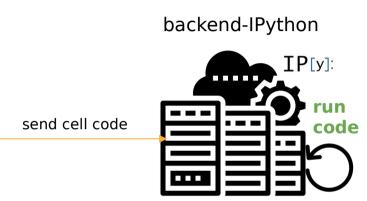




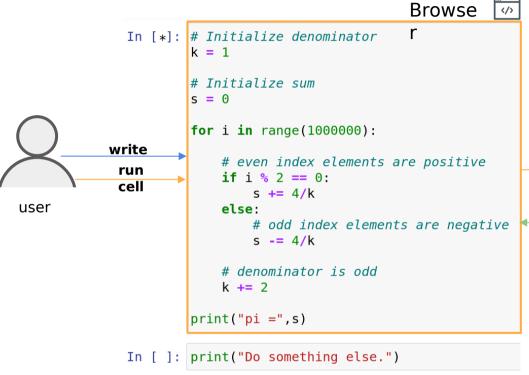


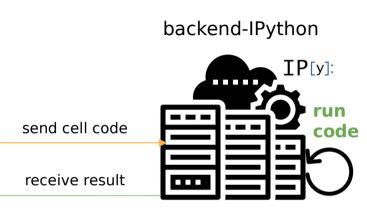




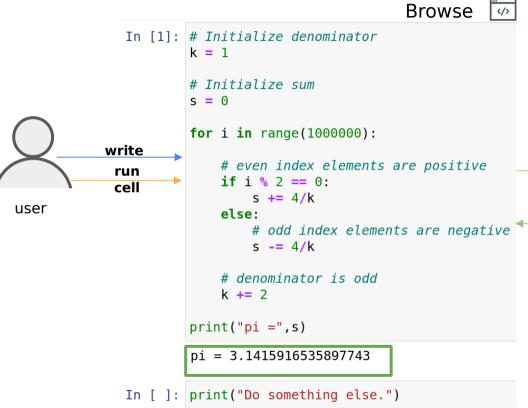


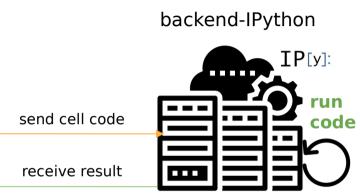




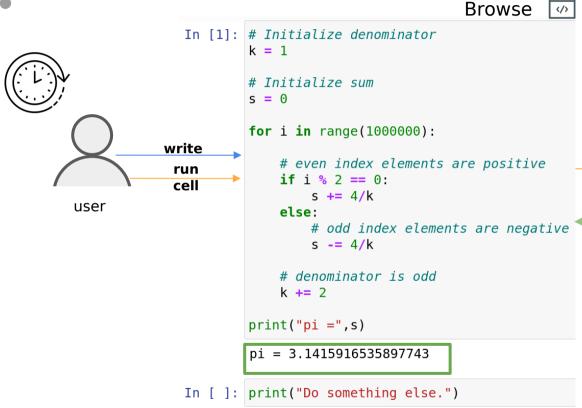


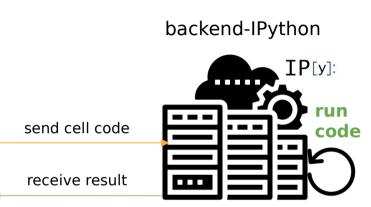




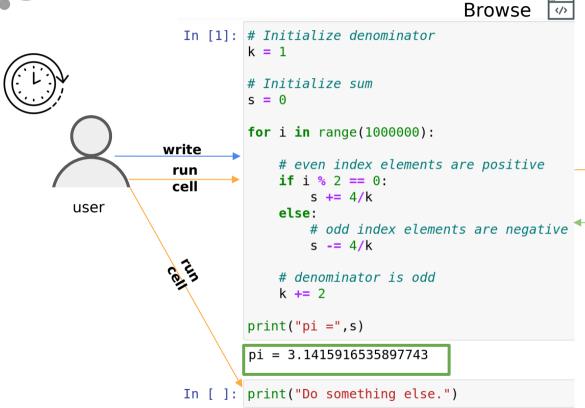


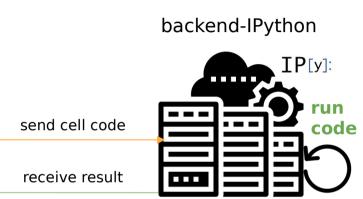




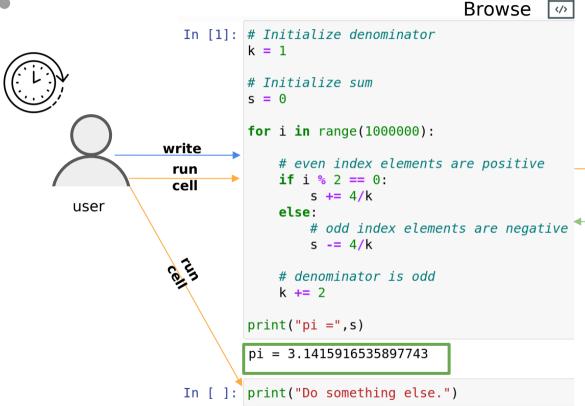


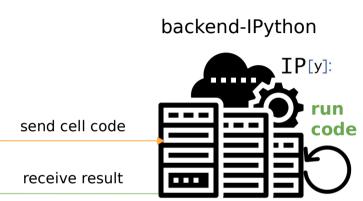




































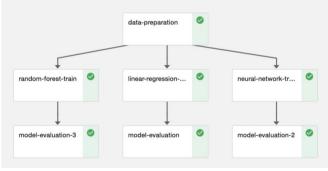


run as pod



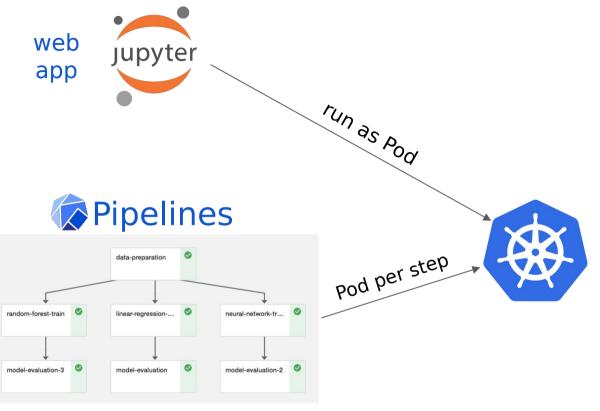




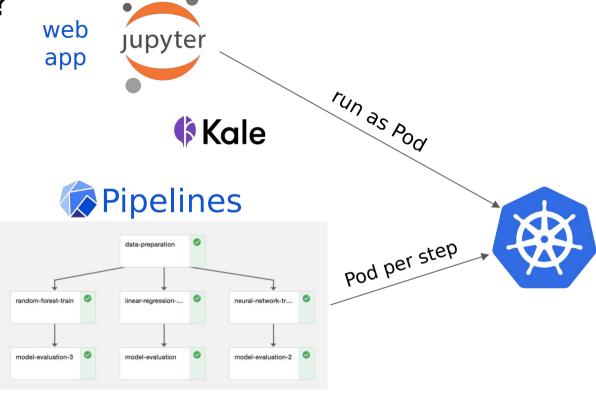




run as bod

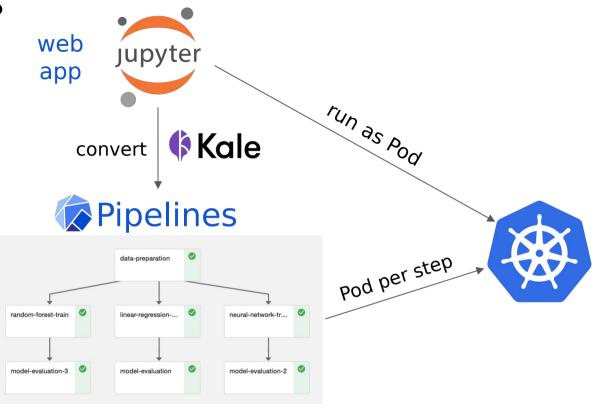




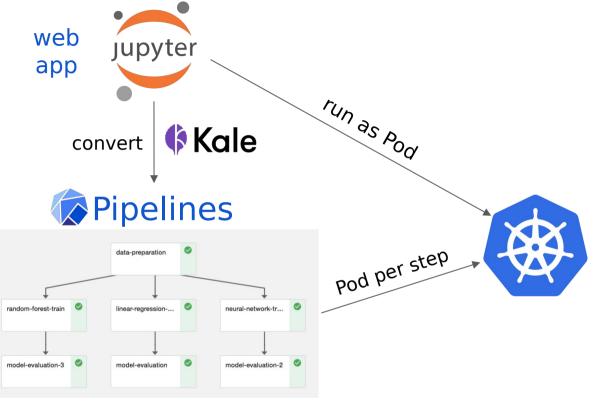






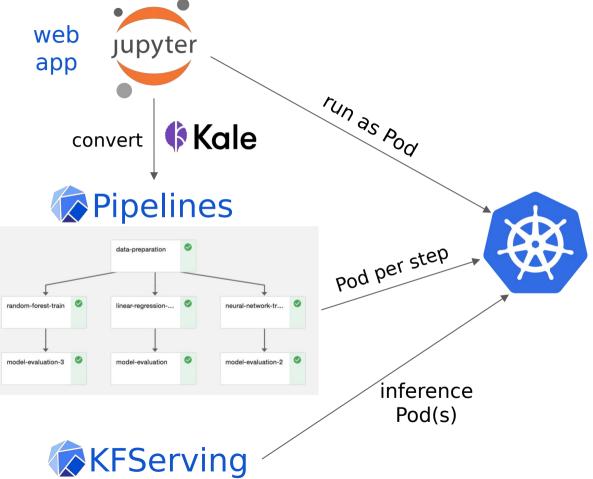




















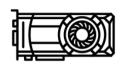


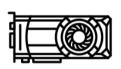


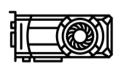


























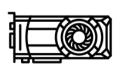
















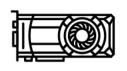






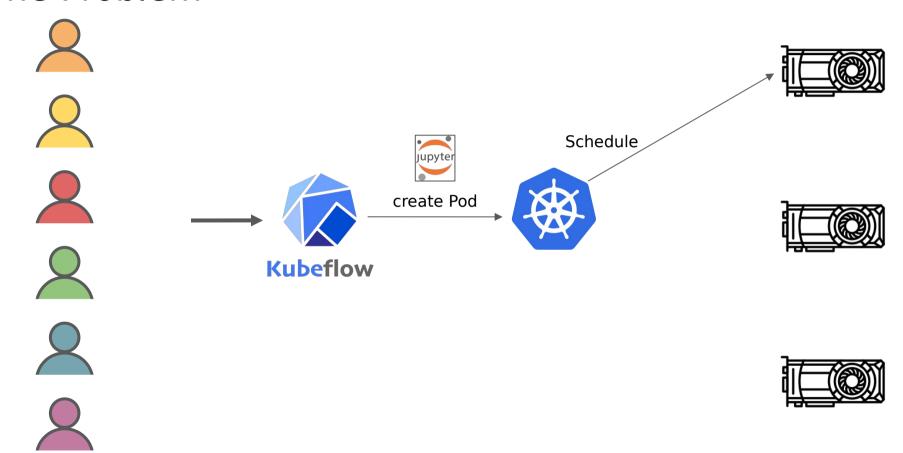


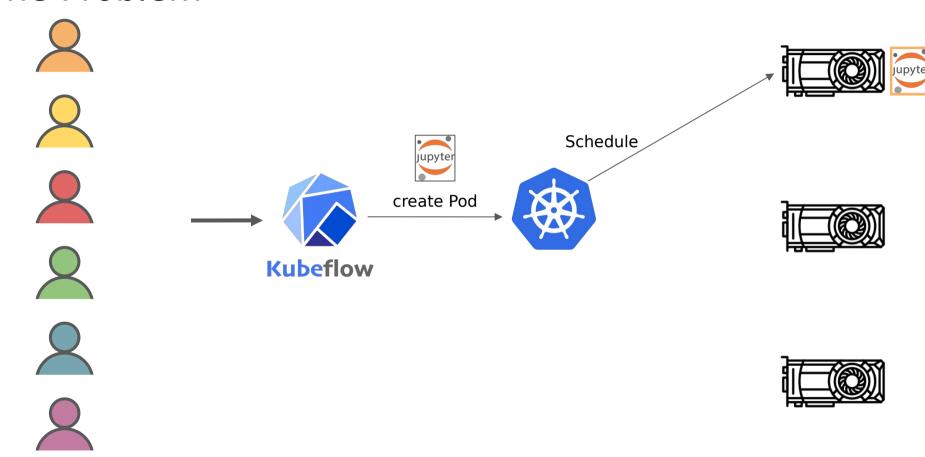


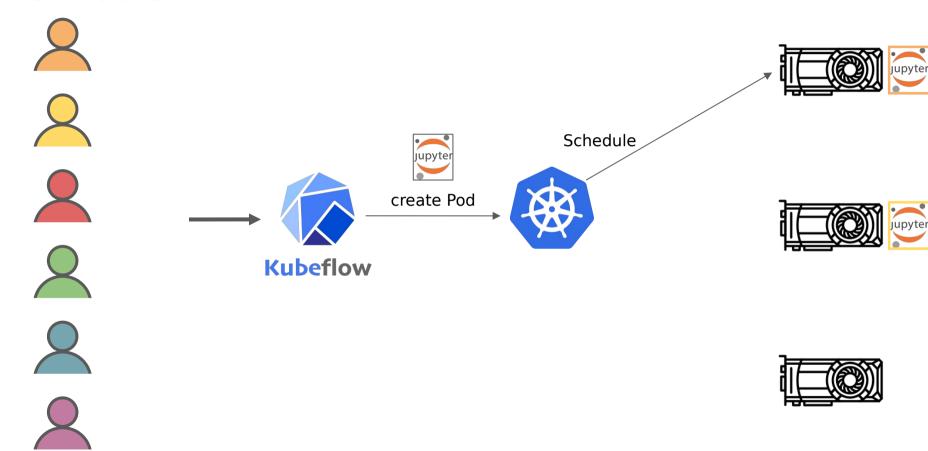


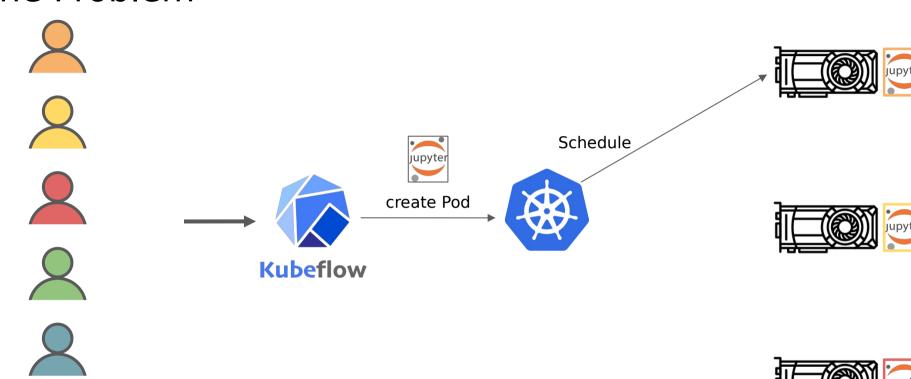


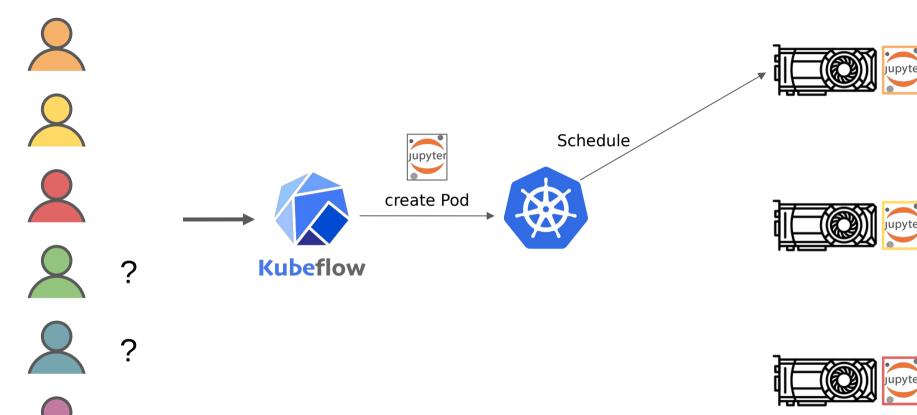


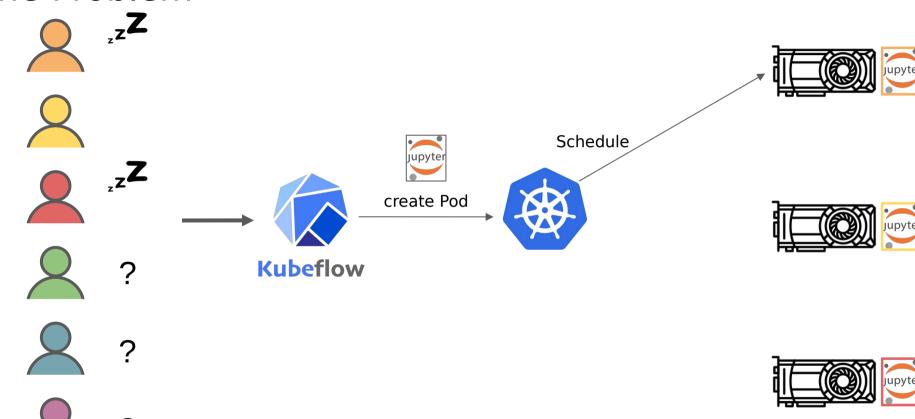


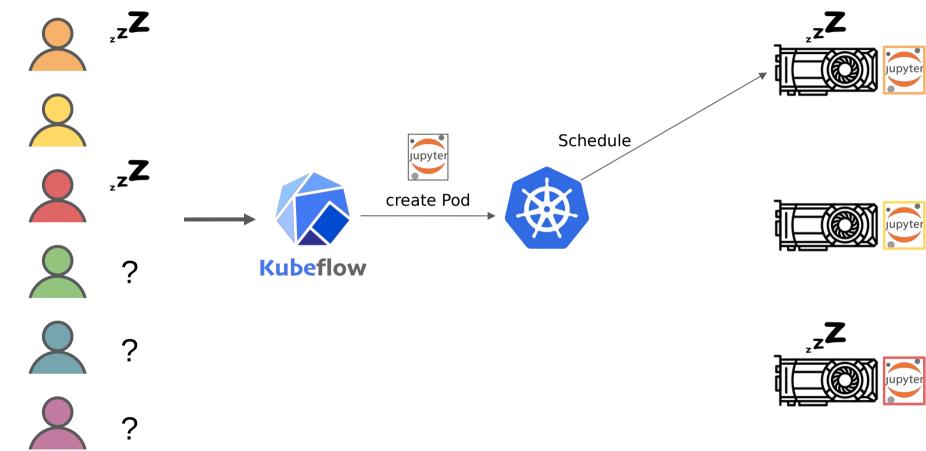






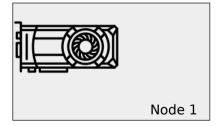




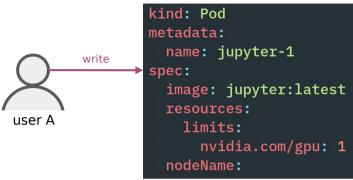




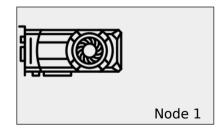
Scheduler





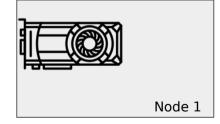


Scheduler

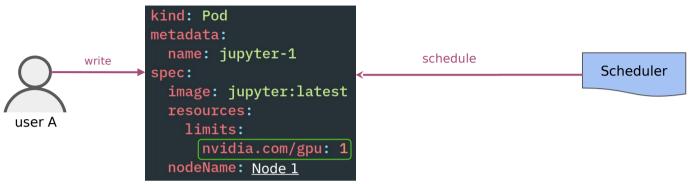


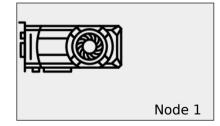




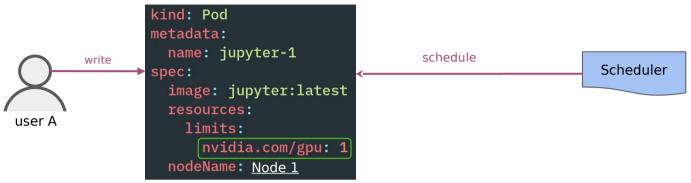


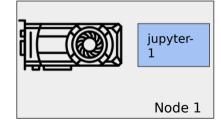






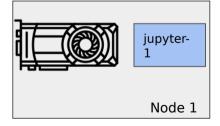








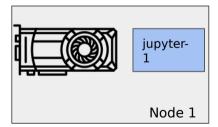




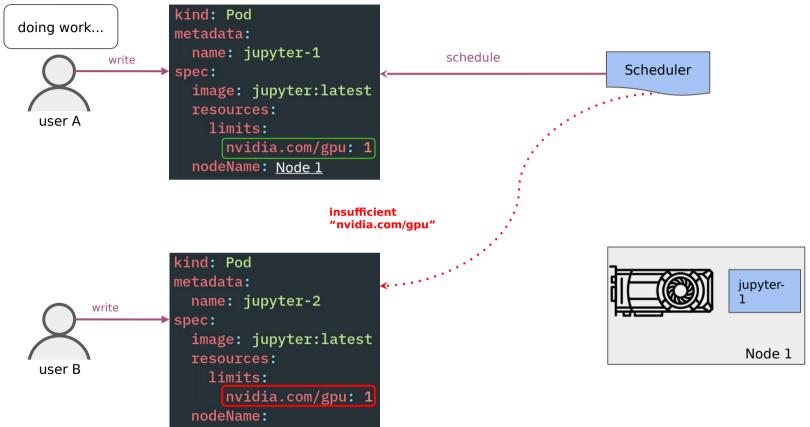




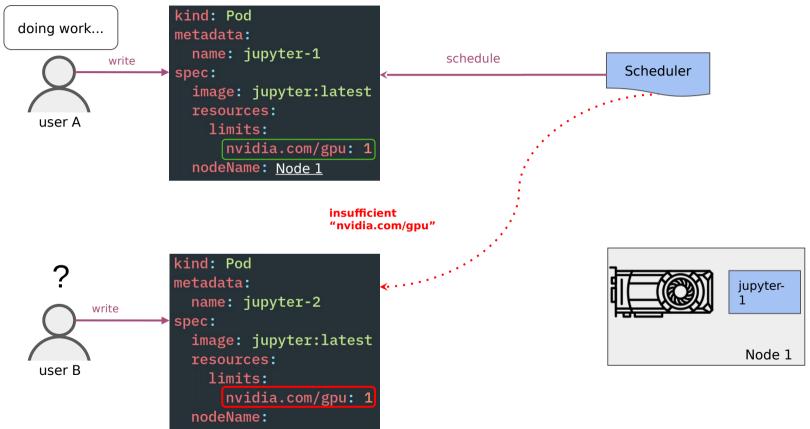




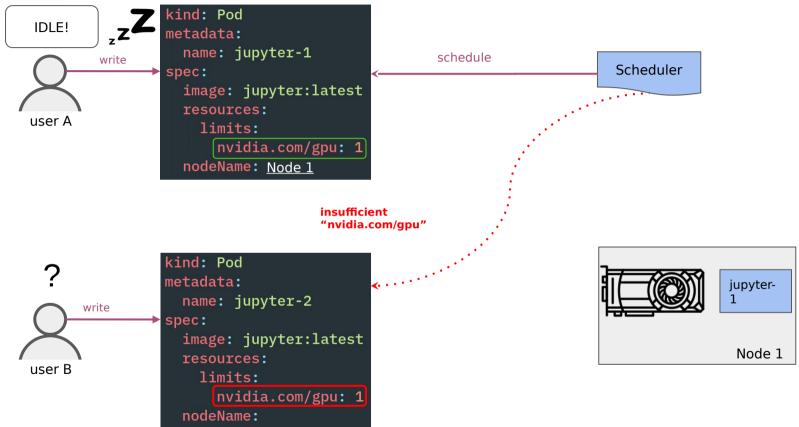




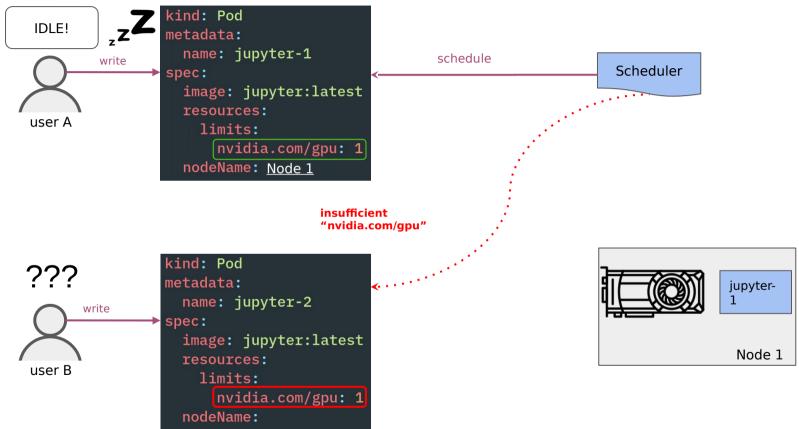




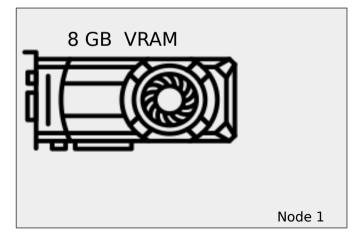






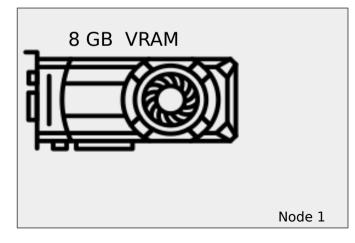


Scheduler

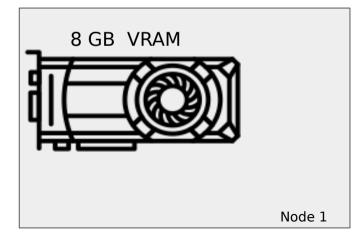


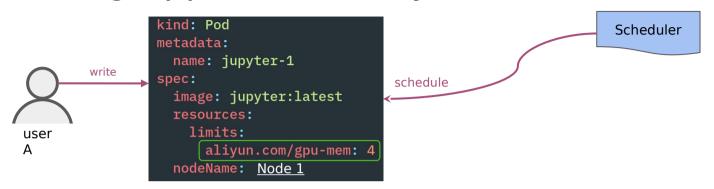


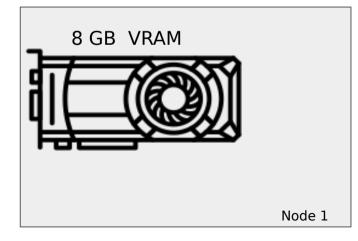
Scheduler



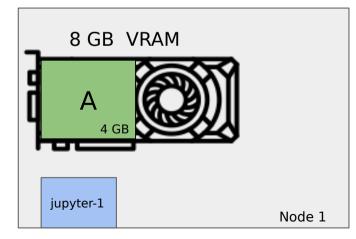


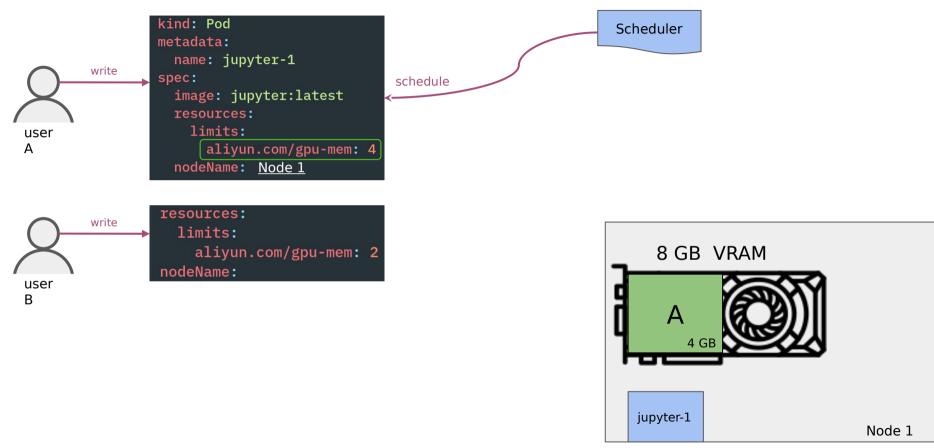


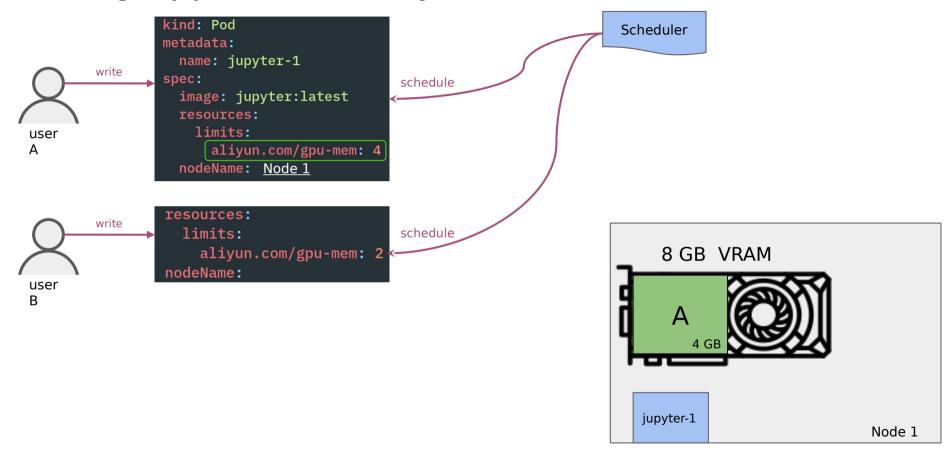


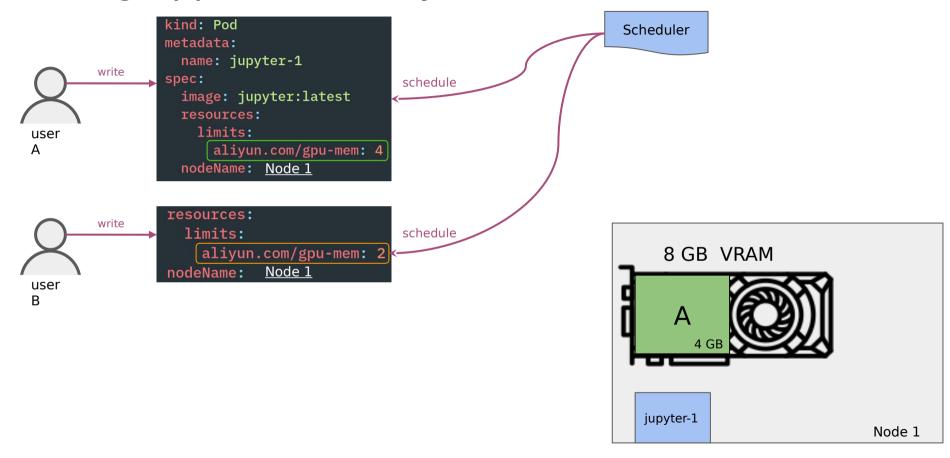


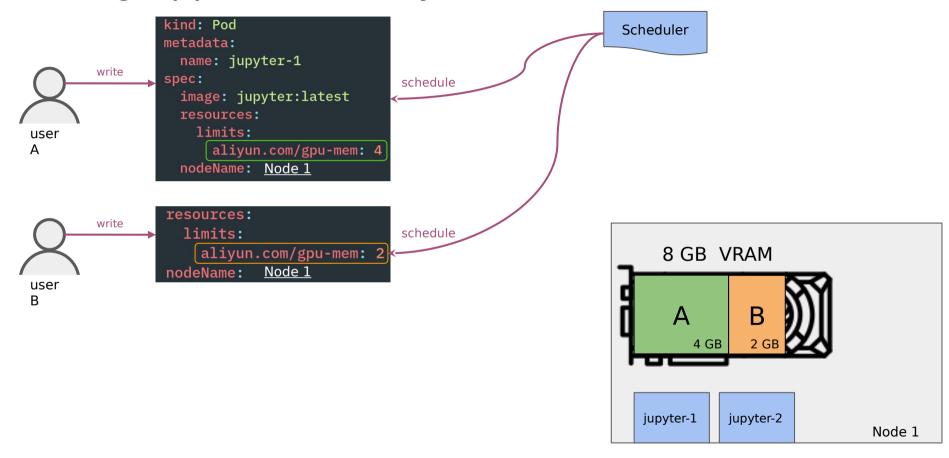


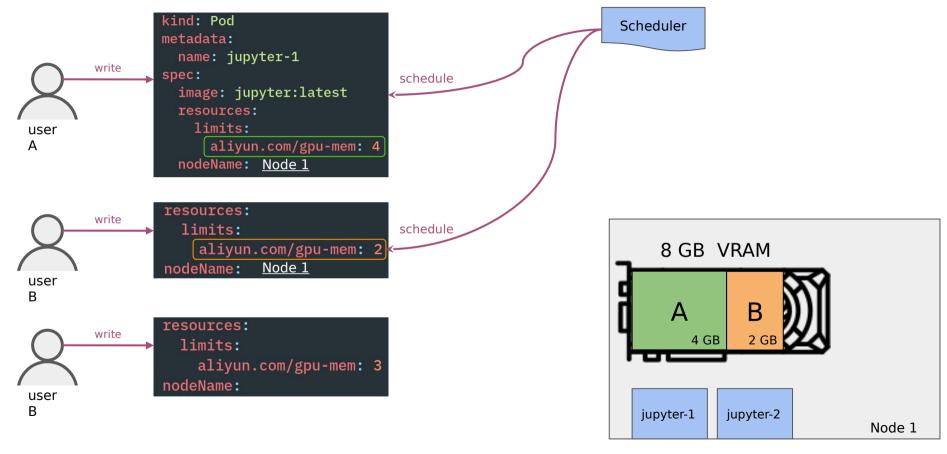


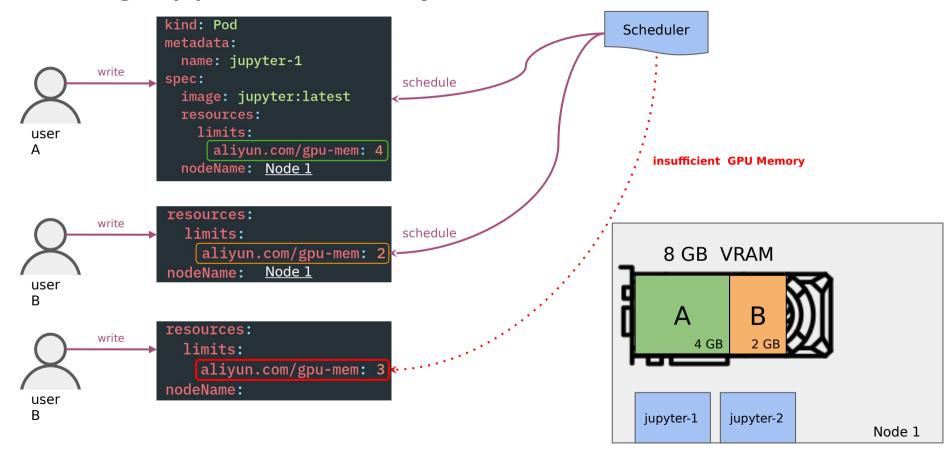


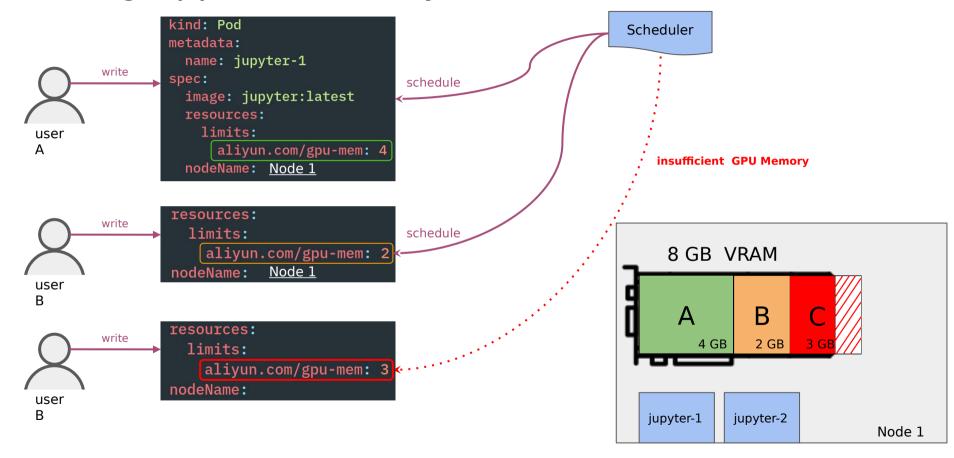














user



user

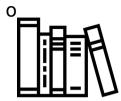
libnvshare.s

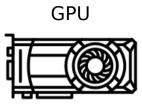


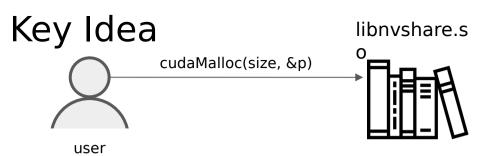


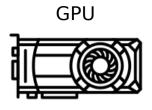
user

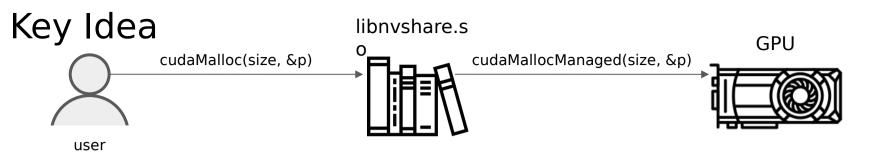
libnvshare.s

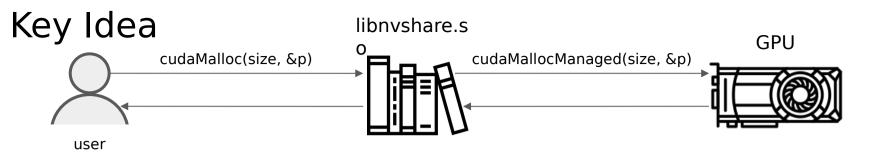


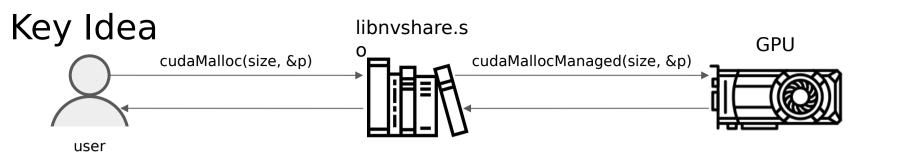


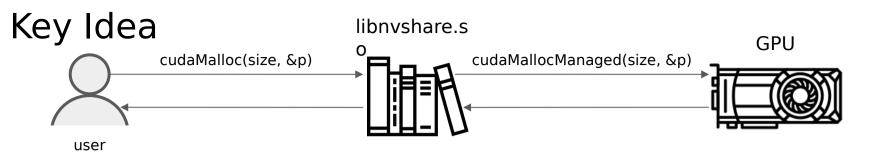




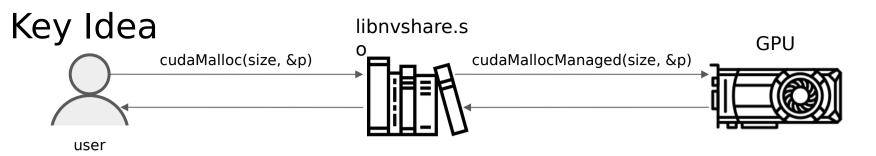














user A

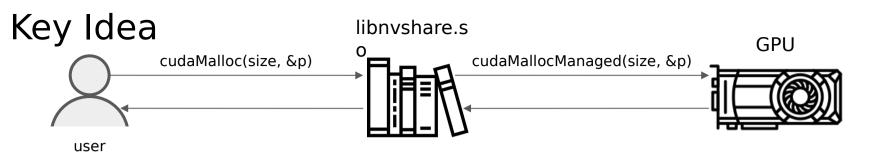
GPU

Α0

A1

A2

А3





user A

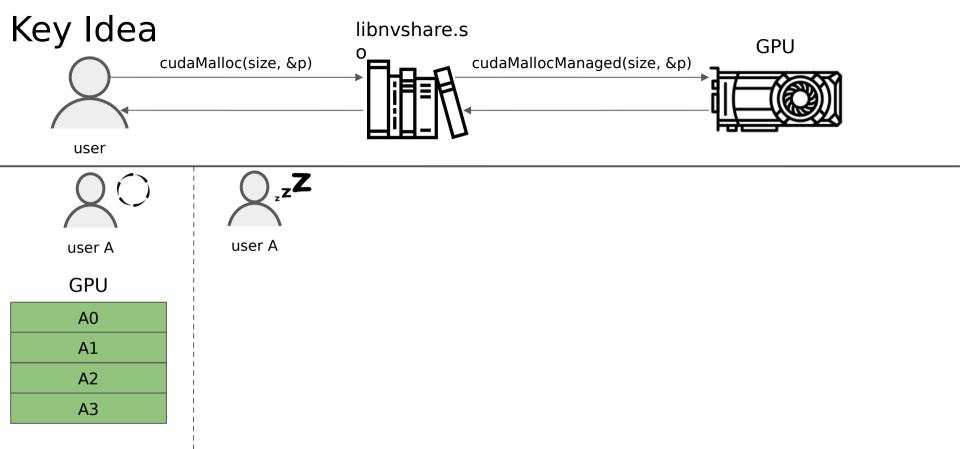
GPU

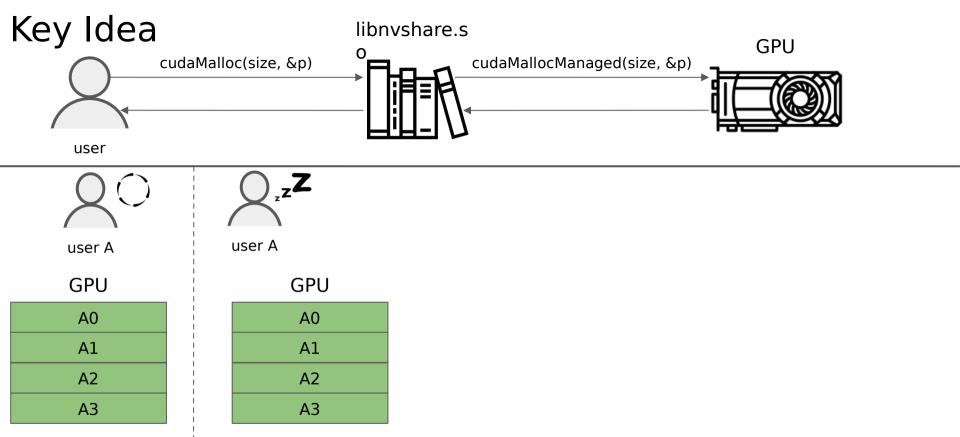
Α0

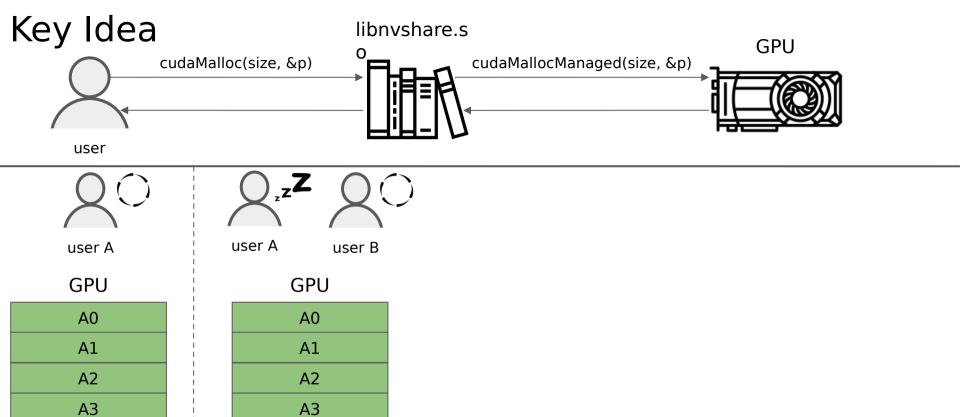
A1

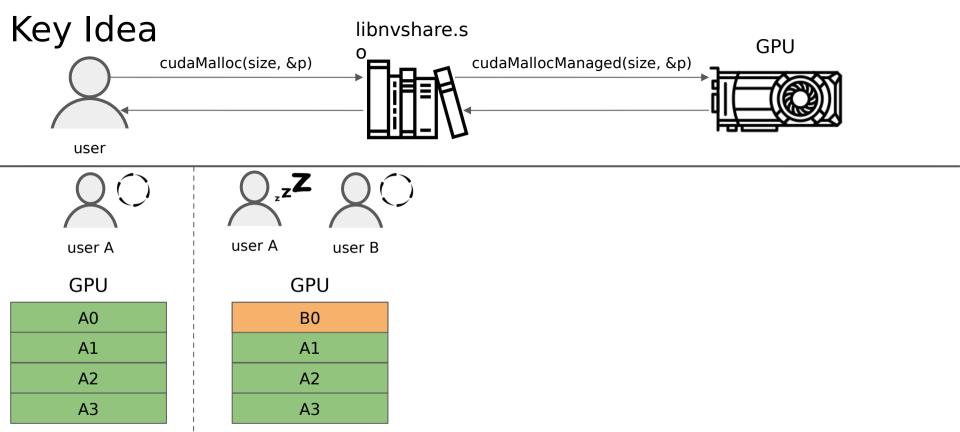
A2

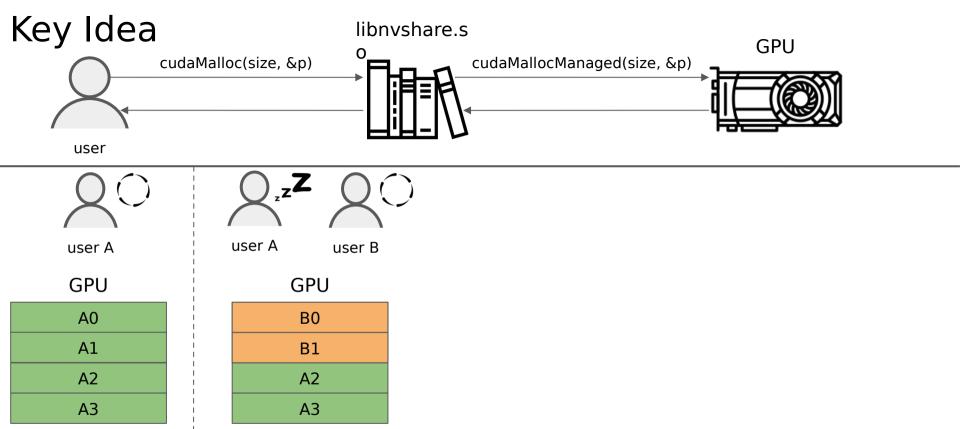
А3

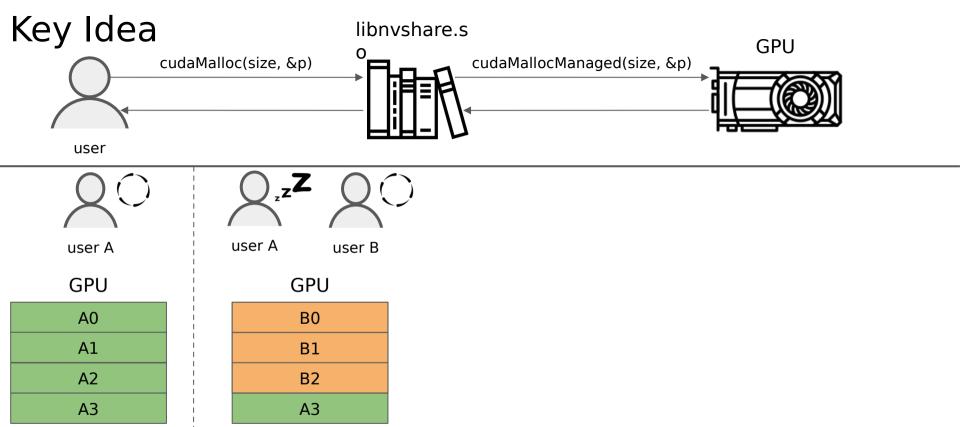


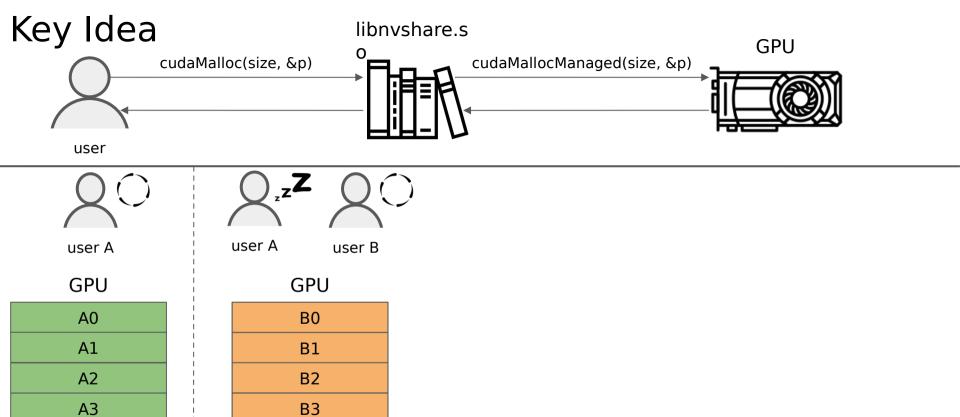


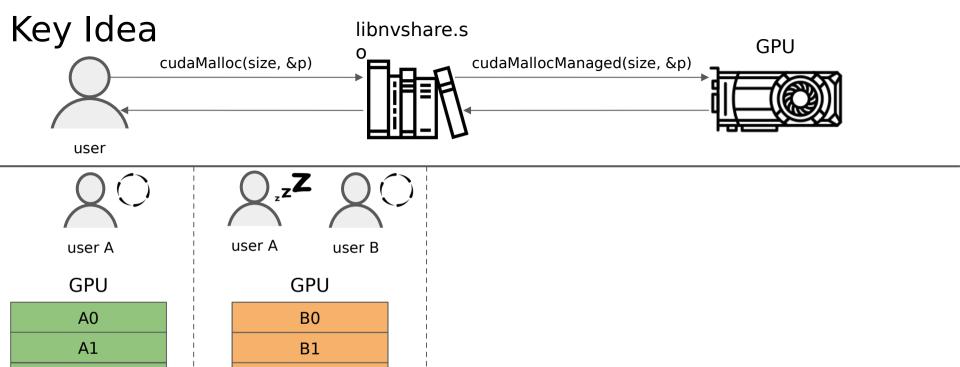










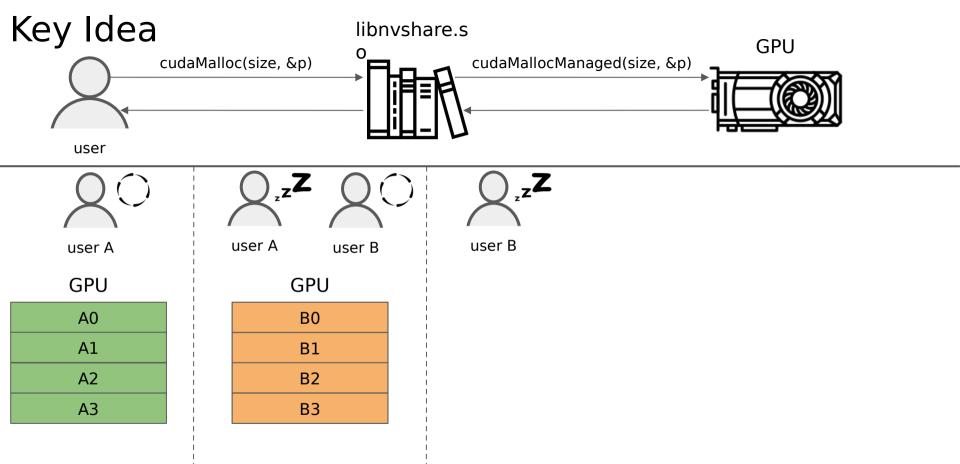


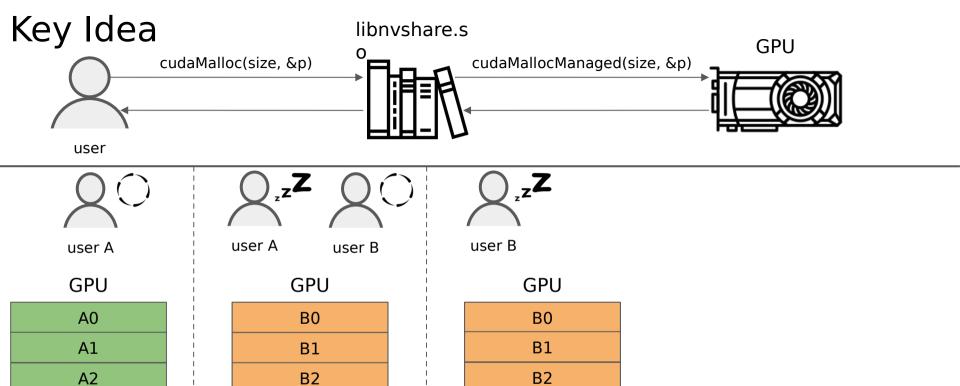
B2

B3

A2

А3

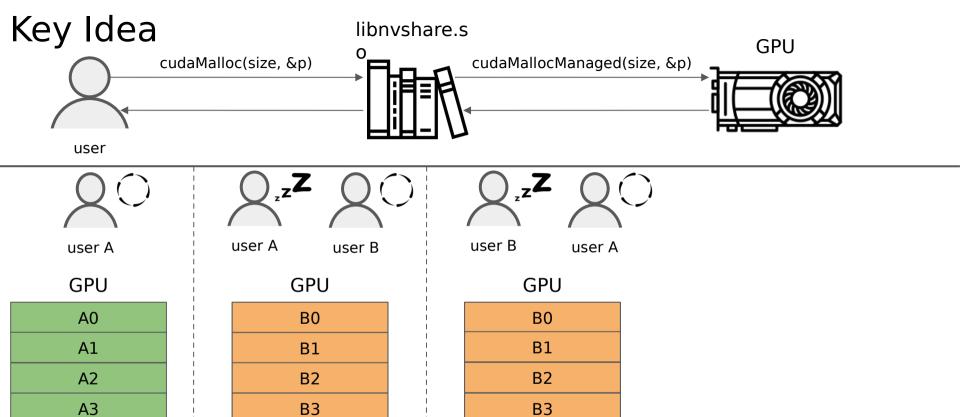


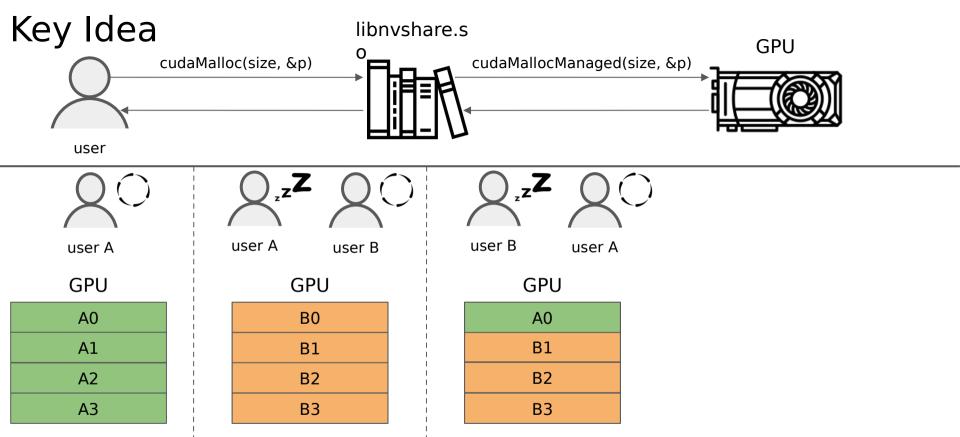


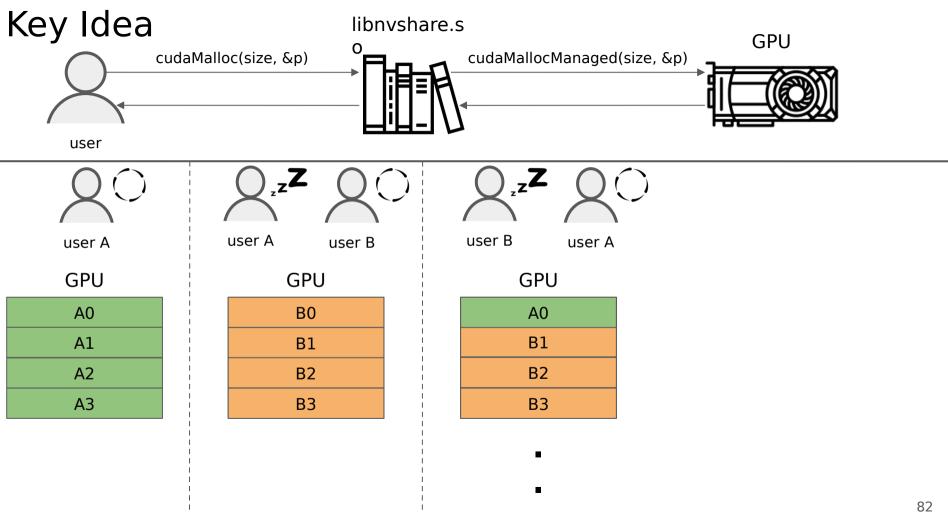
B3

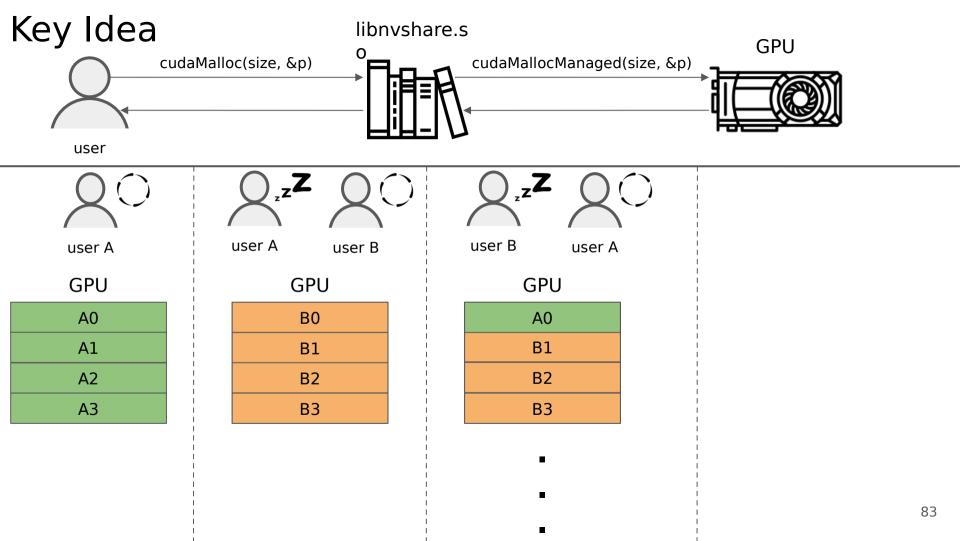
B3

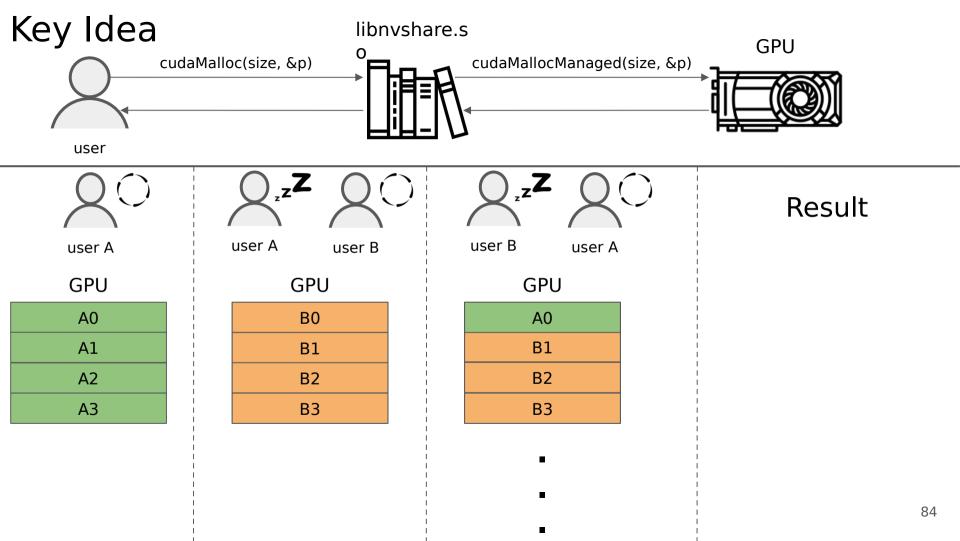
A3

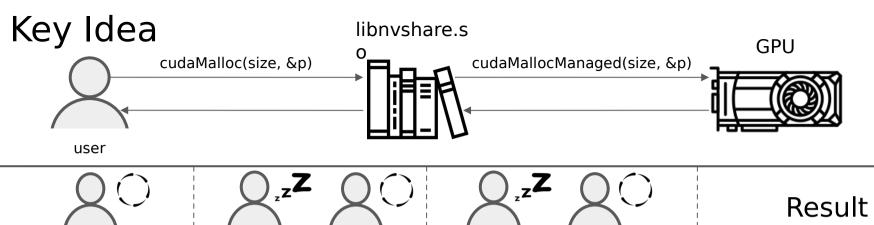














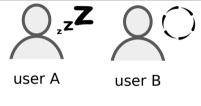
GPU

Α0

A1

A2

A3



GPU

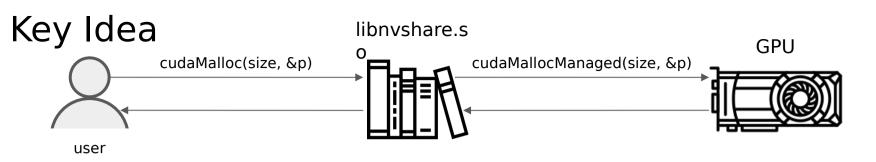
В0 В1 B2 B3

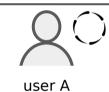


GPU

A0	
В1	
B2	
В3	

of co-located processes limited by system RAM



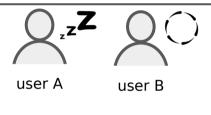


GPU

A0 A1

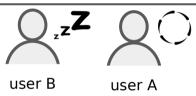
A2

A3



GPU

B0 B1 B2 B3



GPU

Α0	
B1	
B2	
В3	

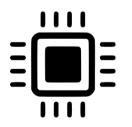
- •
- ļ

Result

- # of co-located processes limited by system RAM
- each process can use the whole GPU memory



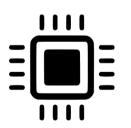


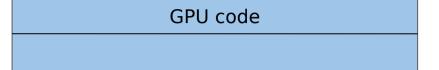


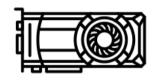
GPU code



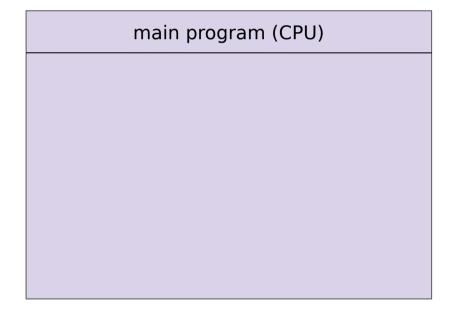


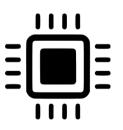












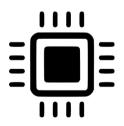
GPU code

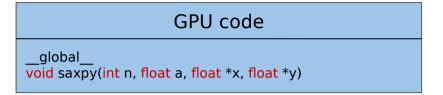
__global__
void saxpy(int n, float a, float *x, float *y)

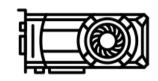




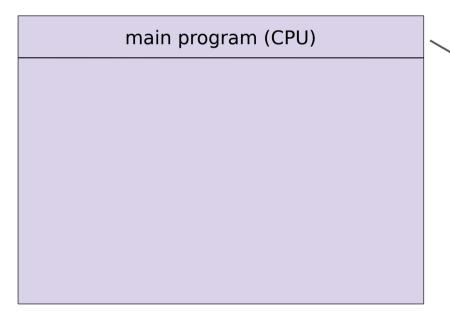
main program (CPU)

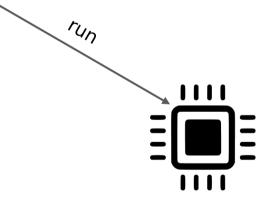


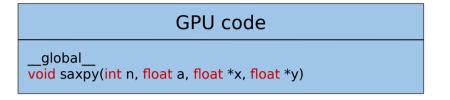


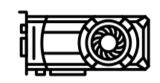








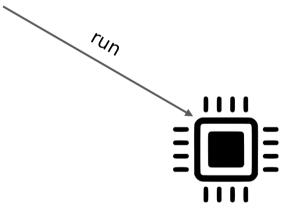




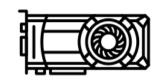


main program (CPU)

malloc();
cudaMalloc();



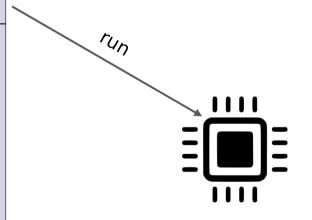
__global__ void saxpy(int n, float a, float *x, float *y)



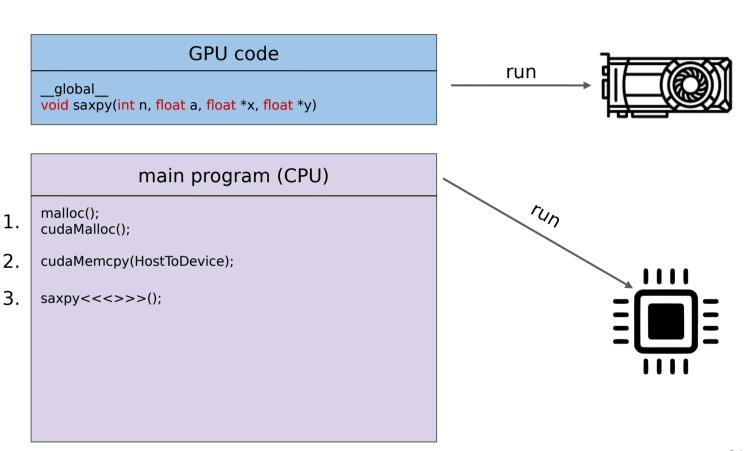


main program (CPU)

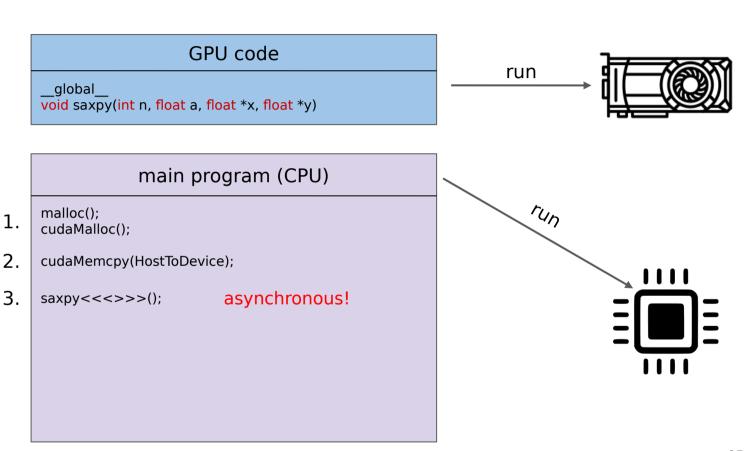
- malloc();
 cudaMalloc();
- cudaMemcpy(HostToDevice);

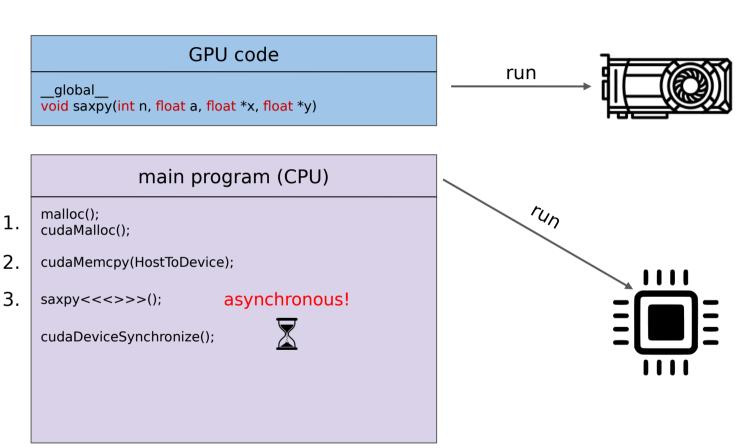


user

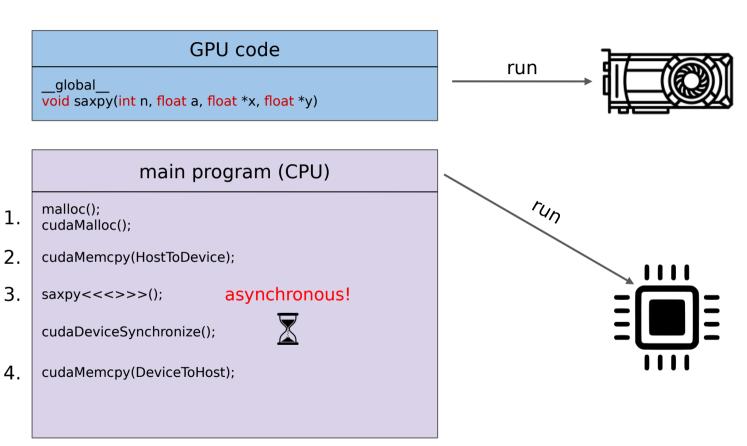


user

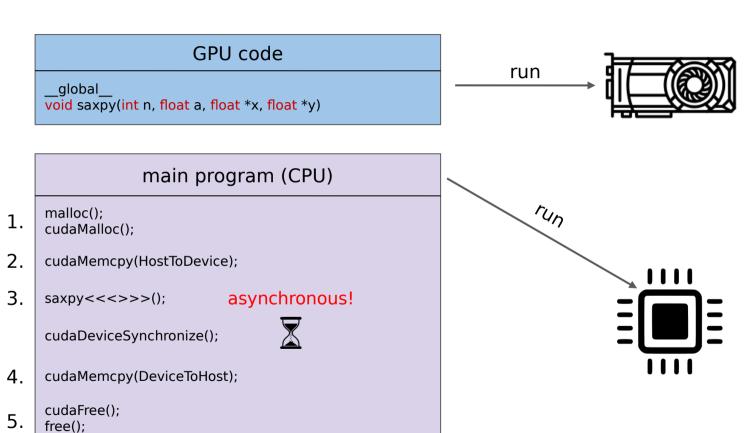




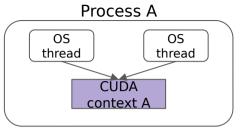


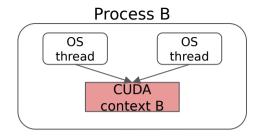


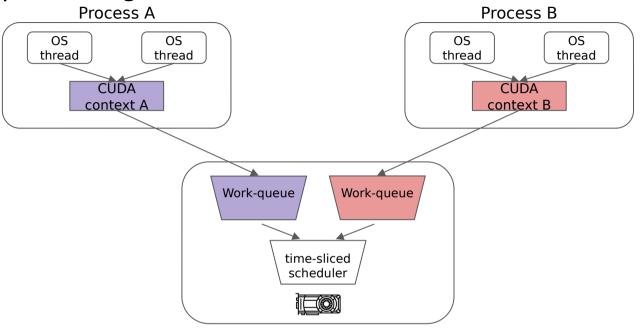


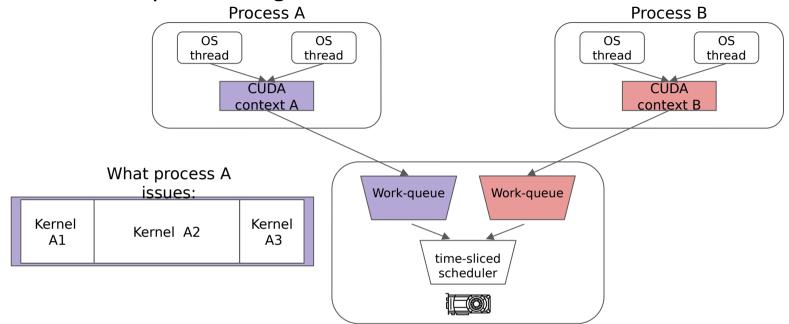


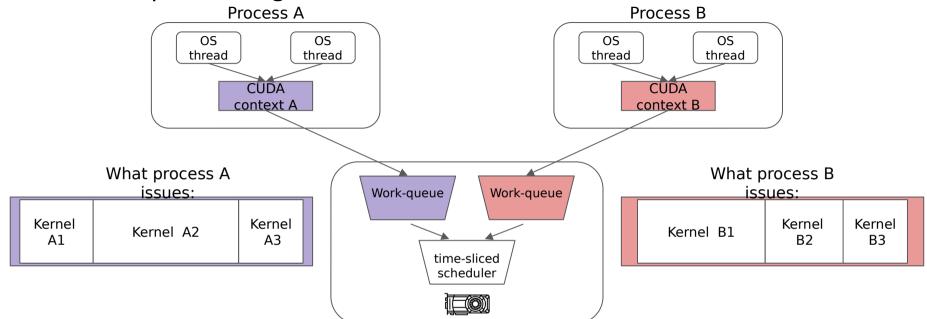


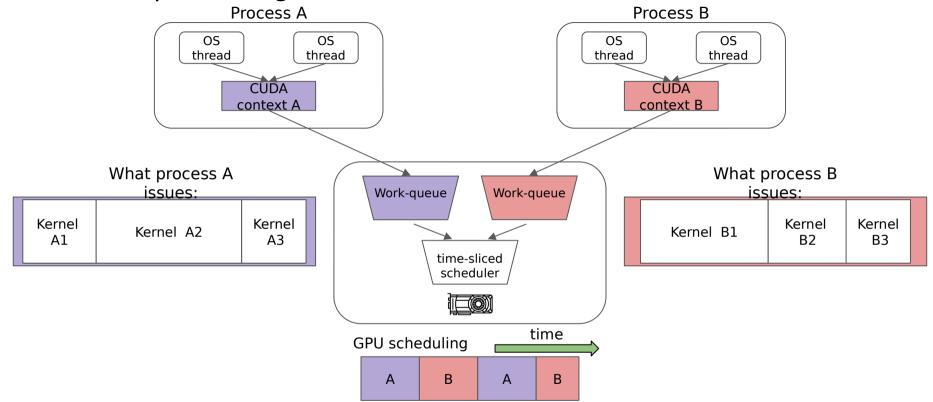




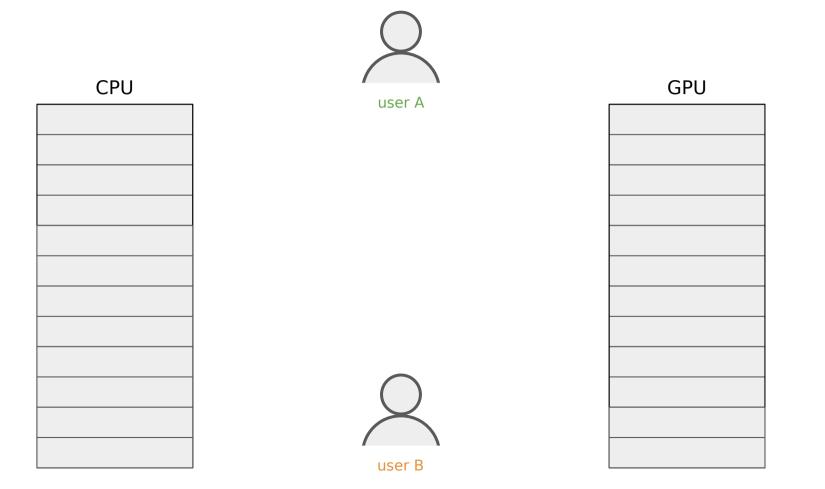


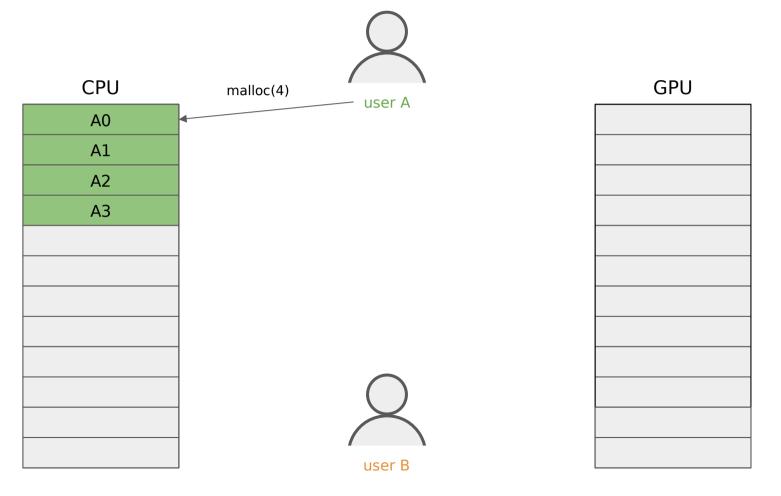


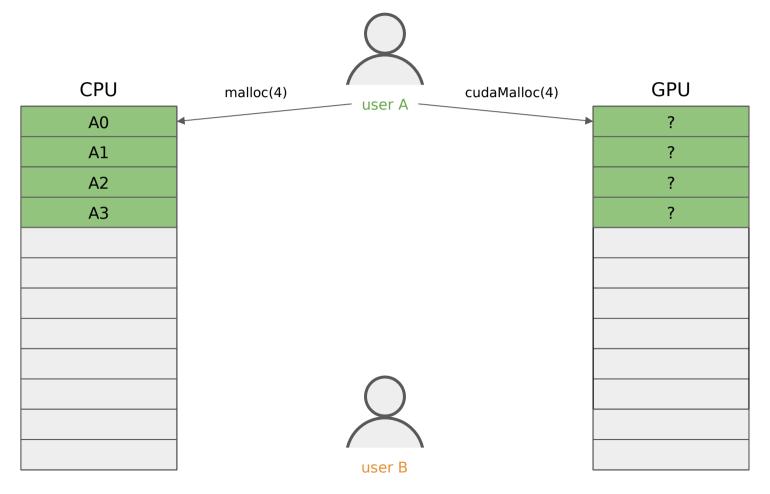


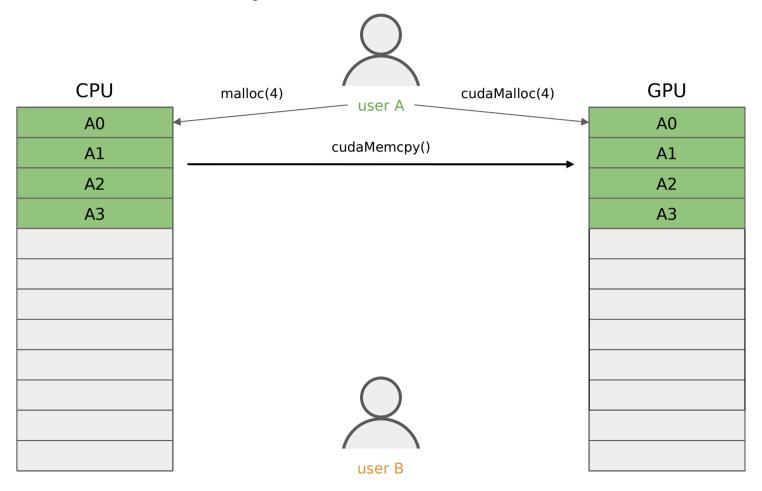


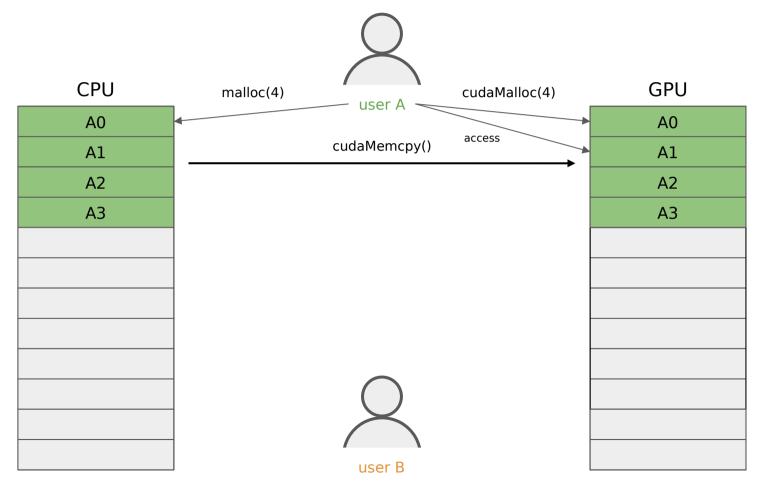
CUDA multi-processing Process A Process B OS OS OS OS thread thread thread thread CUDA CUDA context A context B What process A What process B issues: Work-queue Work-queue issues: Kernel Kernel Kernel Kernel Kernel A2 Kernel B1 Α1 А3 B2 В3 time-sliced scheduler time **GPU** scheduling Α В Α В B2. В1 Α1 A2... ...A2 А3 ...B2 В3 104

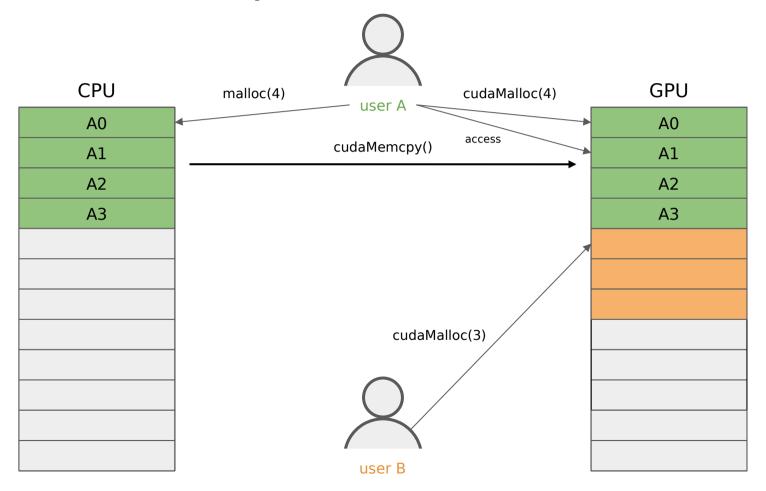


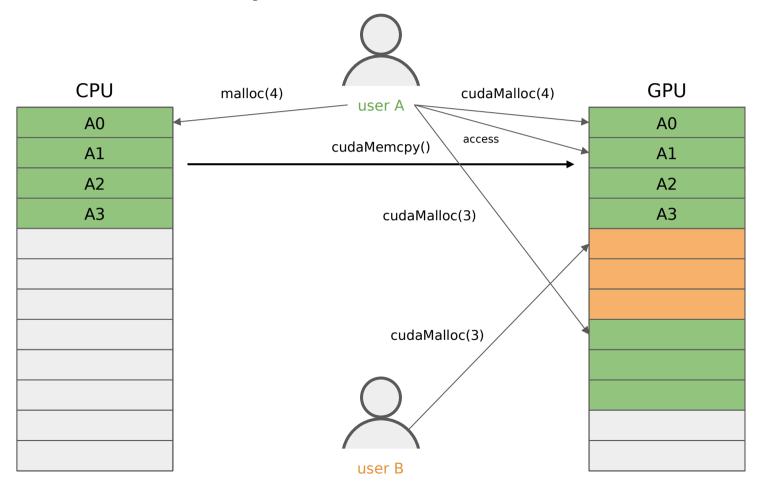


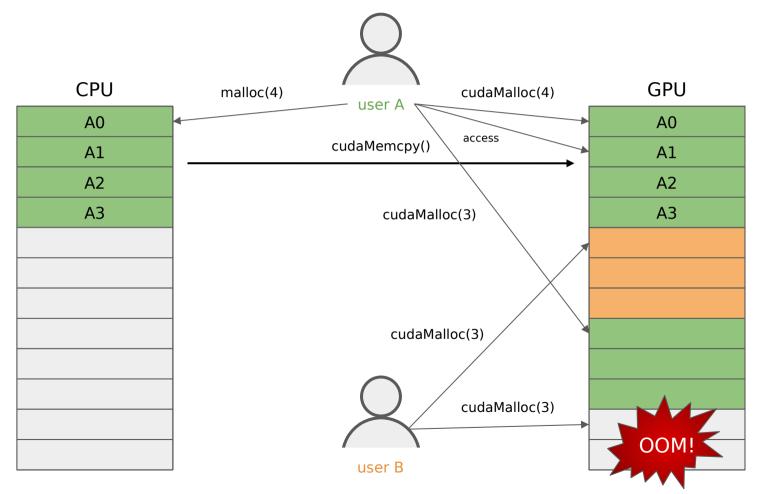




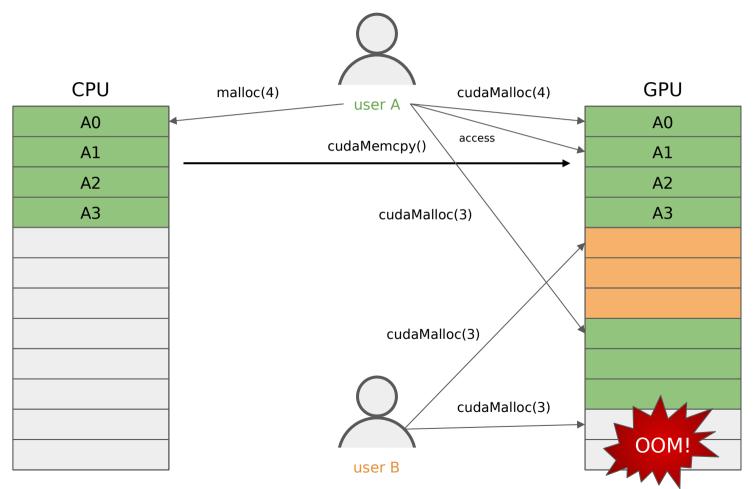


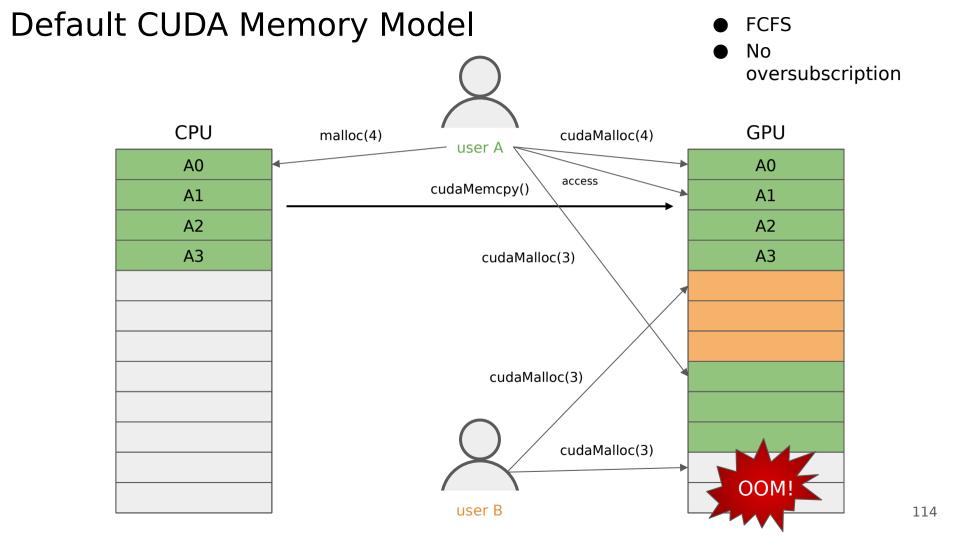


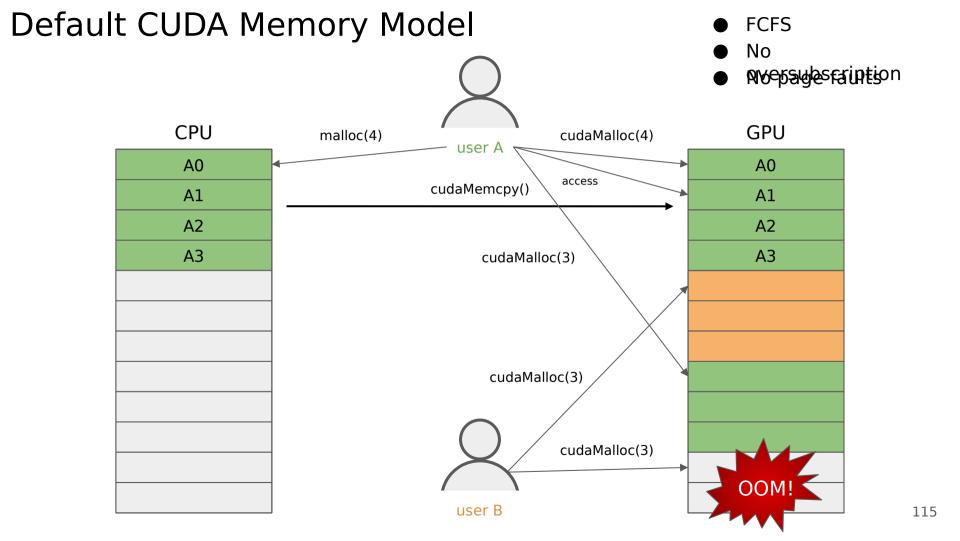


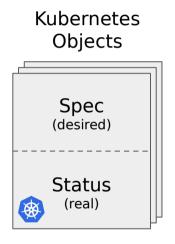


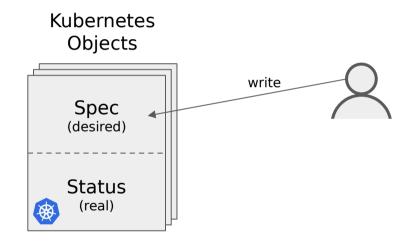
FCFS

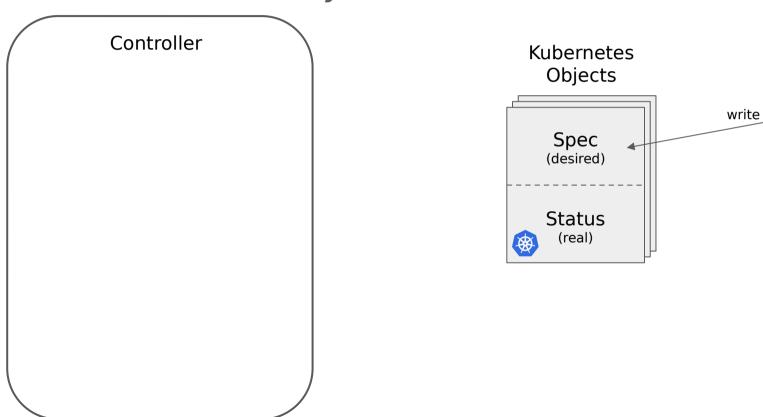


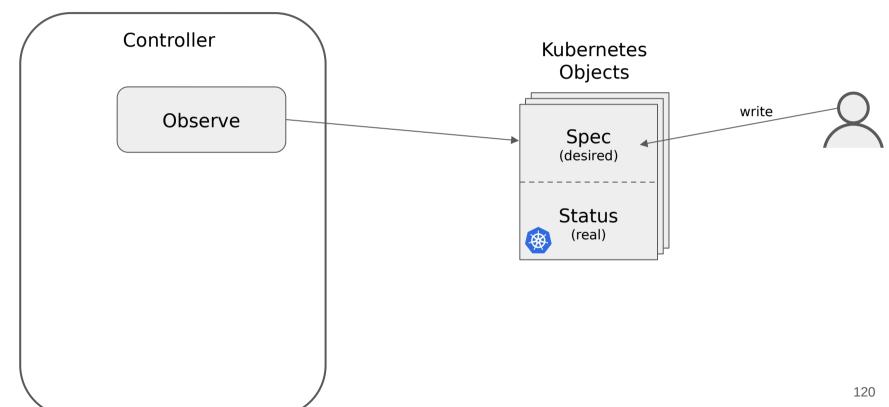


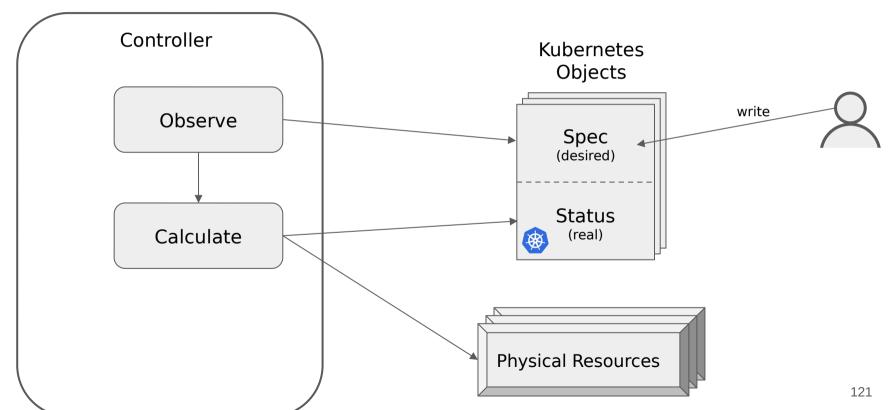


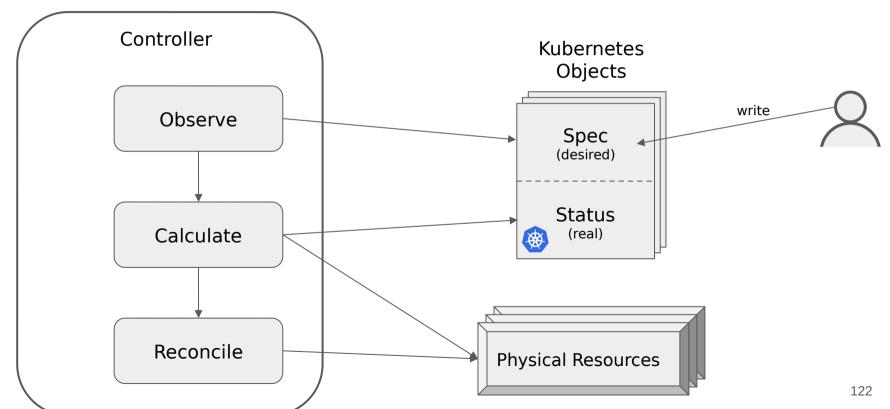


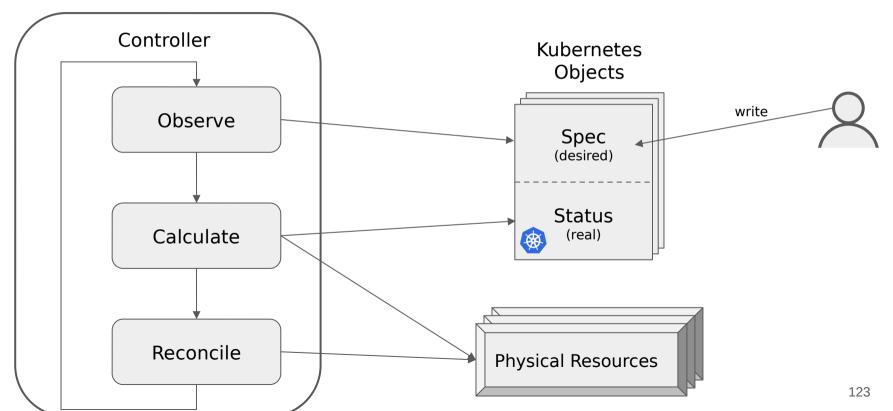


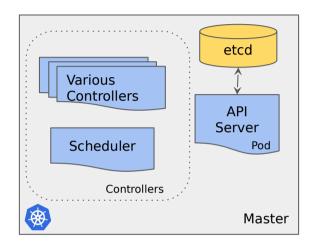


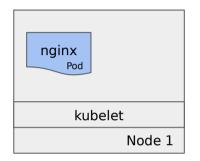




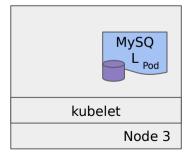


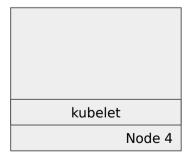


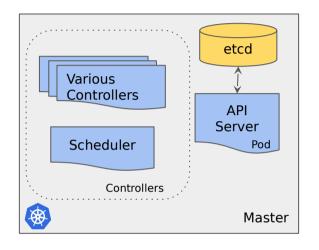




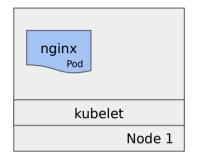


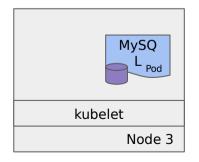


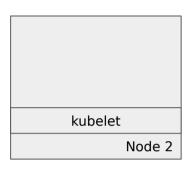


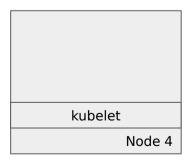


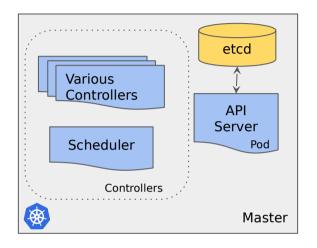


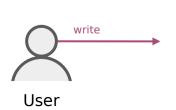


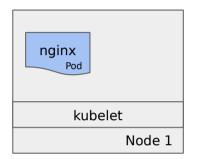


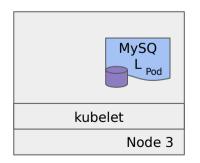


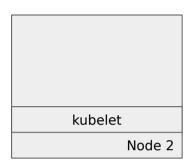


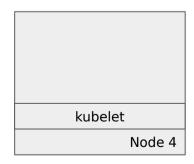


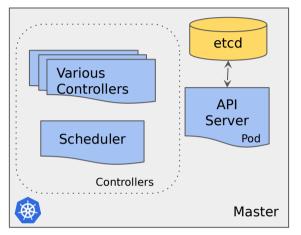




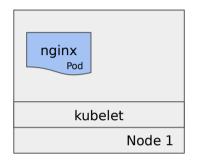


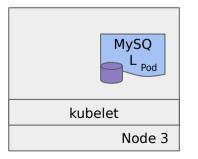


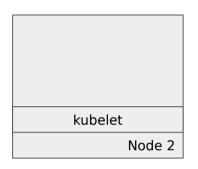


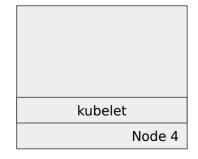


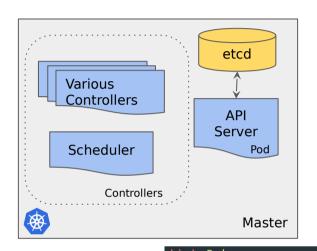






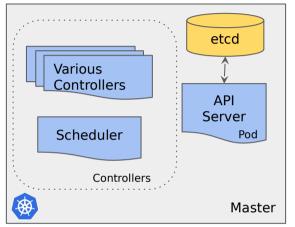


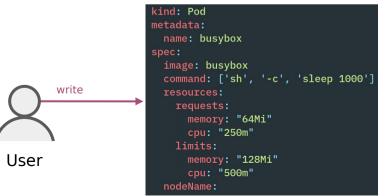


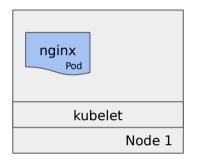


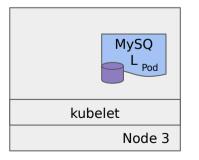


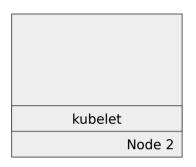
```
kind: Pod
metadata:
  name: busybox
spec:
  image: busybox
  command: ['sh', '-c', 'sleep 1000']
  resources:
    requests:
      memory: "64Mi"
      cpu: "250m"
    limits:
      memory: "128Mi"
      cpu: "500m"
  nodeName:
```

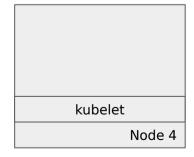


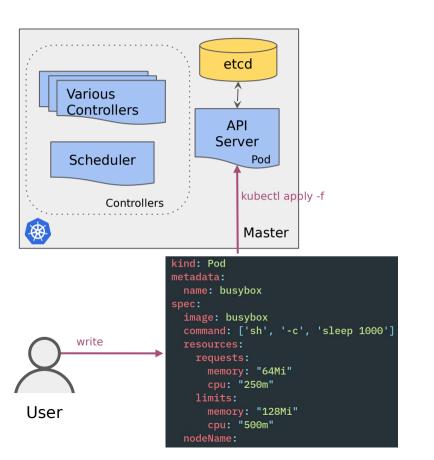


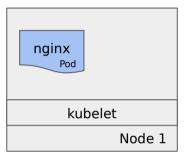


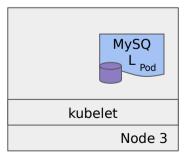


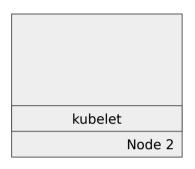


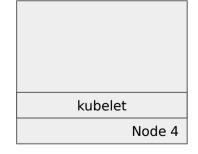


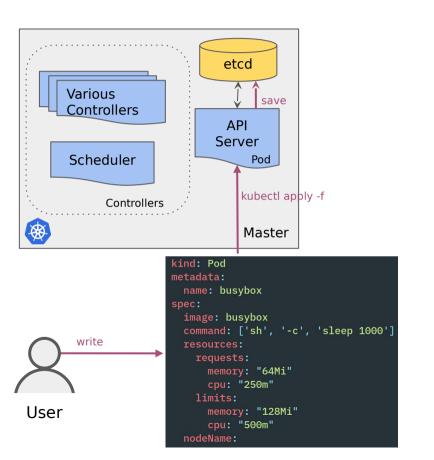


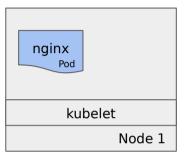


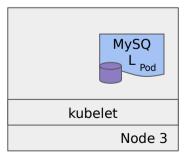


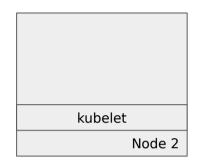


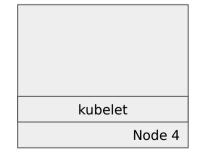


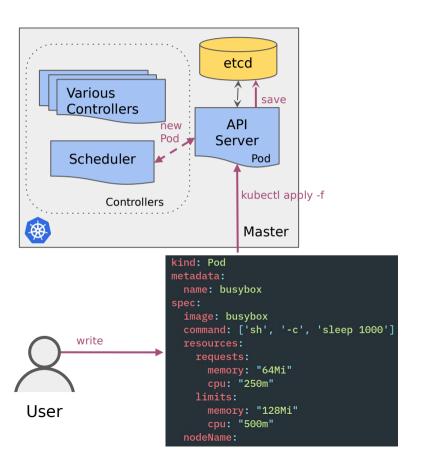


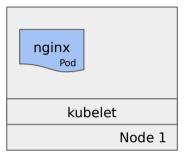


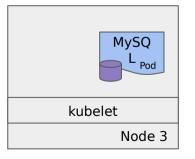


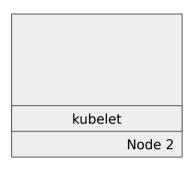




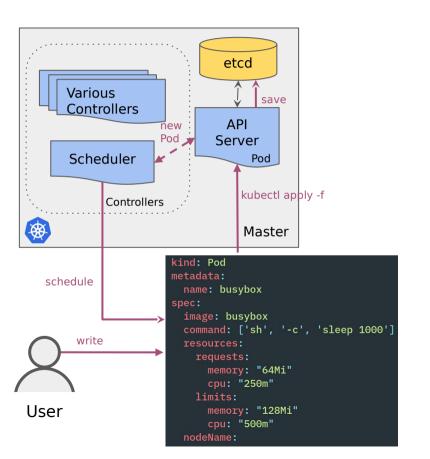


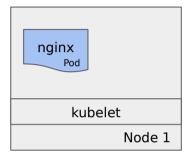


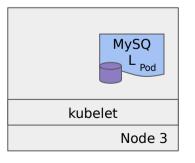


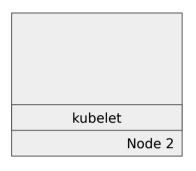


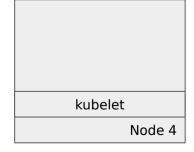


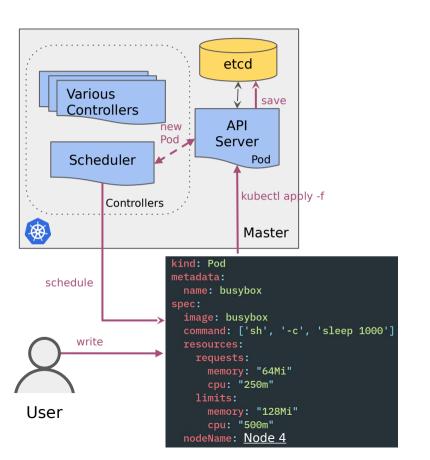


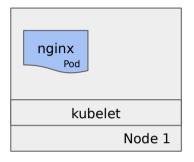


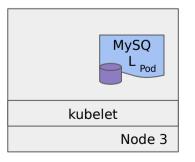


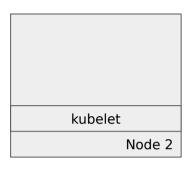


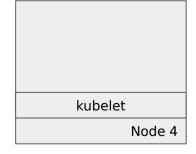


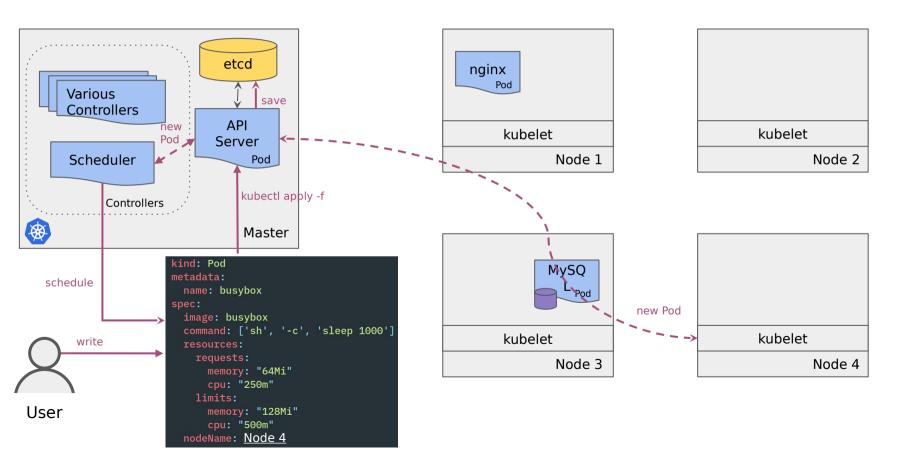


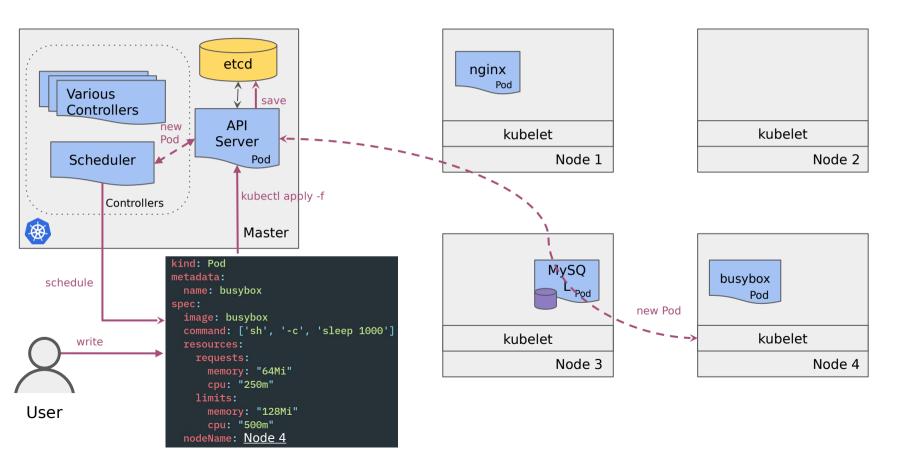


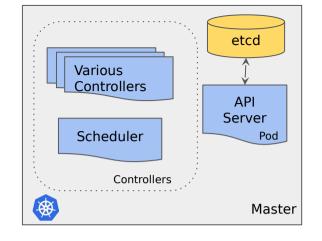


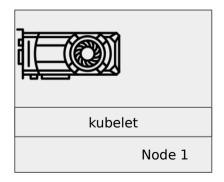




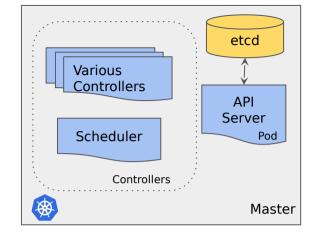


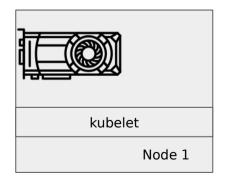




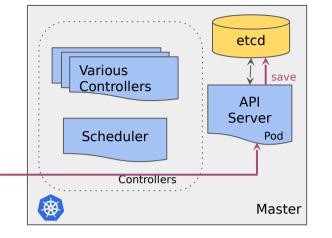




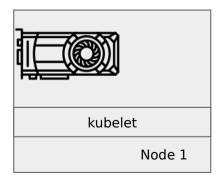




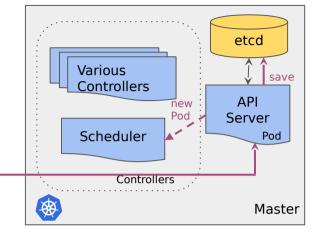




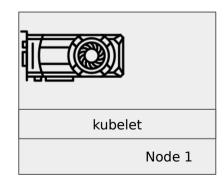
kubectl apply -f

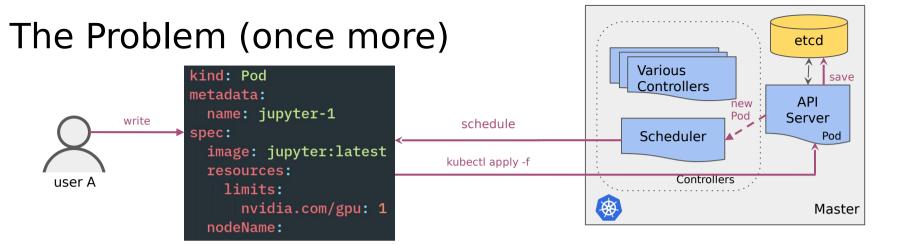


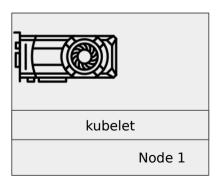


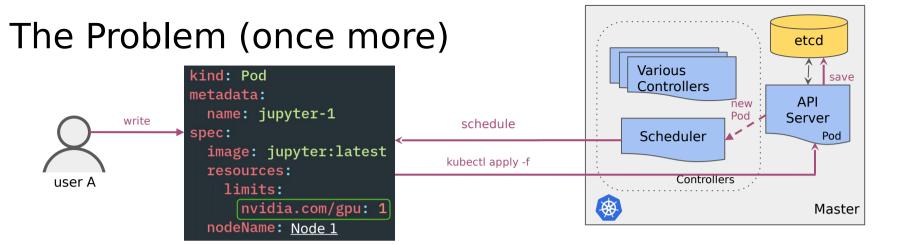


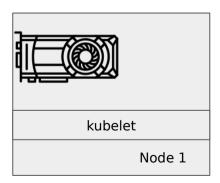
kubectl apply -f

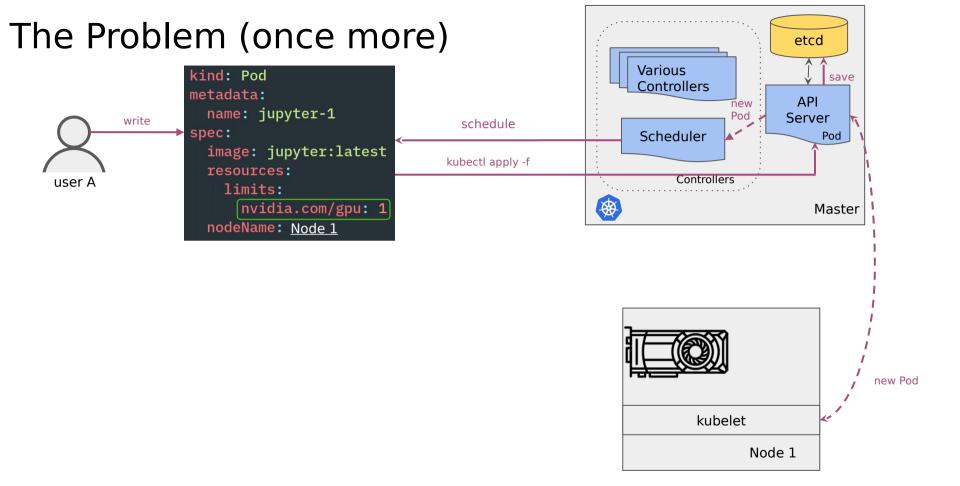


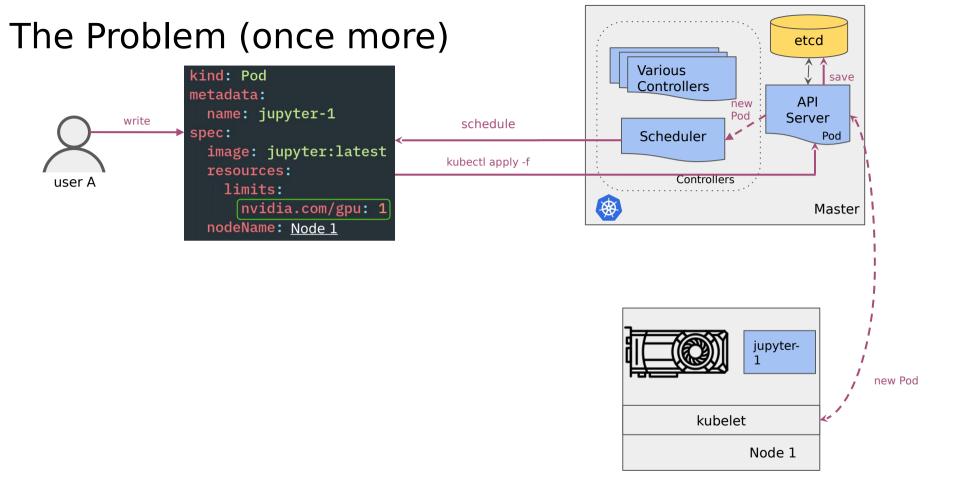


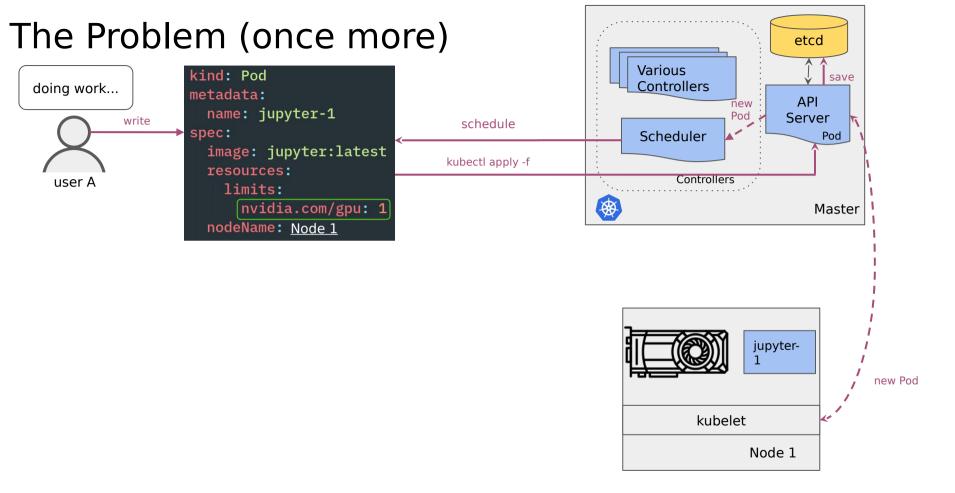


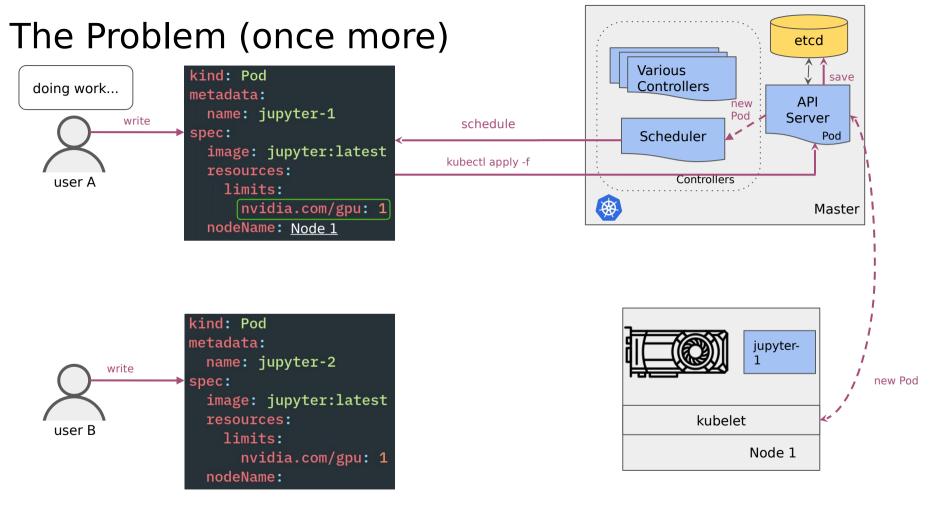


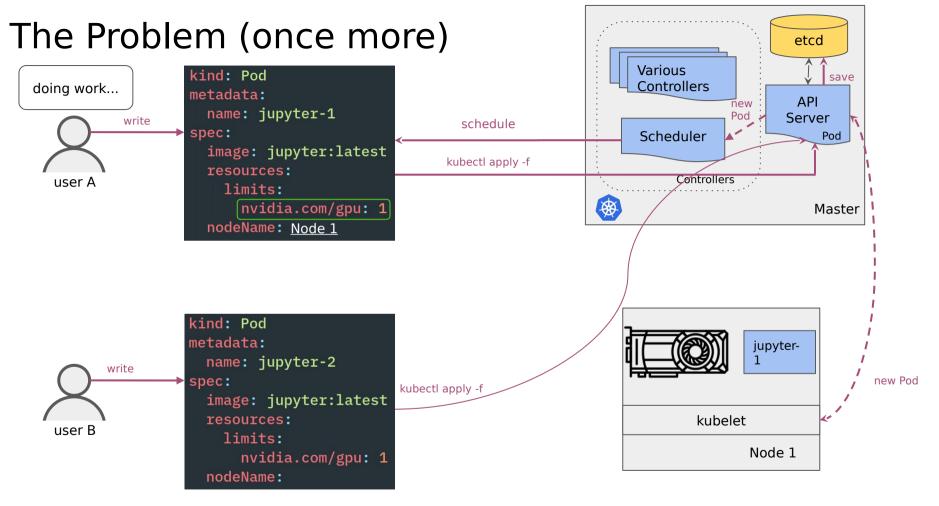


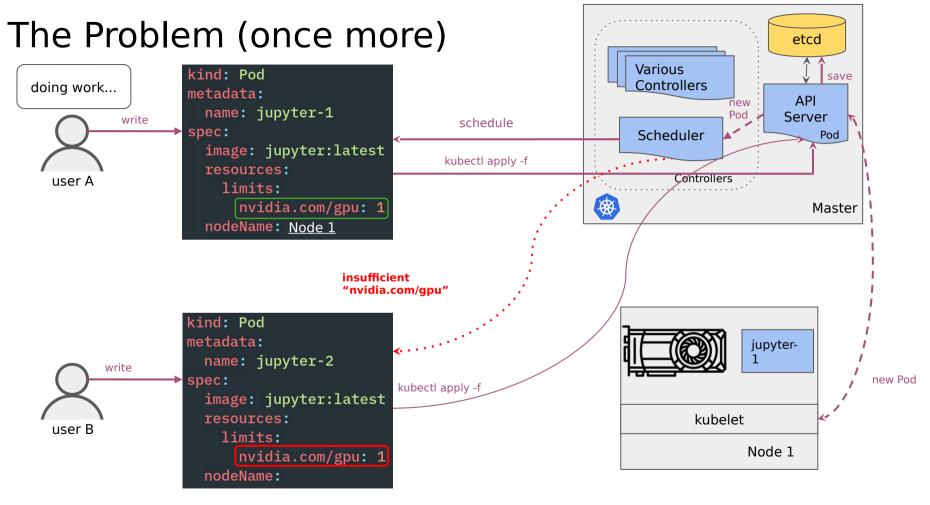


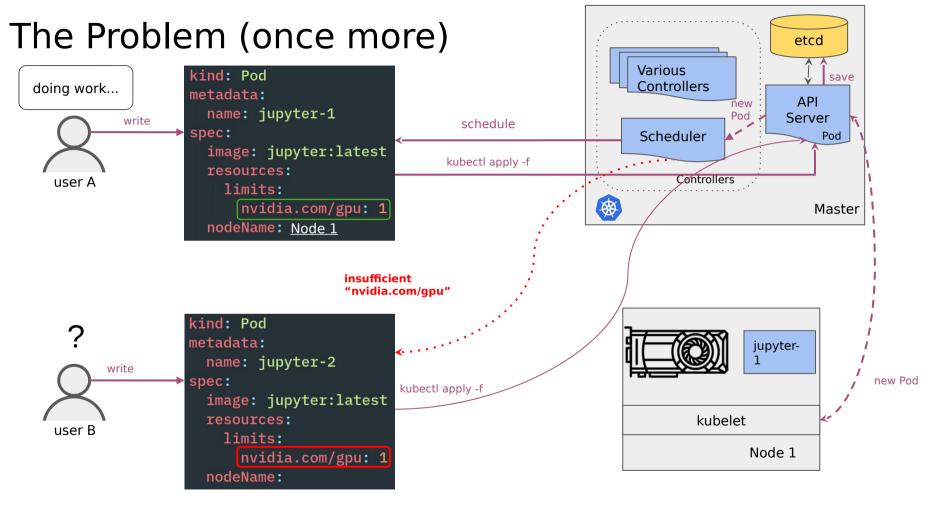


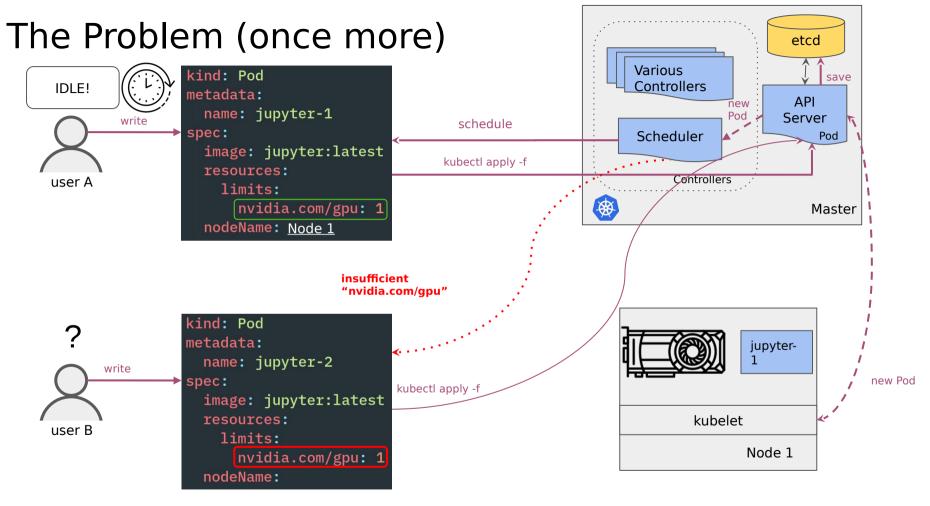


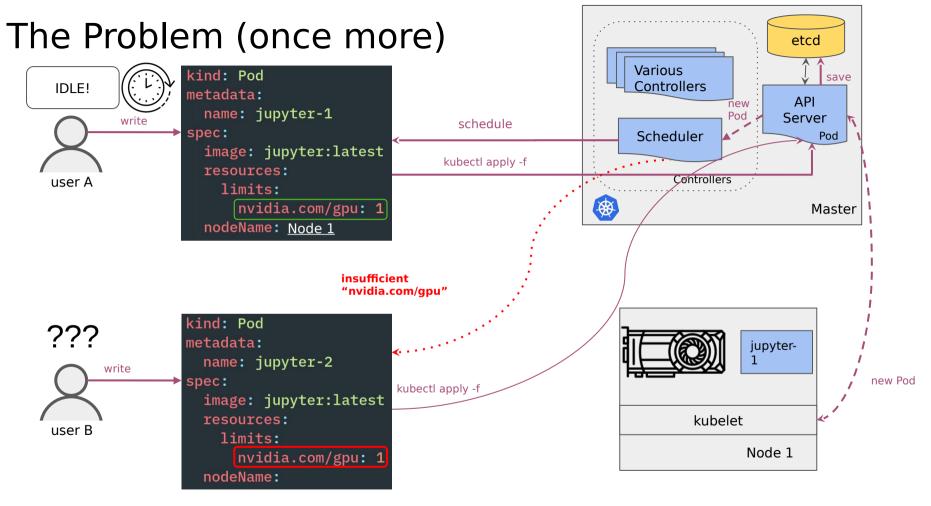




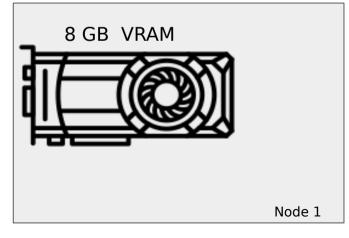






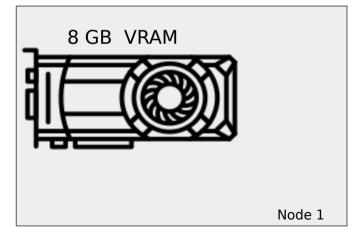


Scheduler

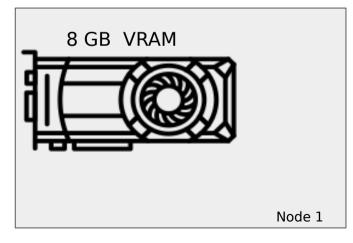


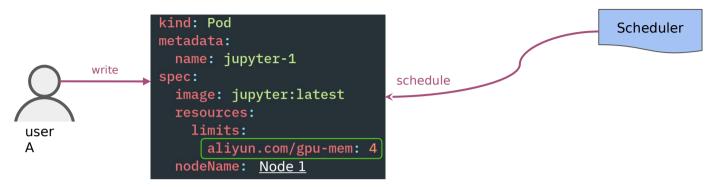


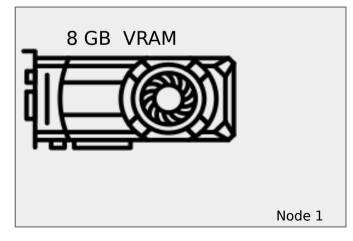
Scheduler

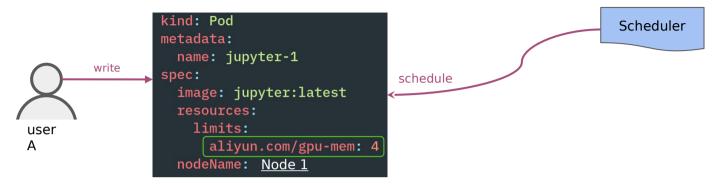


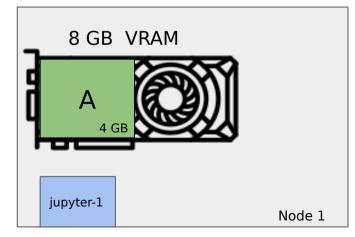


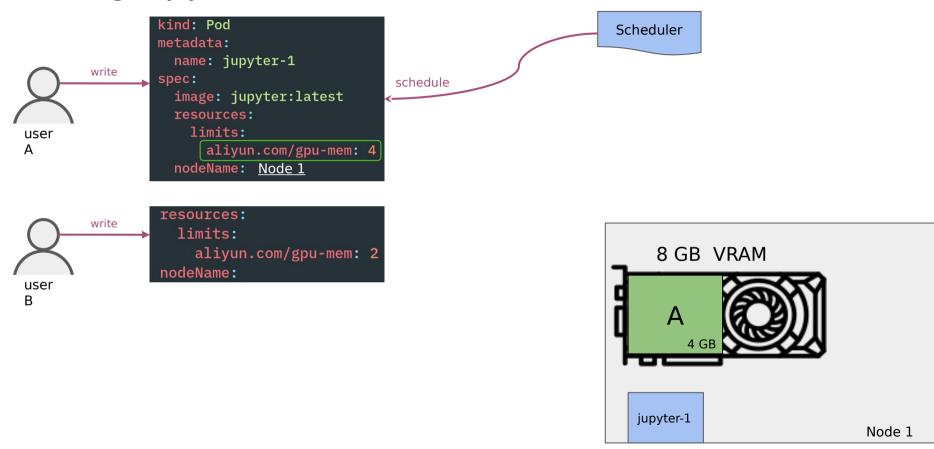


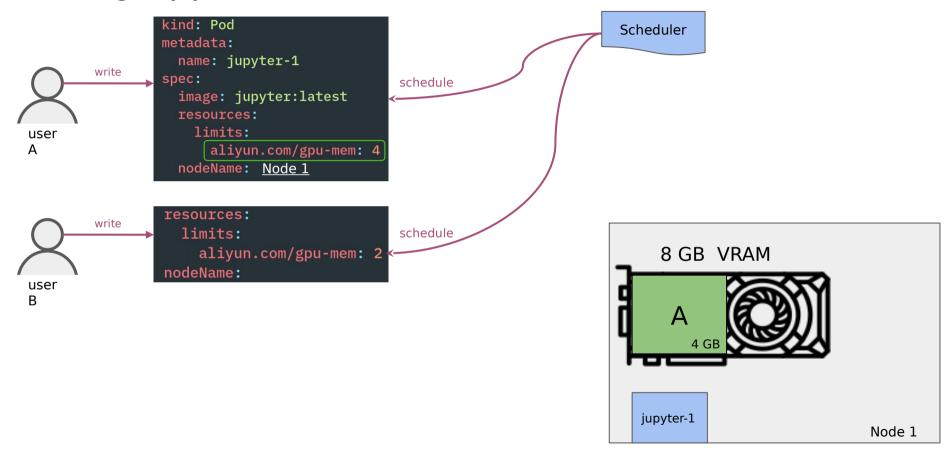


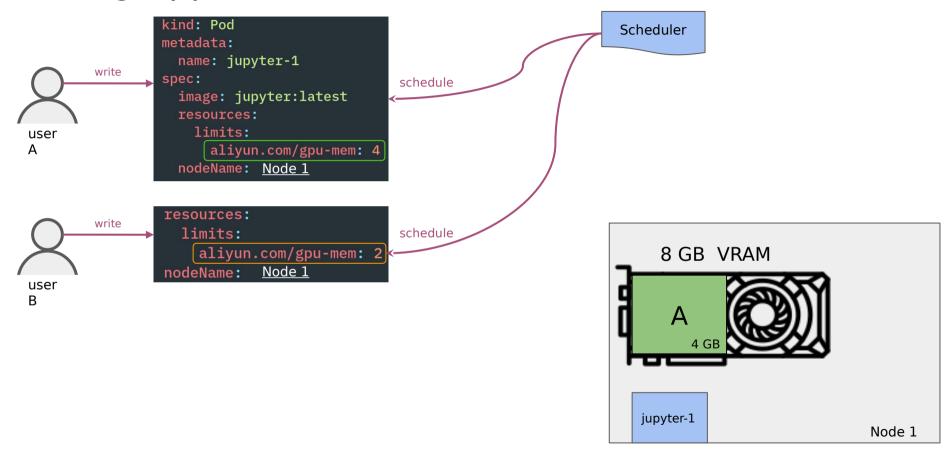


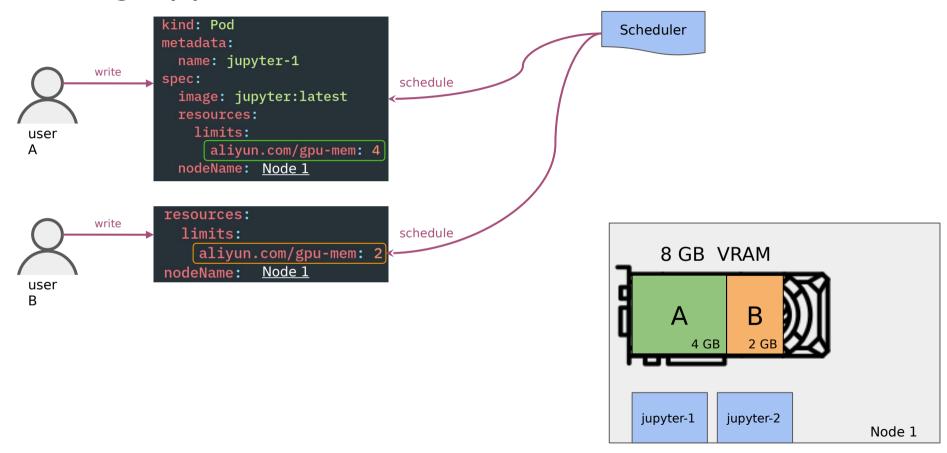


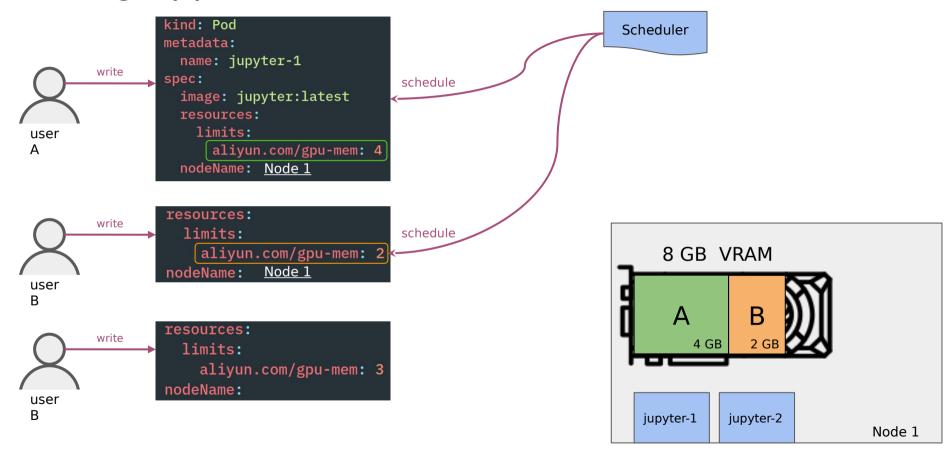


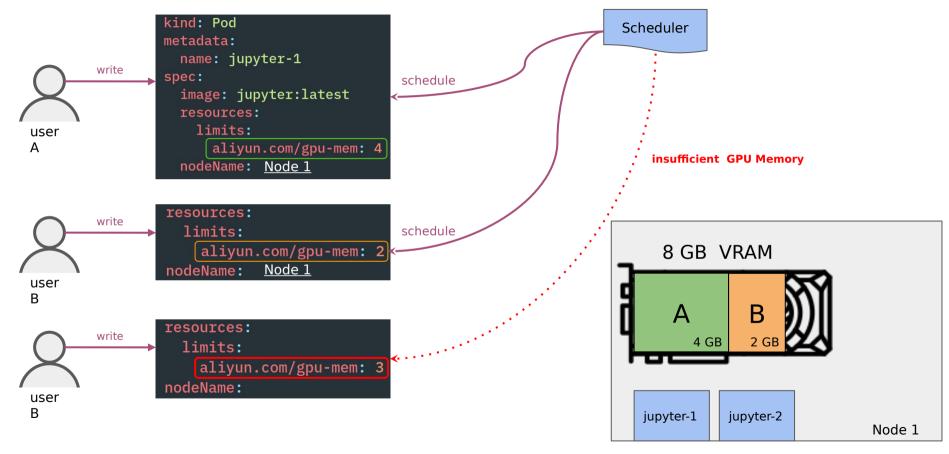


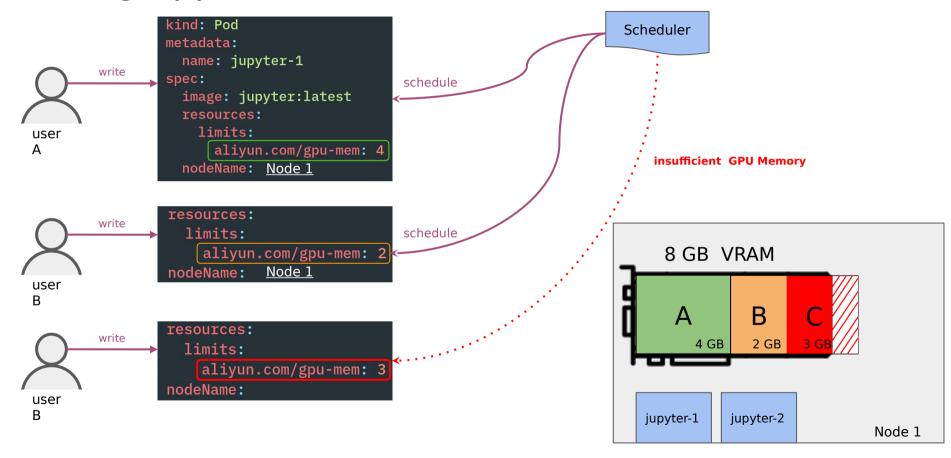












(-) Alibaba Cloud

- Aliyun Scheduler Extender
 - GPUs no longer exclusively assigned to Pods
 - users specify GPU memory requests
 - uses GPU memory to bin pack Pods in Kubernetes
 - does not enforce memory limits (only used for scheduling)

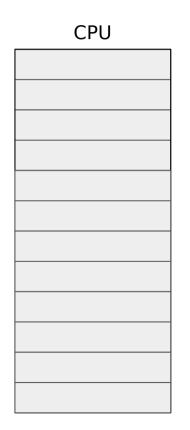
Kubeshare (HPDC '20)

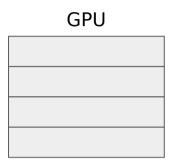
- similar to Aliyun w.r.t. the scheduling part
- enforces GPU memory limits (assigns a memory slice to each process)
- wraps CUDA API to keep track of how much memory each process allocates



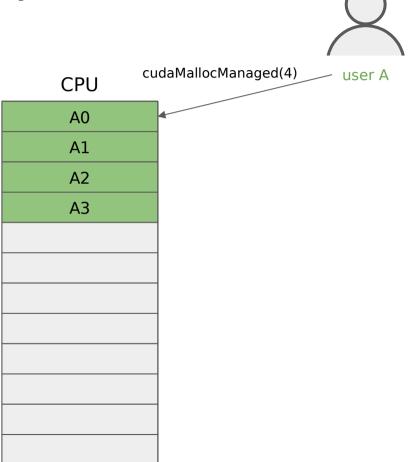
Memory





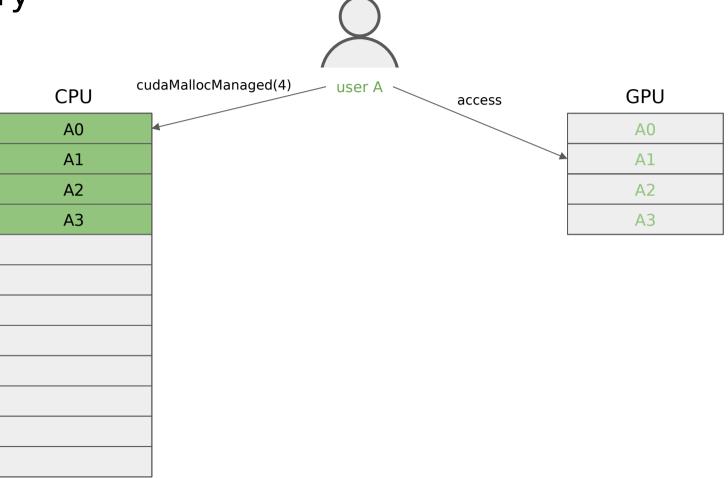


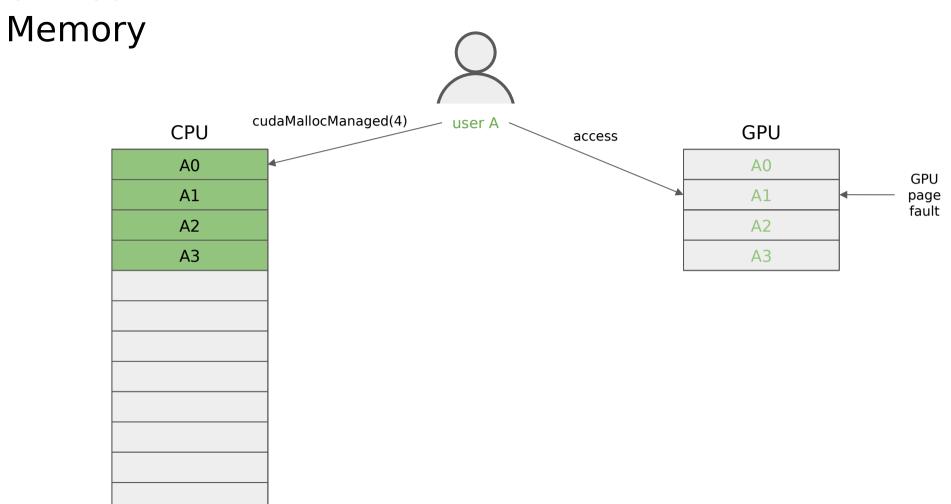
Memory



GPU

A0 A1 A2 A3 Memory





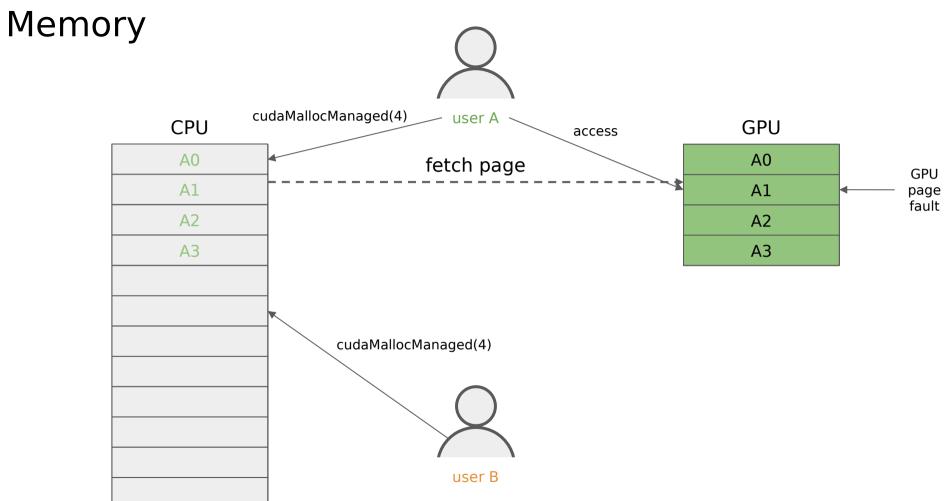
Memory cudaMallocManaged(4) user A CPU **GPU** access Α0 A0 fetch page GPU A1 Α1 page fault A2 A2 **A3 A3**

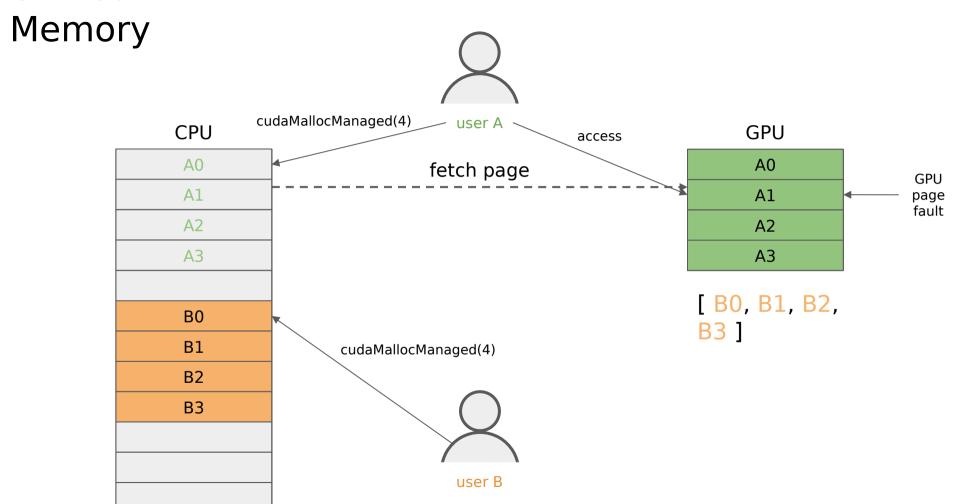
Memory cudaMallocManaged(4) user A CPU **GPU** access A0 Α0 fetch page GPU A1 Α1 page fault A2 A2 А3 **A3**

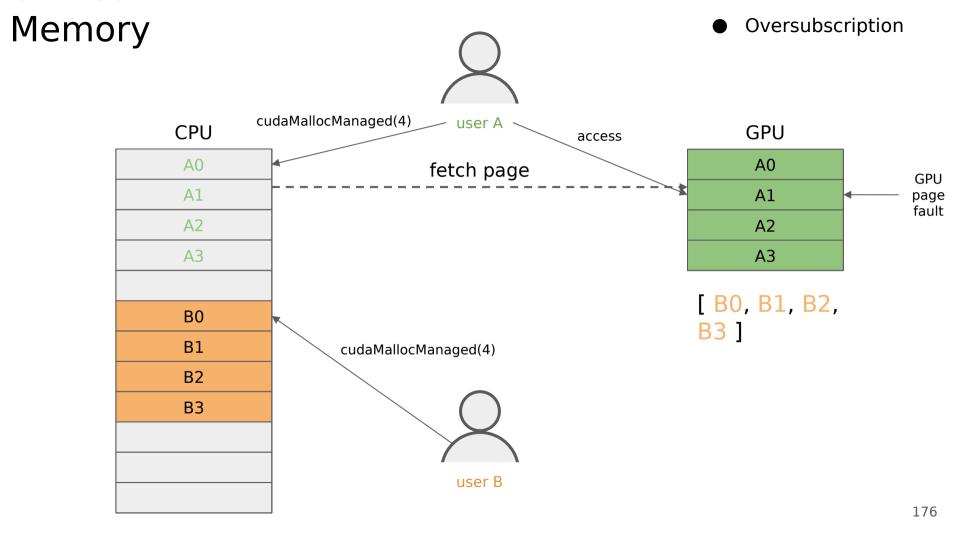
Memory cudaMallocManaged(4) user A CPU **GPU** access Α0 A0 fetch page GPU A1 Α1 page fault A2 A2 А3 **A3**

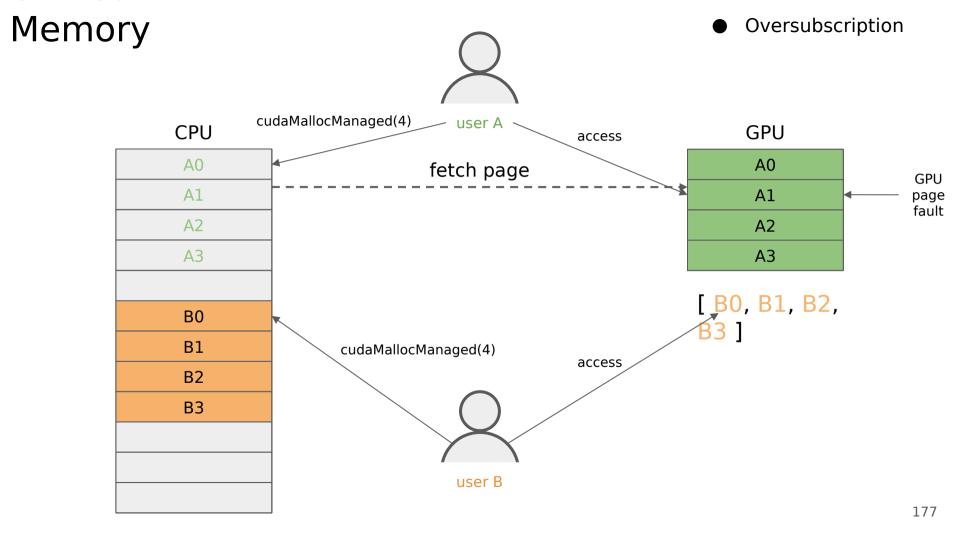
Memory cudaMallocManaged(4) user A CPU **GPU** access Α0 A0 fetch page GPU A1 Α1 page fault A2 A2 **A3** А3

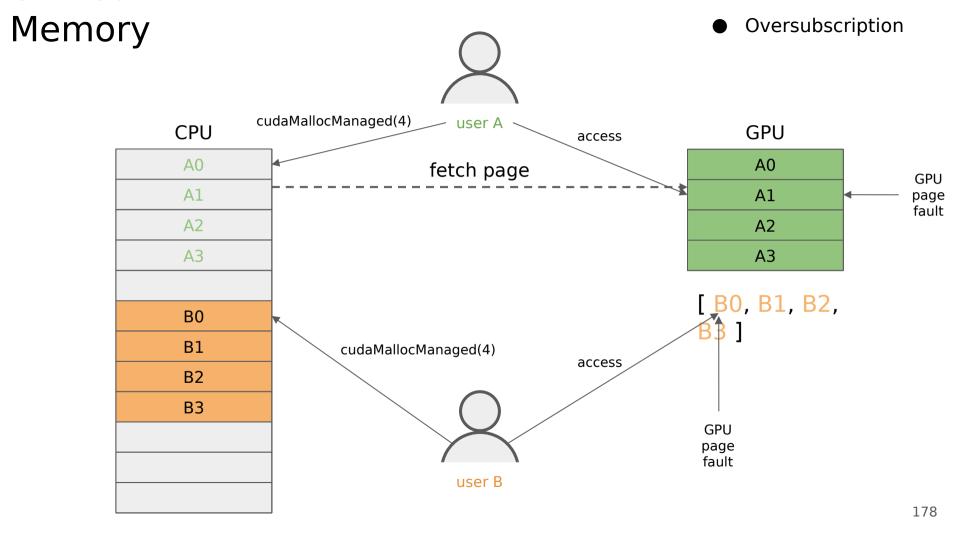
Memory cudaMallocManaged(4) user A CPU **GPU** access A0 Α0 fetch page GPU A1 Α1 page fault A2 A2 **A3** А3 user B

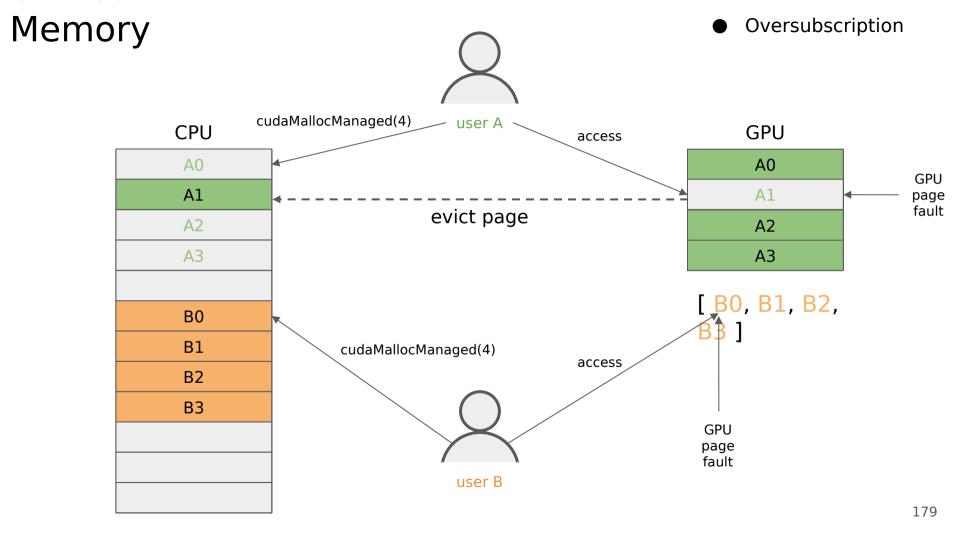


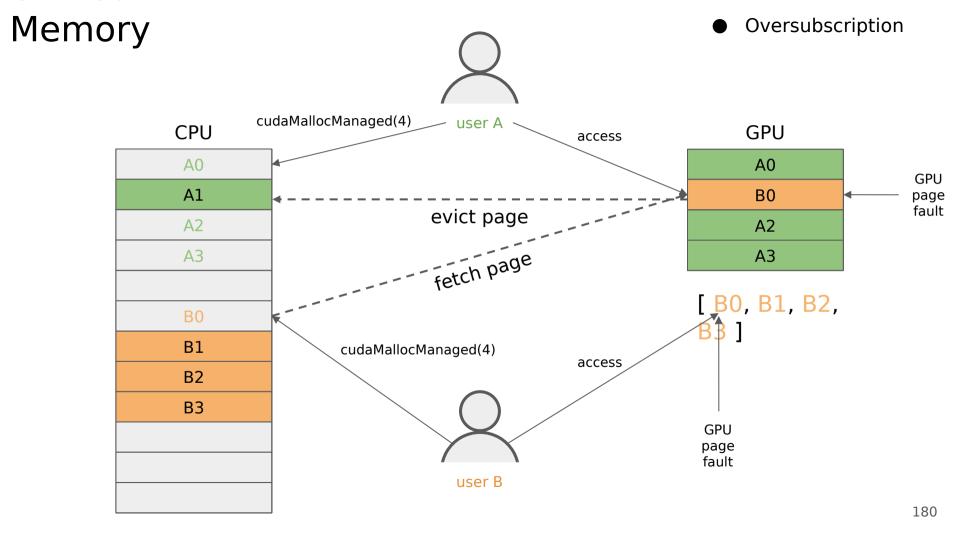


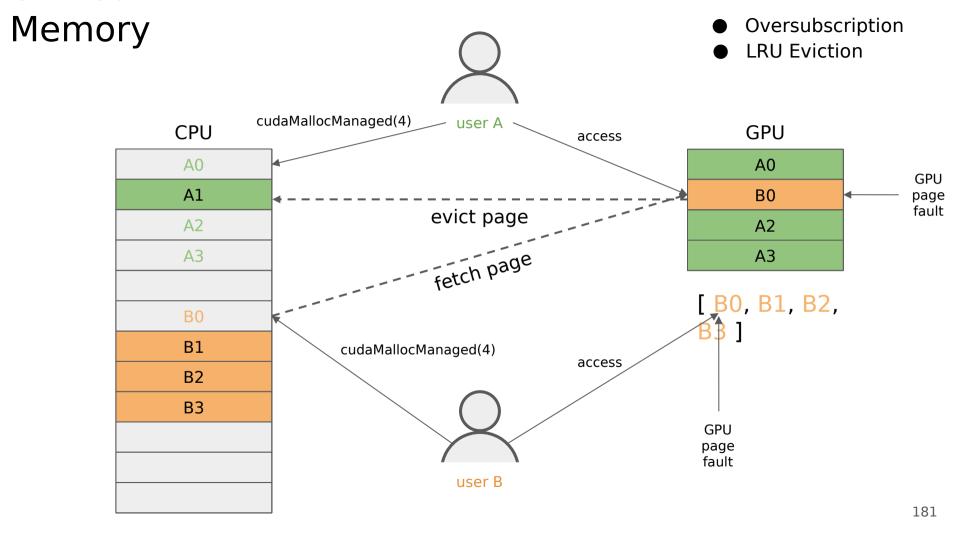












 execve() loads executable code into memory and passes control to ld.so

- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols

- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

\$ nm -D ./matrixMul

U cudaLaunchKernel U cudaMalloc U cudaMemcpy

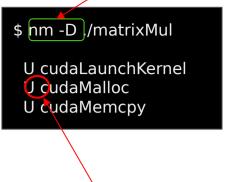
- execve() loads executable code into memory and passes control to Id.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

print dynamic symbol table entries



- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

print dynamic symbol table entries

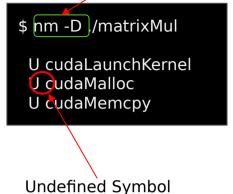


Undefined Symbol

188

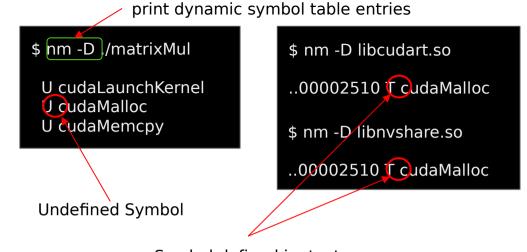
- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

print dynamic symbol table entries

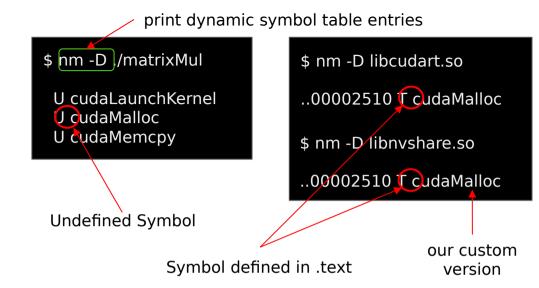


\$ nm -D libcudart.so
..00002510 T cudaMalloc
\$ nm -D libnvshare.so
..00002510 T cudaMalloc

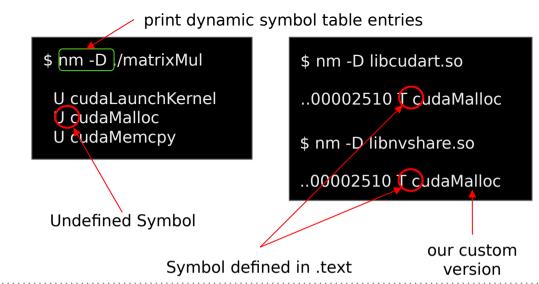
- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution



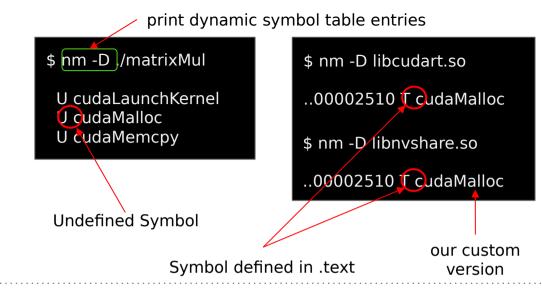
- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution



- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

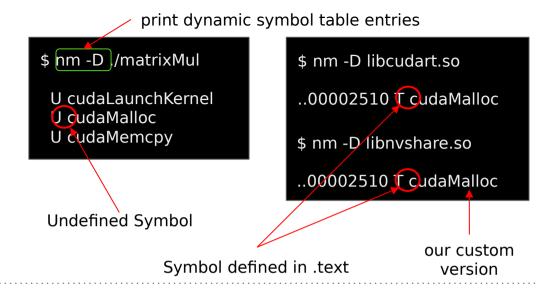


- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution



Normally:

- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

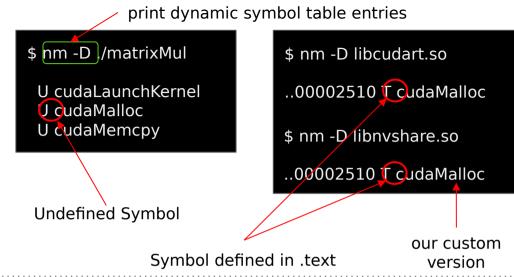


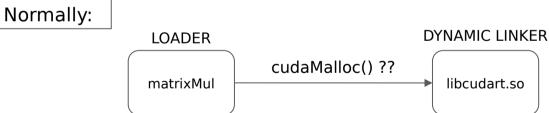
Normally:

LOADER

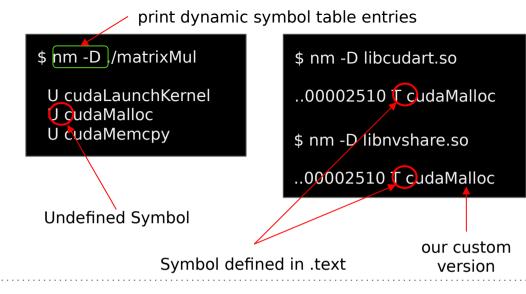
matrixMul

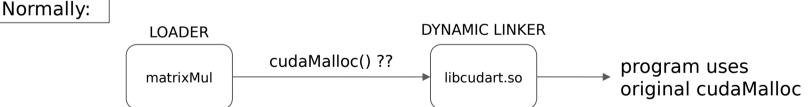
- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution



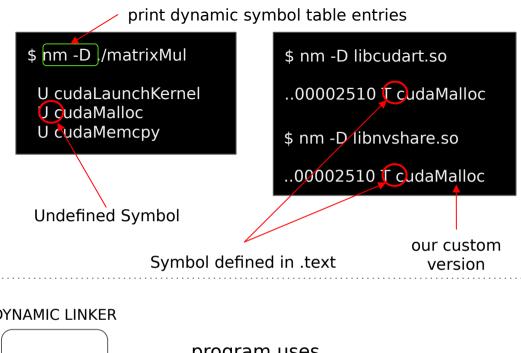


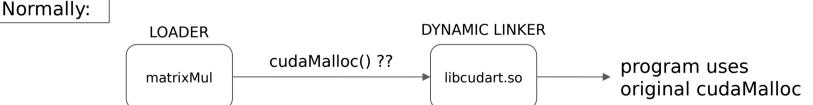
- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution



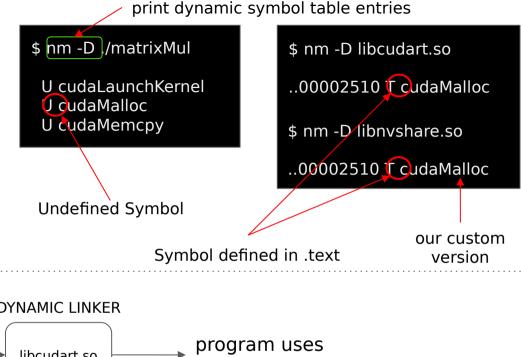


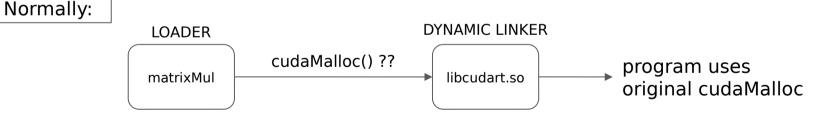
- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution





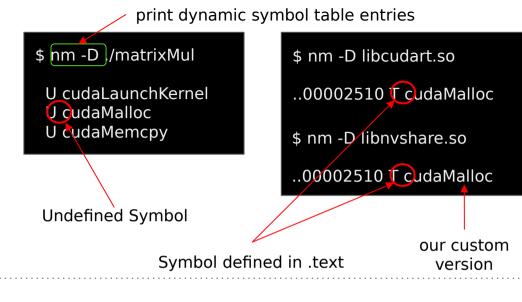
- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

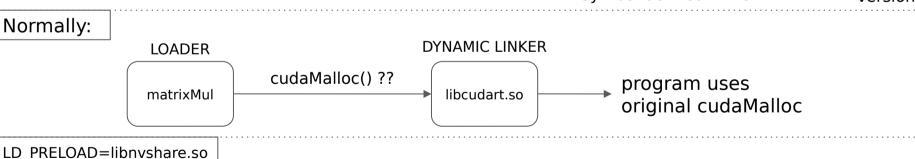




LD_PRELOAD=libnvshare.so

- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution

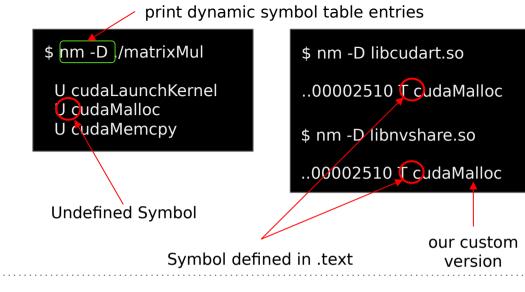


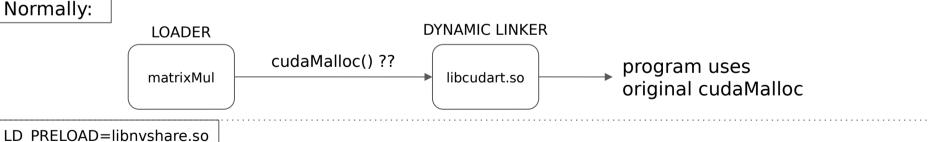


LOADER

matrixMul

- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution





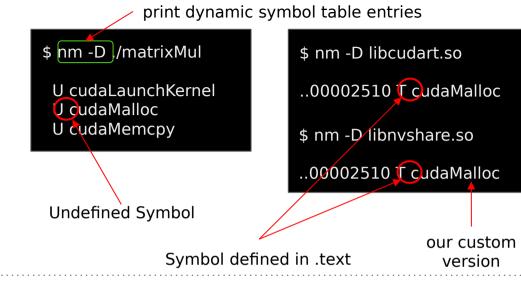
LOADER

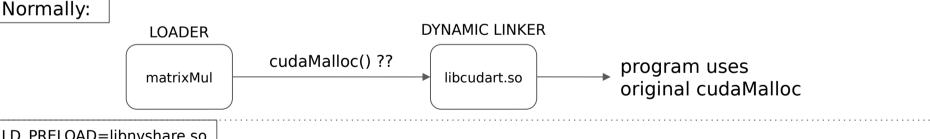
DYNAMIC LINKER

cudaMalloc() ??

libnvshare.
so

- execve() loads executable code into memory and passes control to ld.so
- Id.so maps shared objects to program address space and resolves symbols
- application begins execution





LD_PRELOAD=libnvshare.so

LOADER

DYNAMIC LINKER

matrixMul

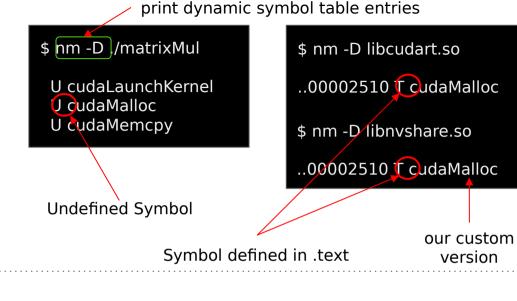
cudaMalloc() ??

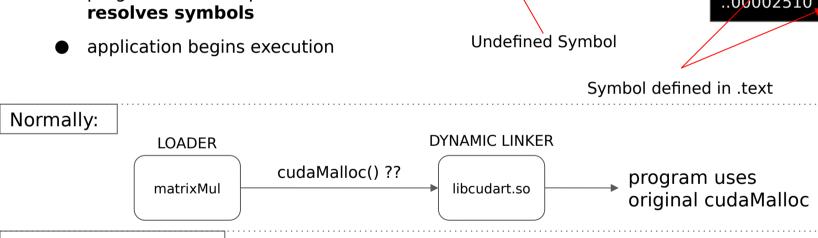
libnvshare.
so

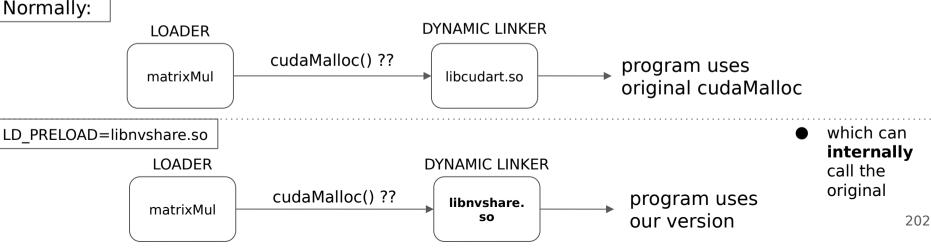
program uses
our version

201

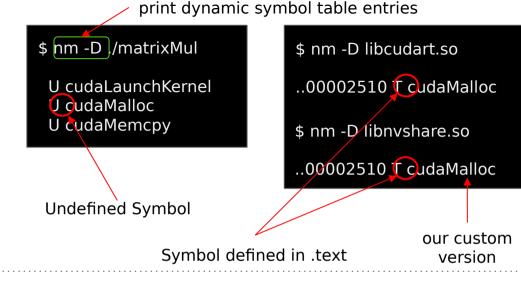
- execve() loads executable code into memory and passes control to *ld.so*
- *Id.so* maps shared objects to program address space and resolves symbols

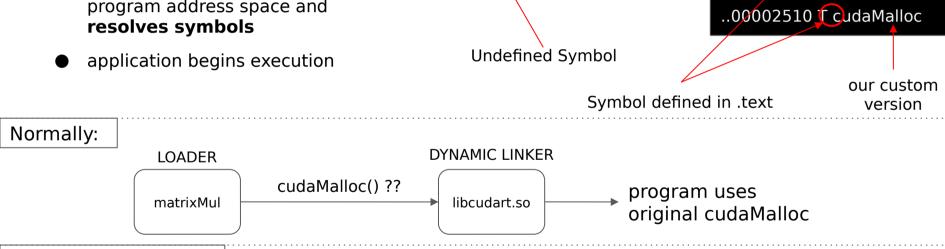


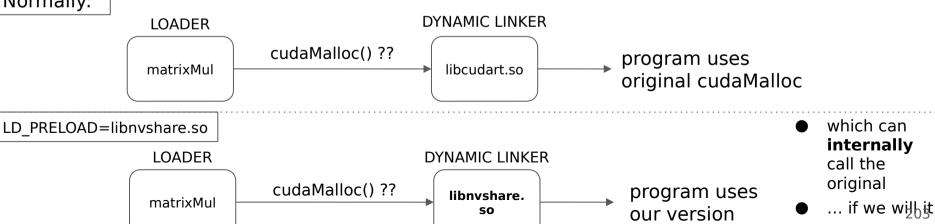




- execve() loads executable code into memory and passes control to *ld.so*
- *Id.so* maps shared objects to program address space and resolves symbols











user

libnvshare.so

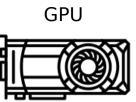


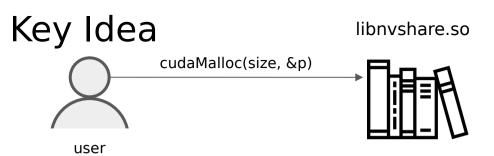


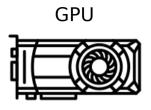
user

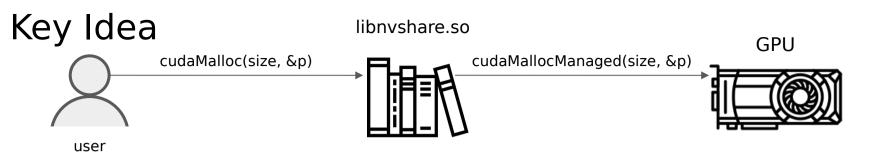
libnvshare.so

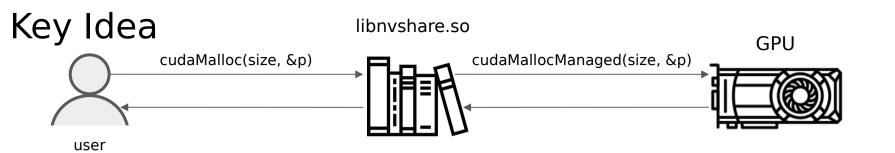


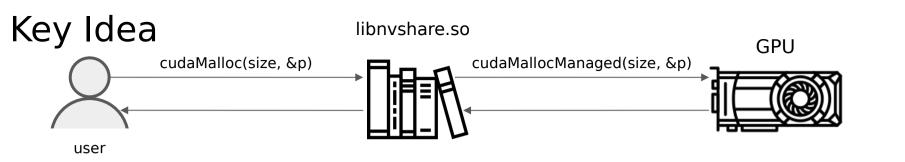


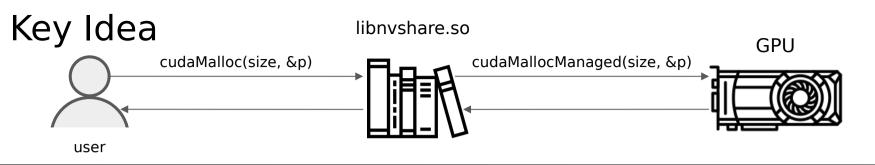




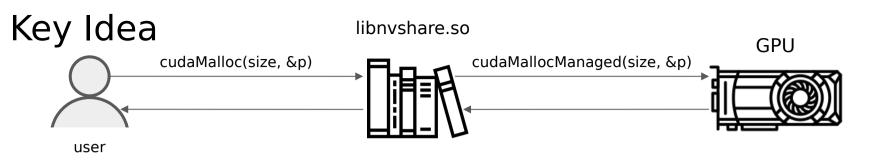














user A

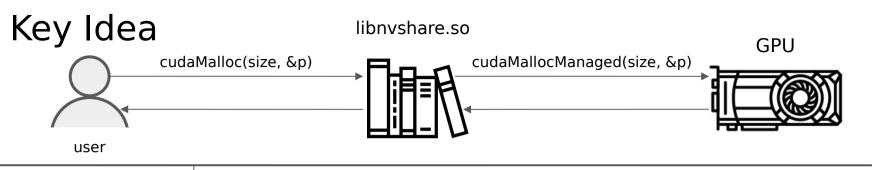
GPU

Α0

A1

A2

А3





user A

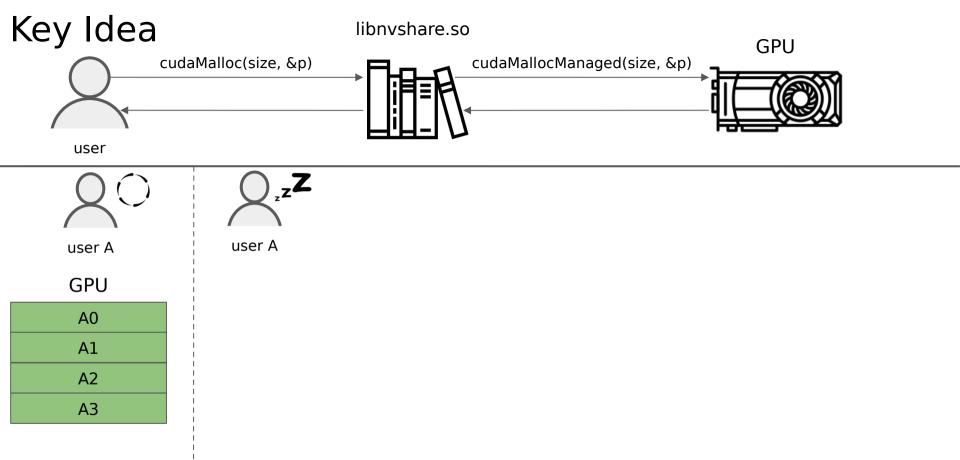
GPU

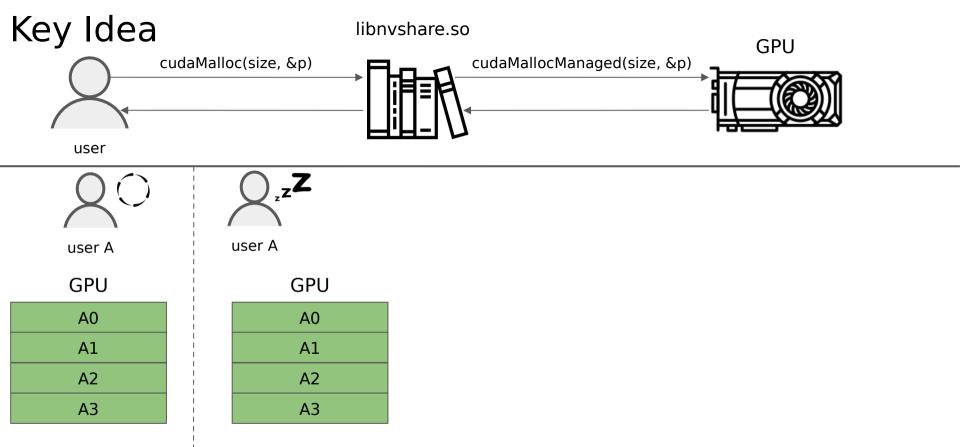
Α0

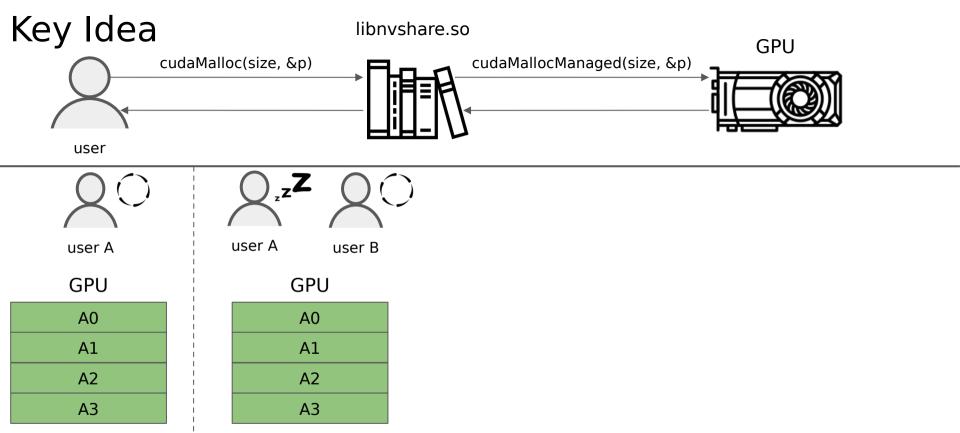
Α1

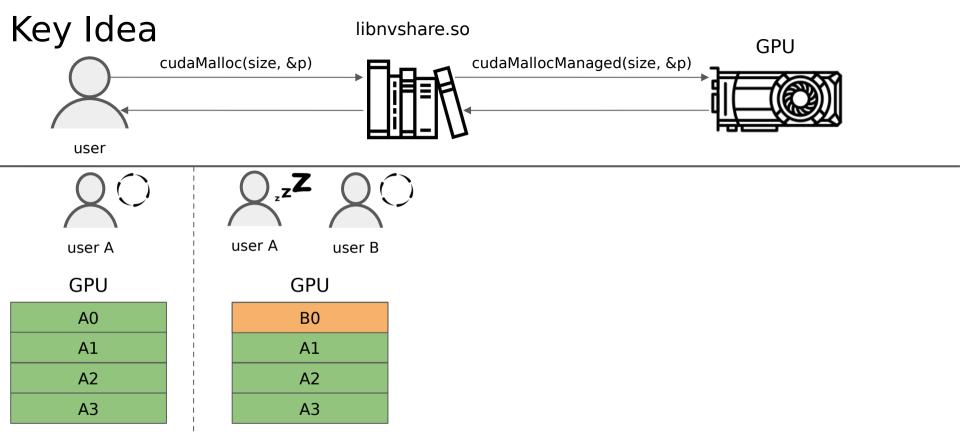
A2

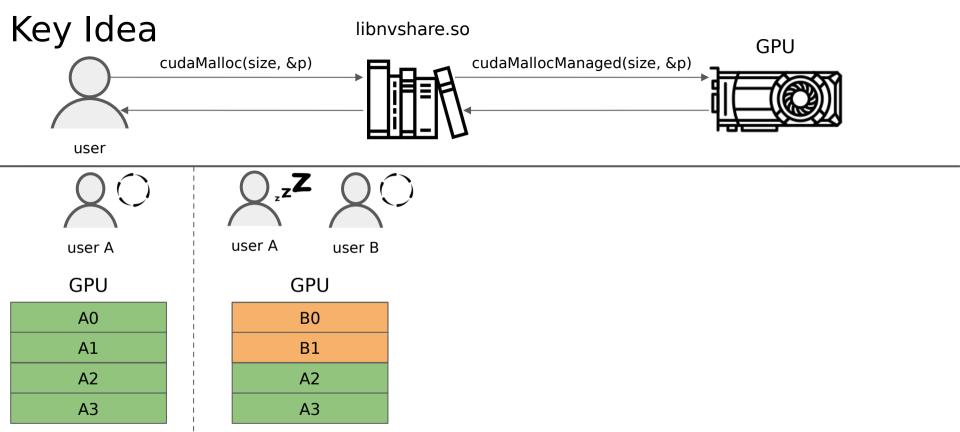
А3

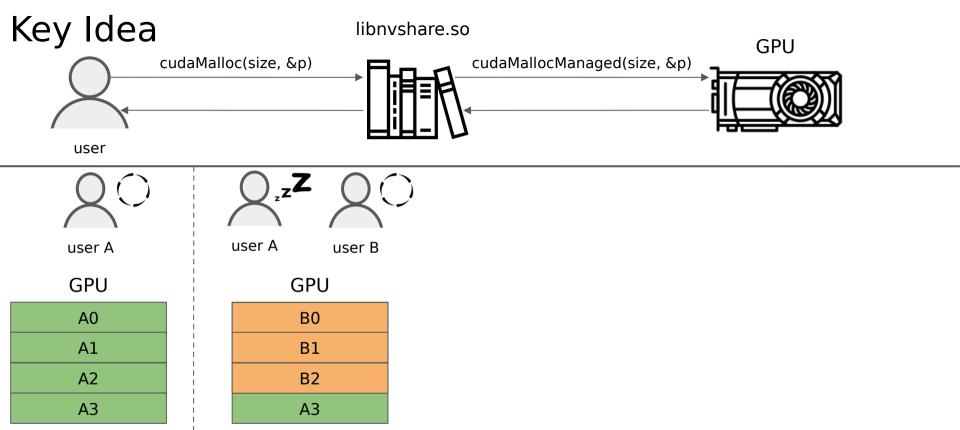


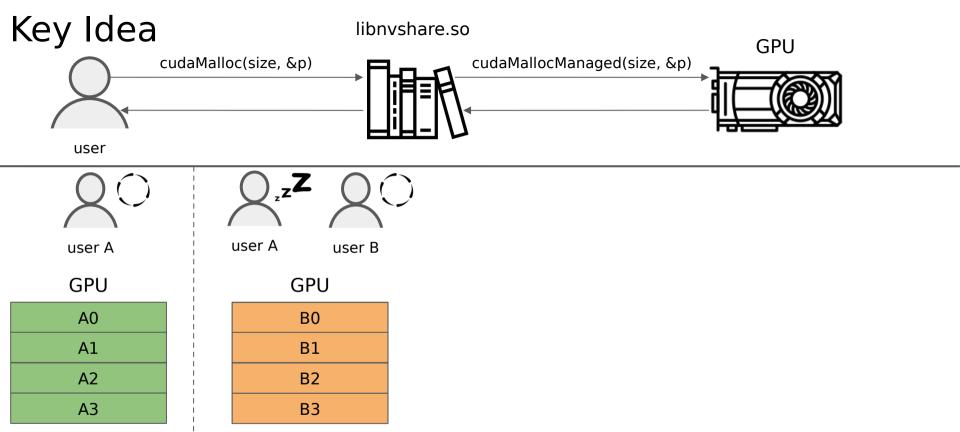


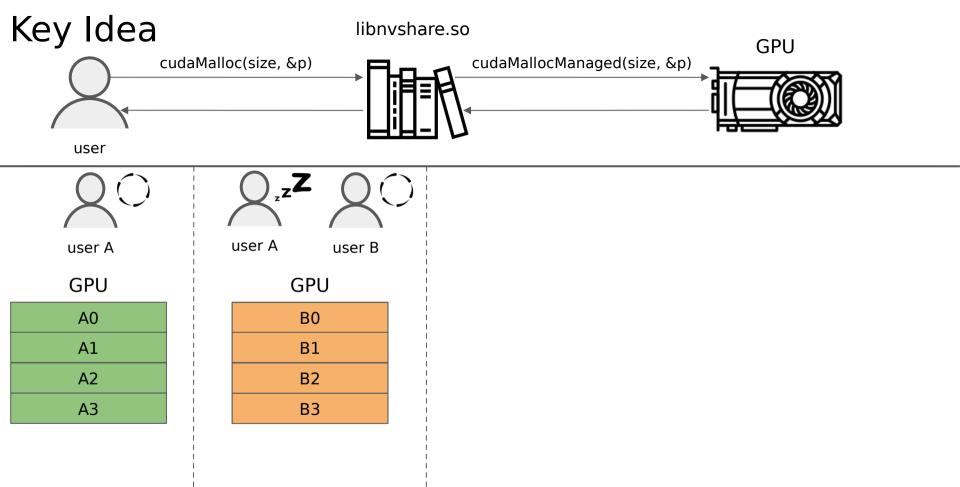


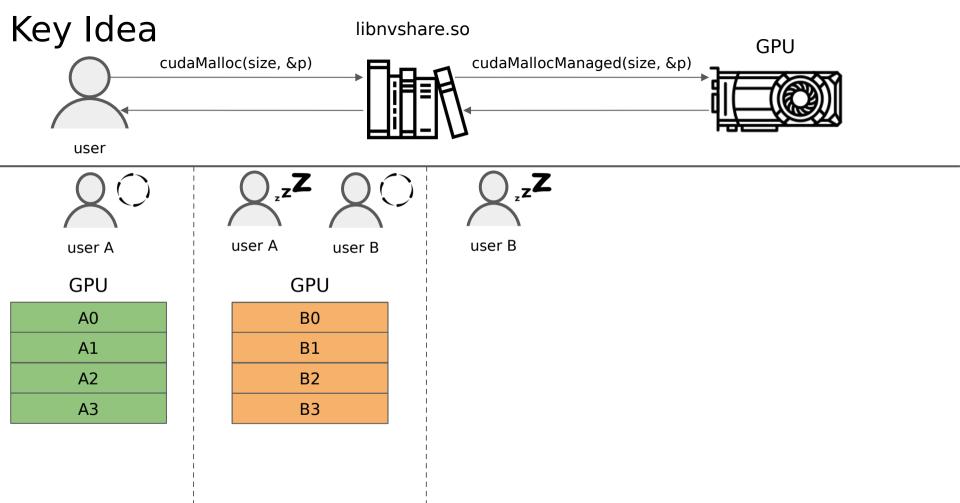


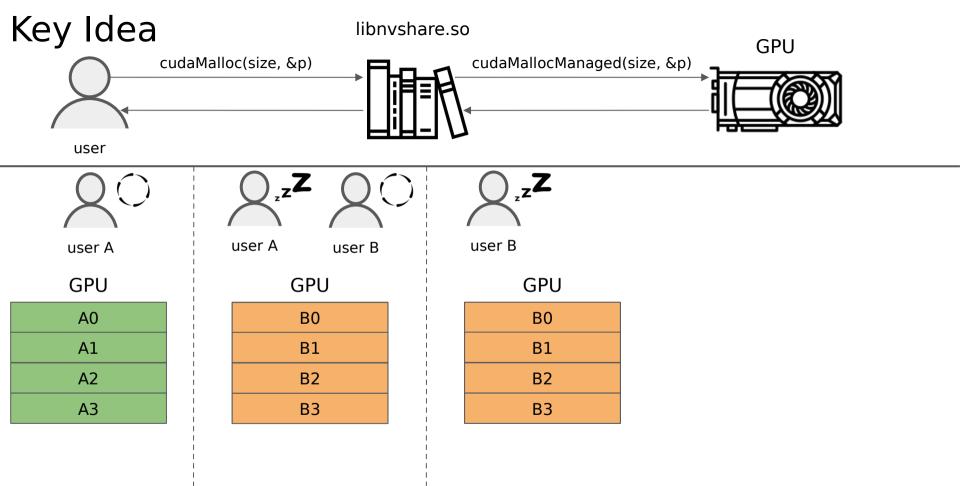


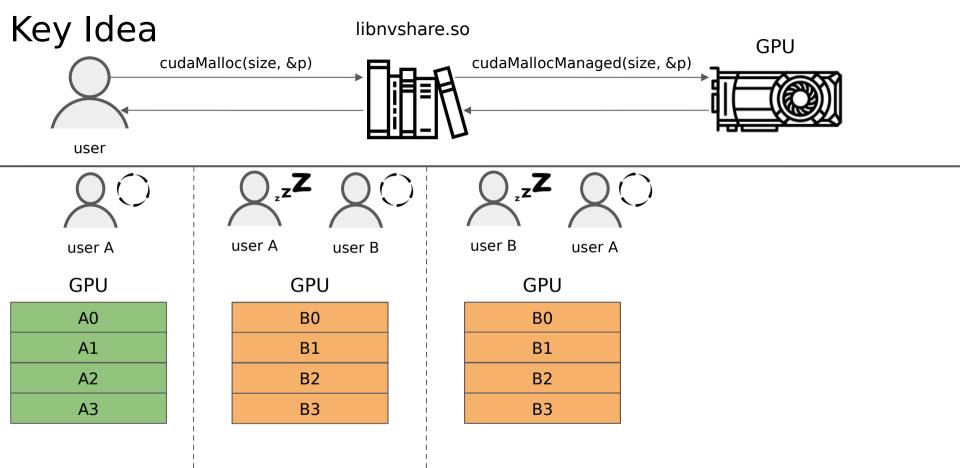


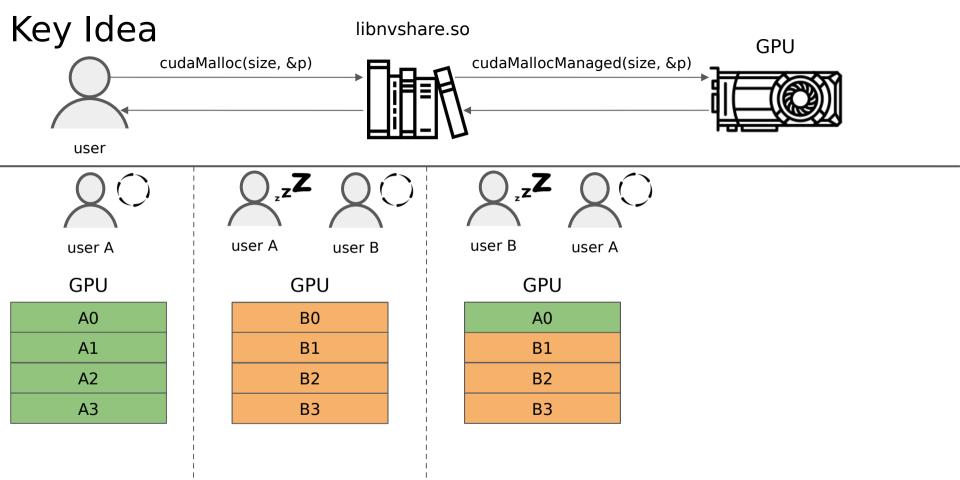


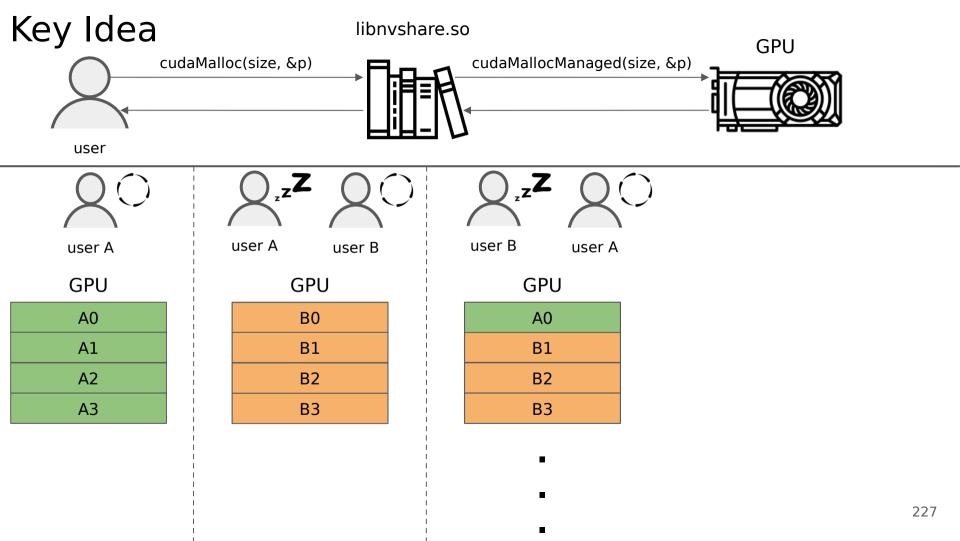


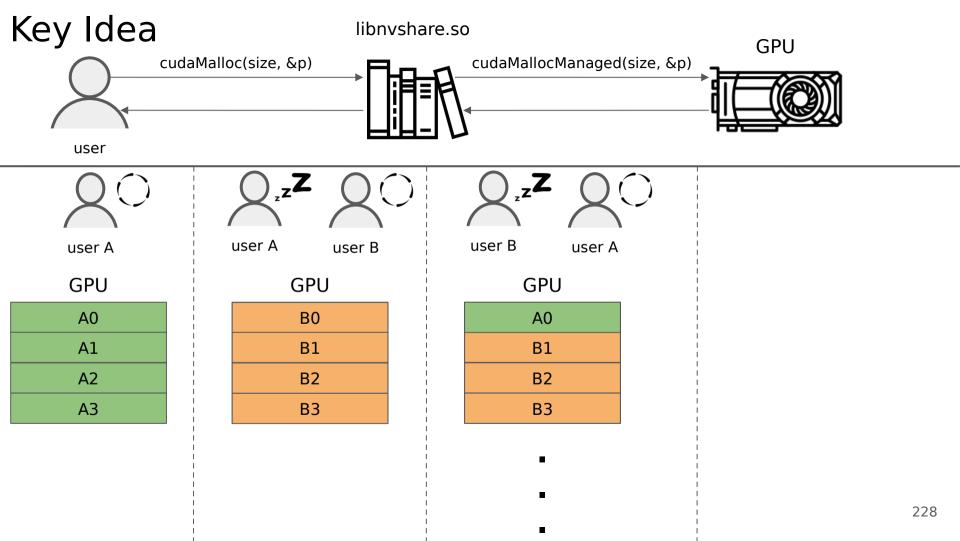


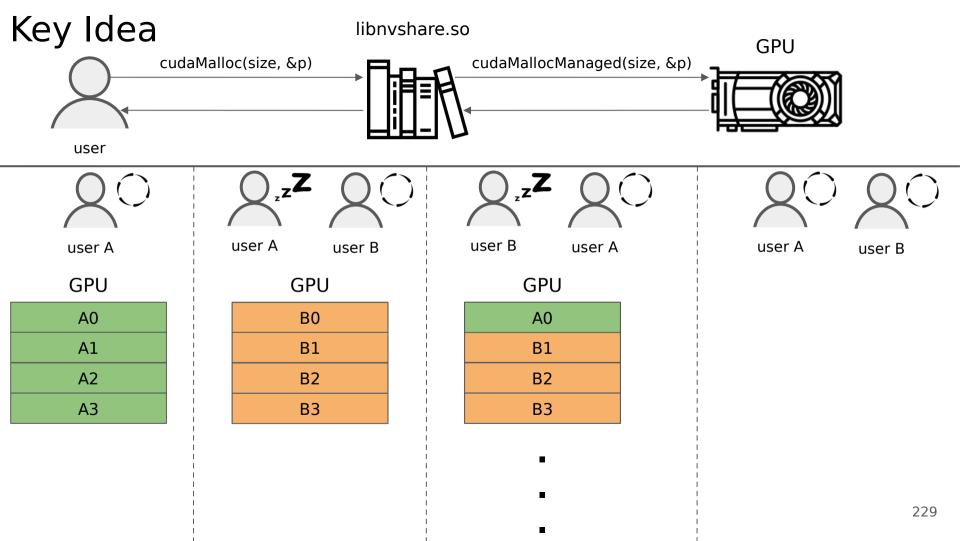


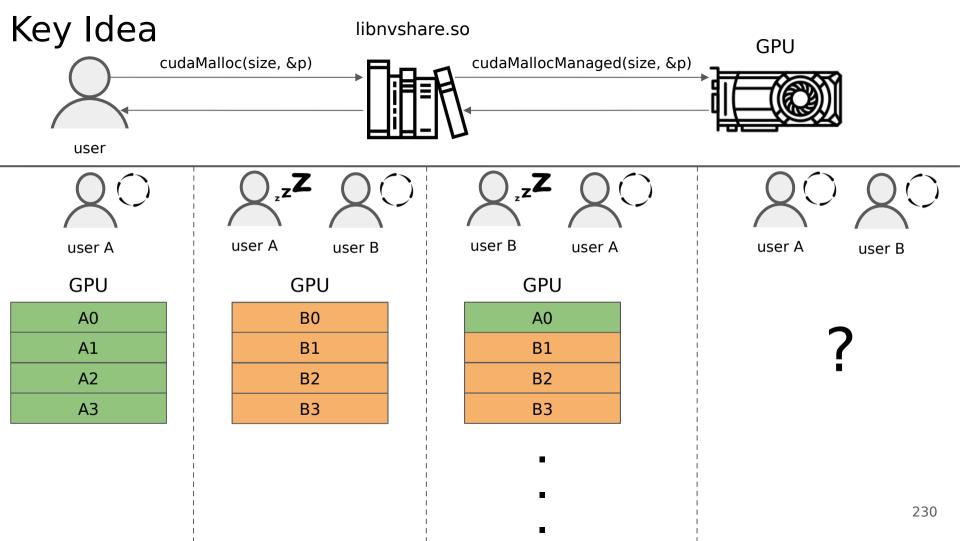




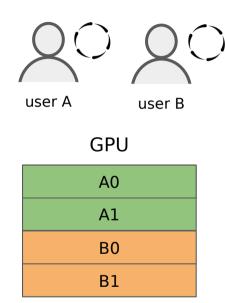


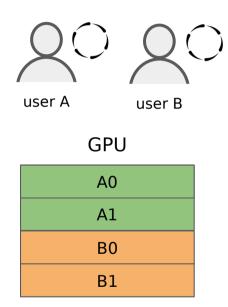






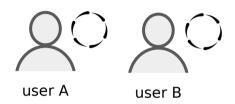






 Σ Wss_i< GPU memory

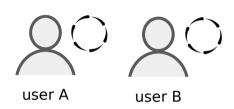
Sometimes



GPU

A0	
A1	
В0	
В1	

Sometimes



GPU

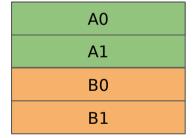
A0
A1
В0
B1

Sometimes

Other times

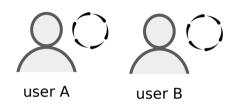


GPU

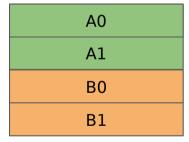


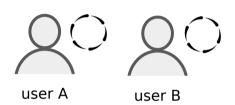
Sometimes

Other times









2020

user B

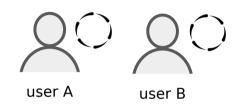
Sometimes

GPU

user A

A0 A1 B0 B1

 Σ Wss_i< GPU memory No page faults Other times

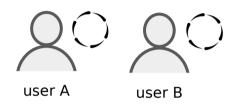


GPU

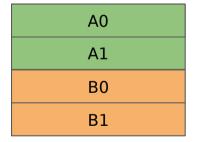
A0
В3
A2
B1

Sometimes

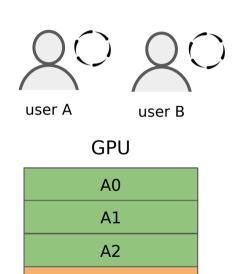
Other times



GPU



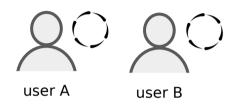
 Σ Wss_i< GPU memory No page faults



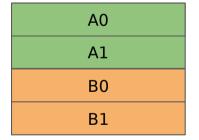
B1

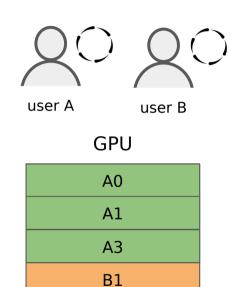
Sometimes

Other times



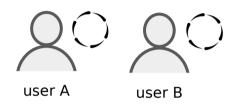
GPU



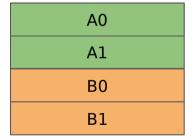


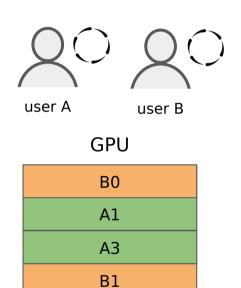
Sometimes

Other times



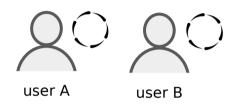
GPU



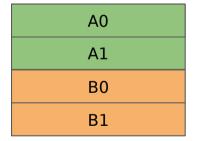


Sometimes

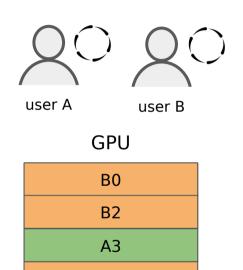
Other times



GPU



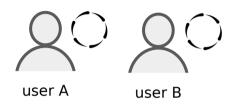
 Σ Wss_i< GPU memory No page faults



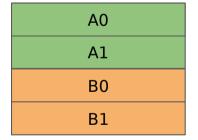
B1

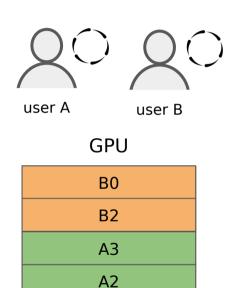
Sometimes

Other times



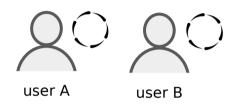
GPU





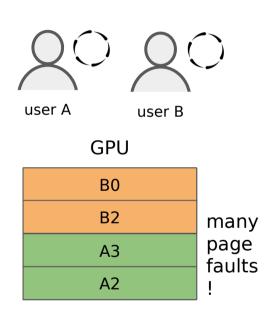
Sometimes

Other times



GPU





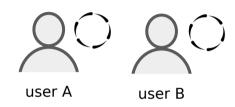
Sometimes

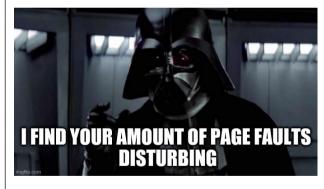
Other times



GPU

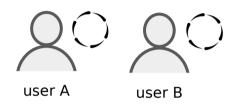
A0 A1 B0 B1



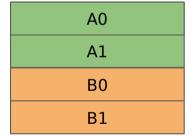


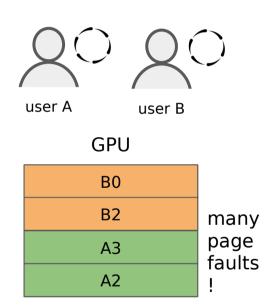
Sometimes

Other times



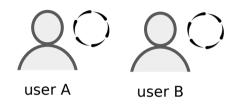
GPU



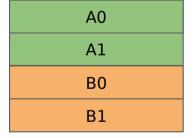


Sometimes

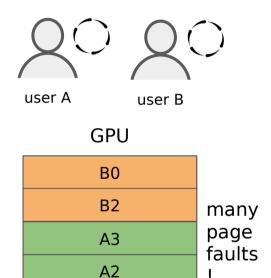
Other times



GPU



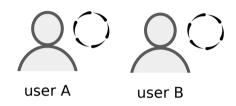
 Σ Wss_i< GPU memory No page faults



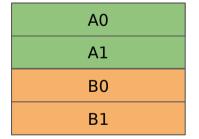
 $\Sigma Wss_i > GPU memory$

Sometimes

Other times



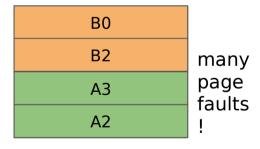
GPU



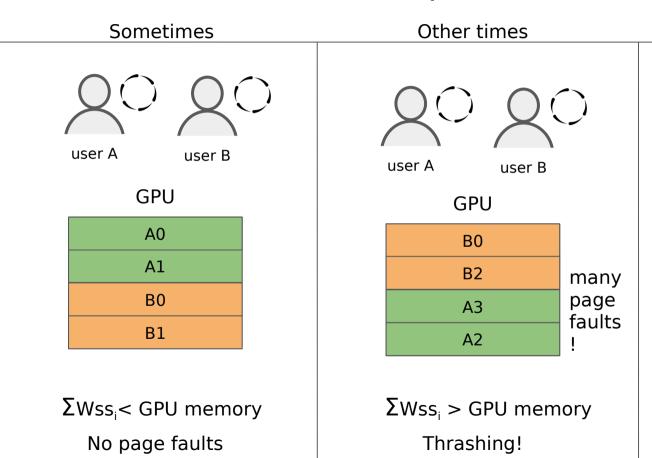
 Σ Wss_i< GPU memory No page faults

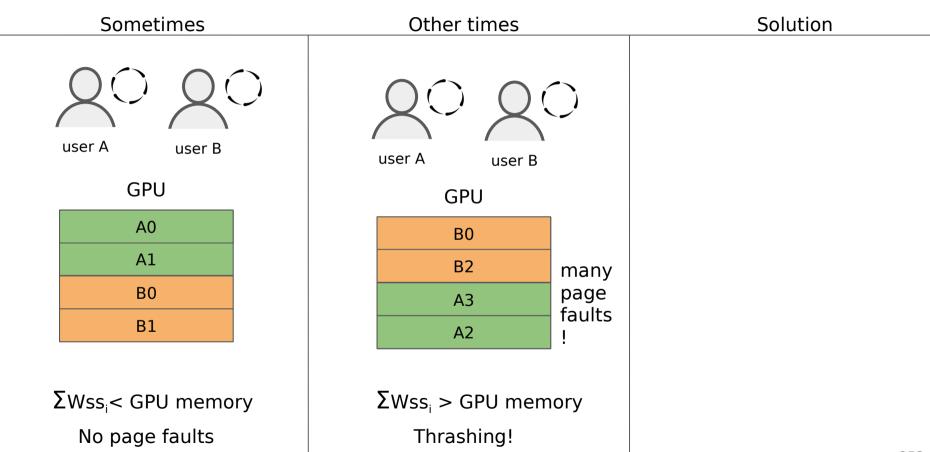


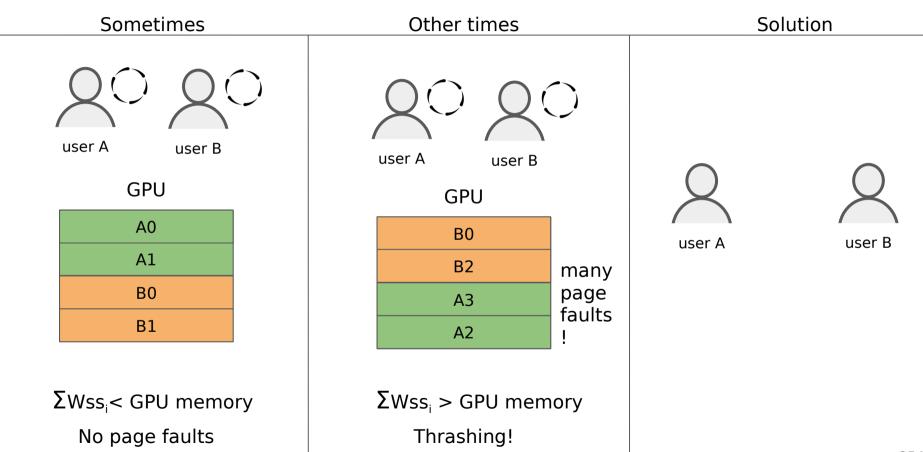
GPU

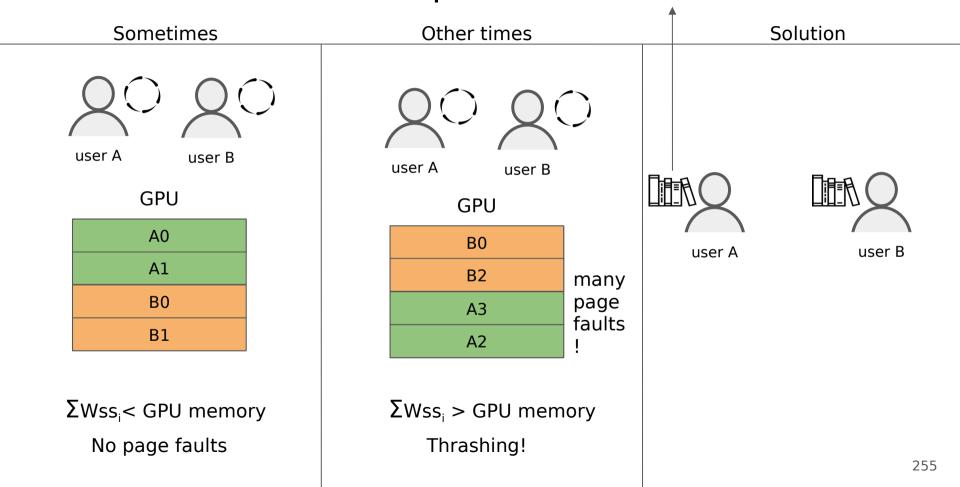


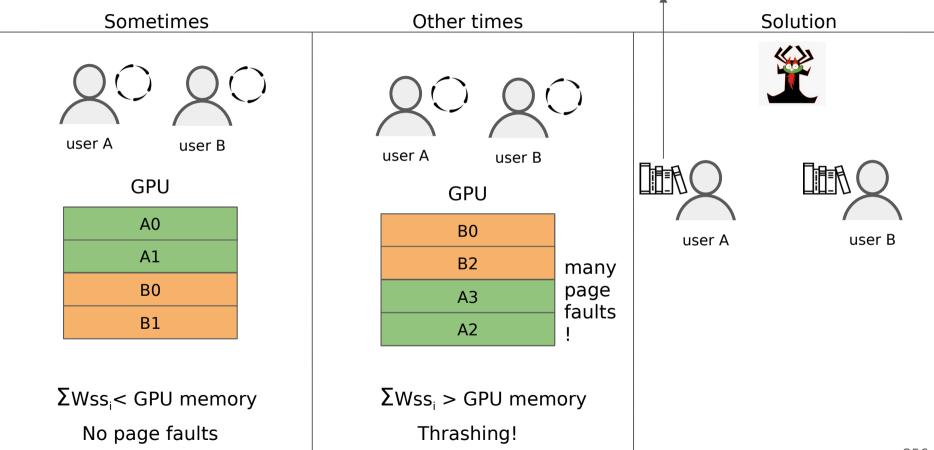
 Σ Wss_i > GPU memory Thrashing!

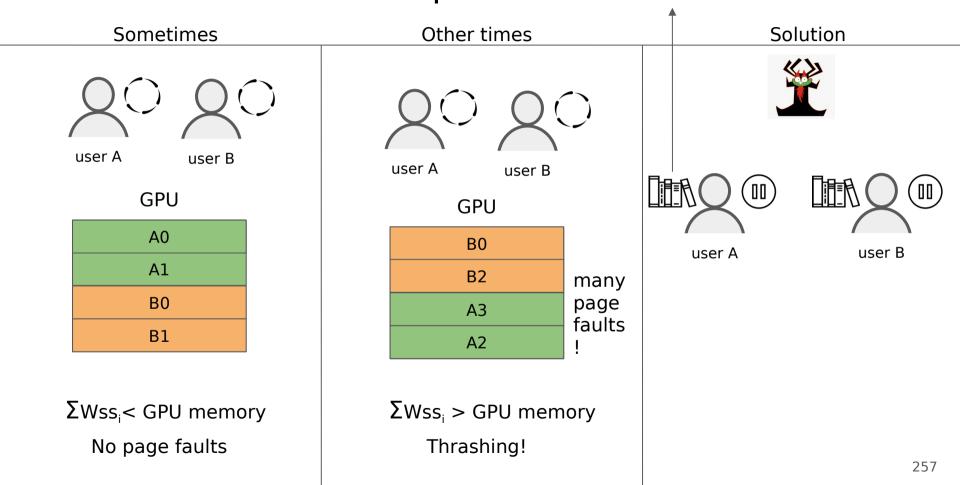


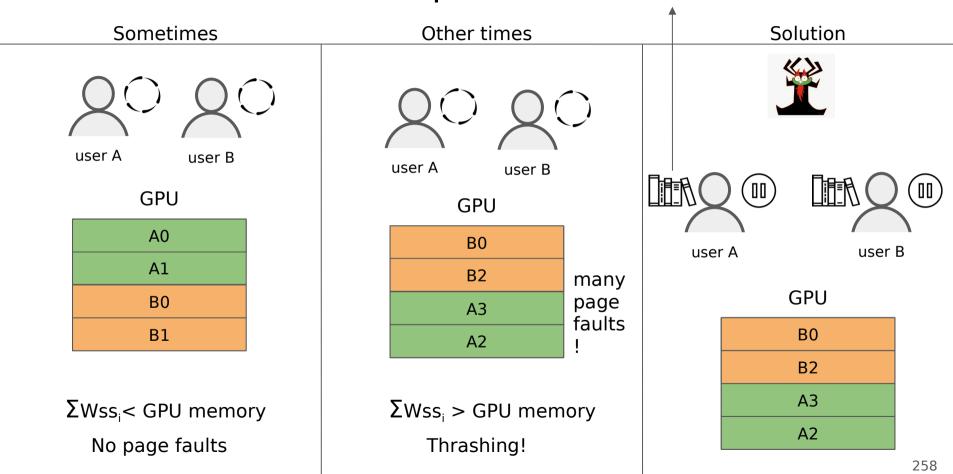


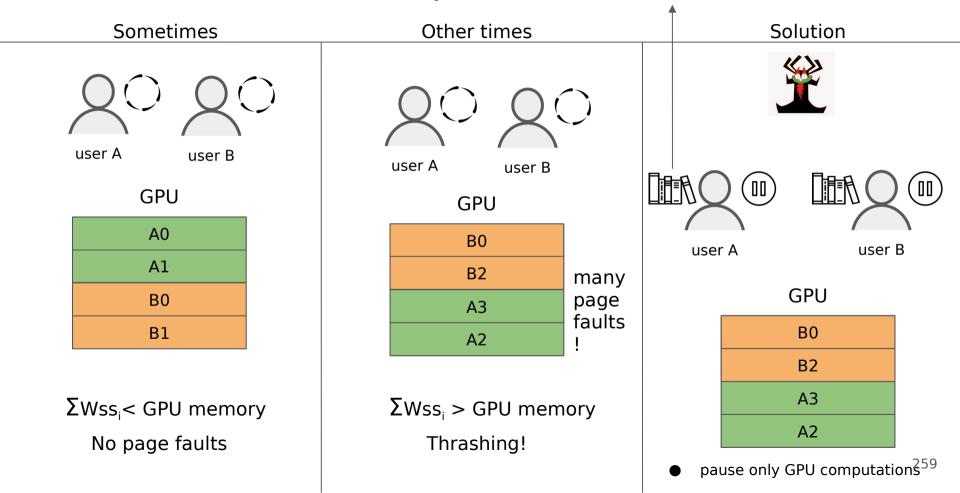


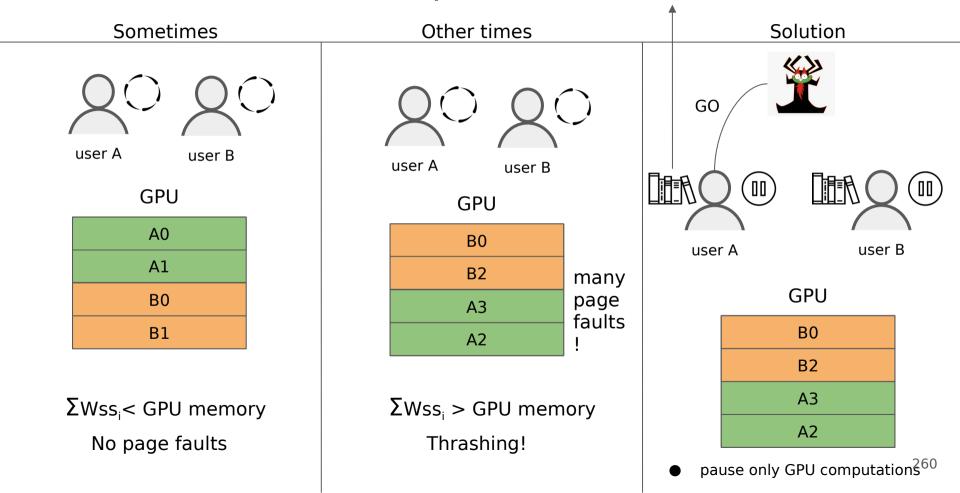


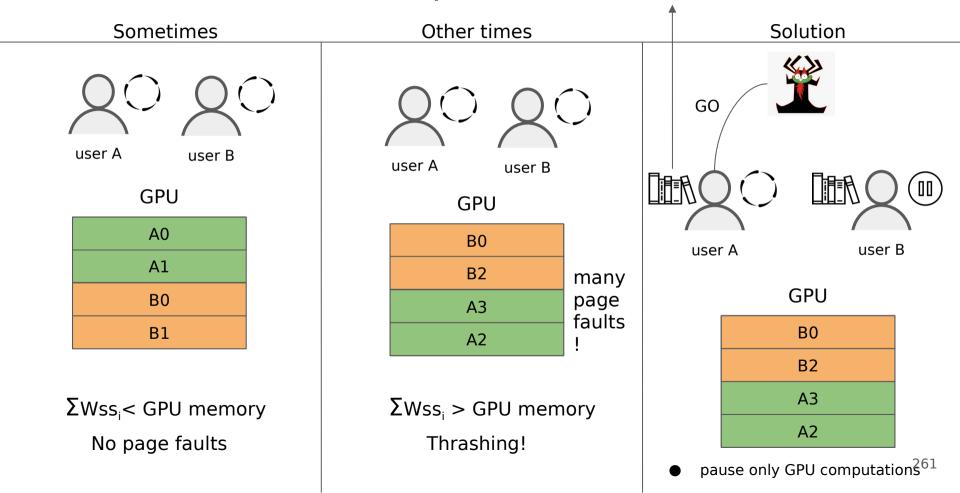


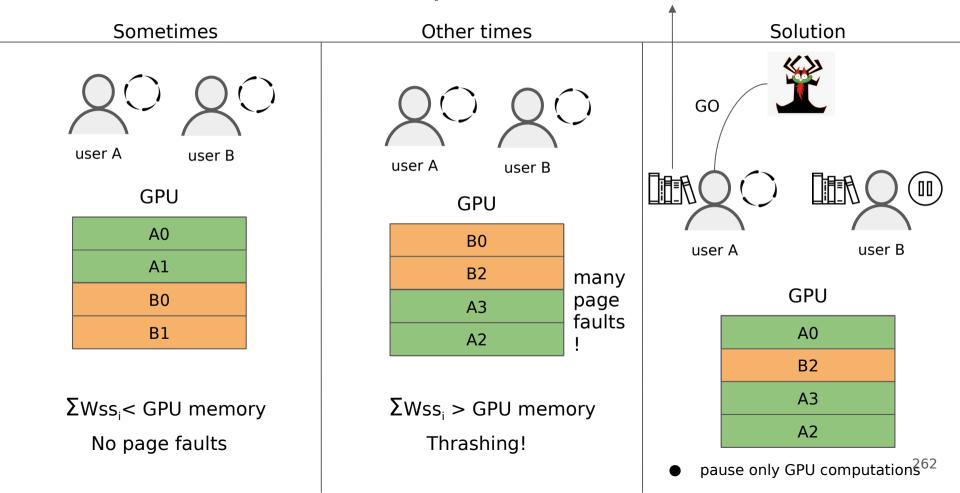


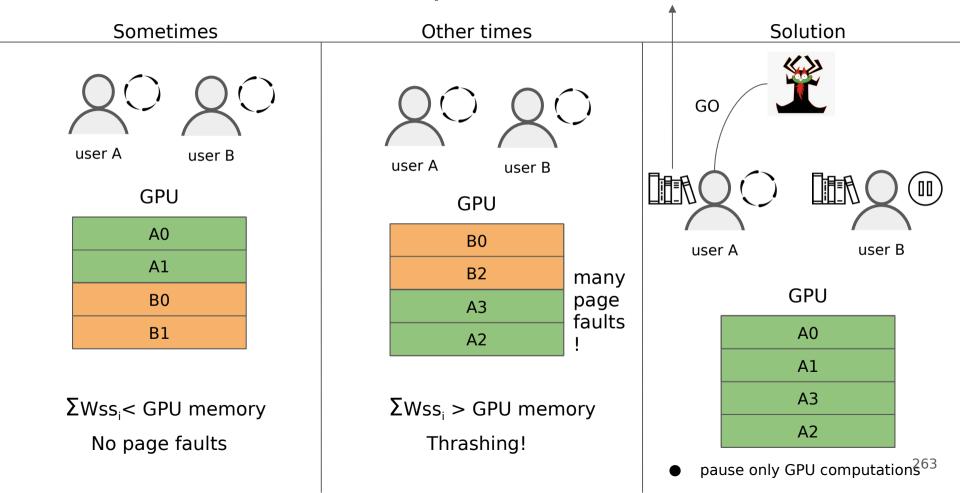


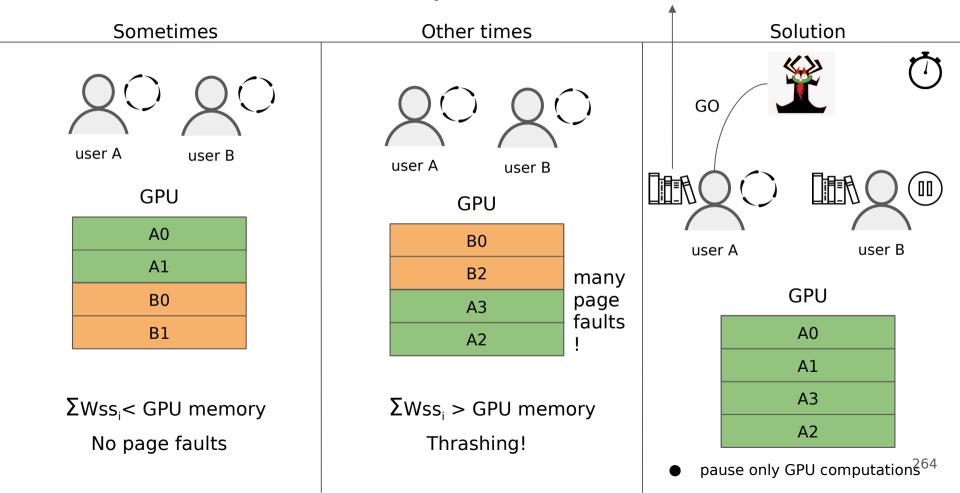


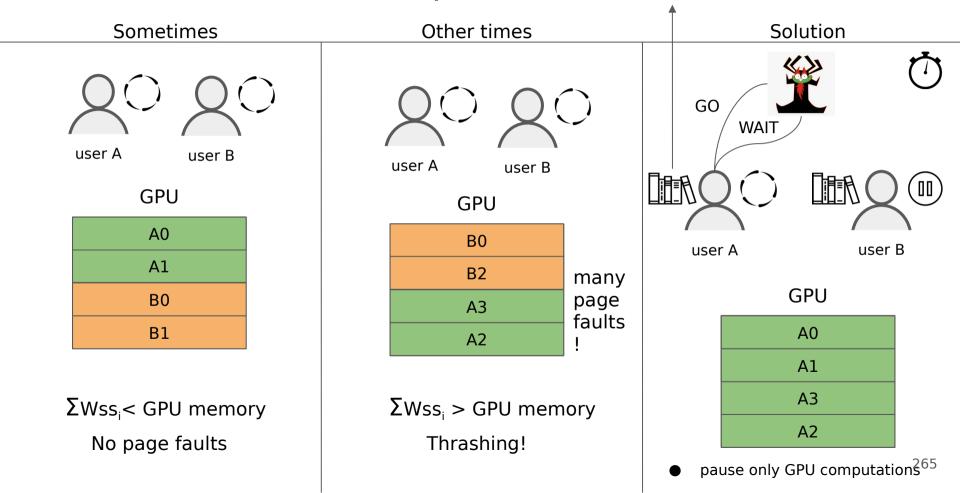


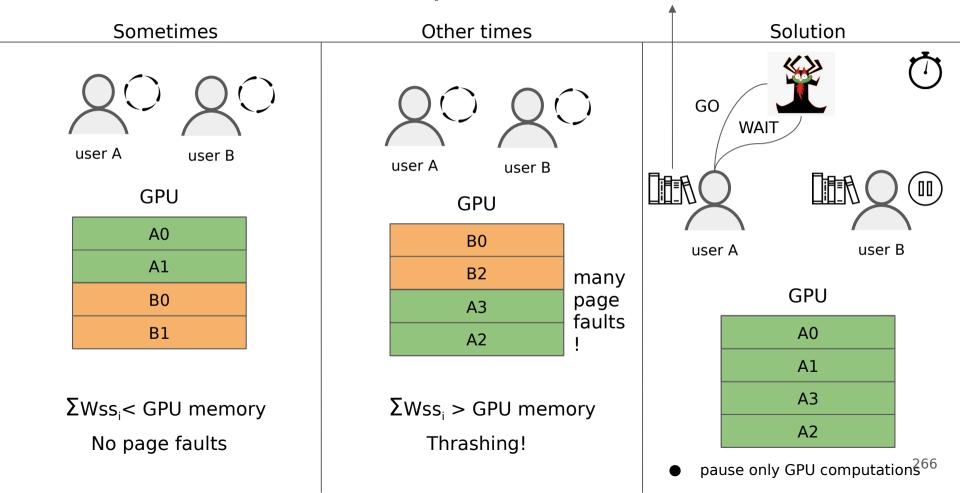


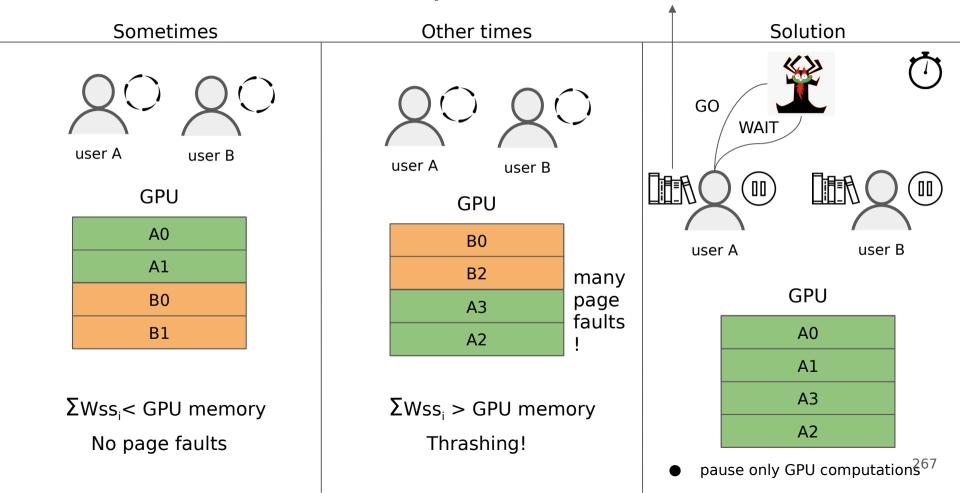


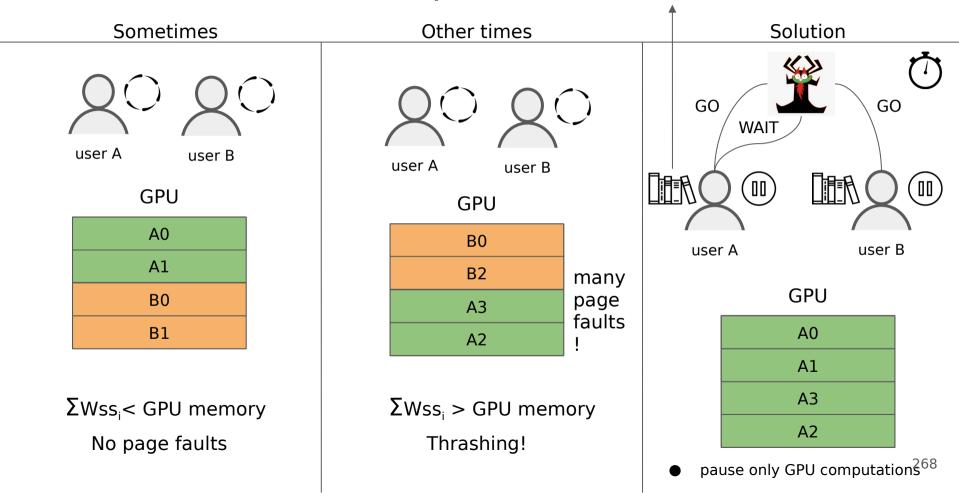


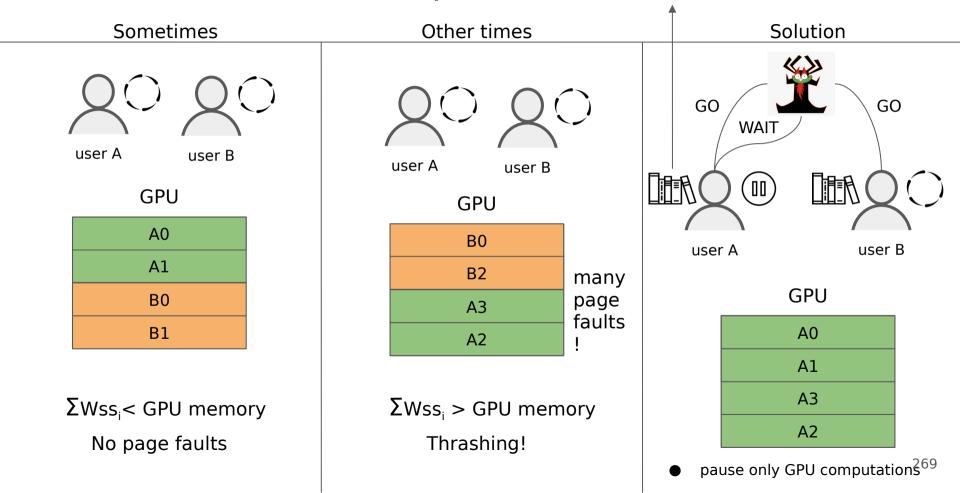


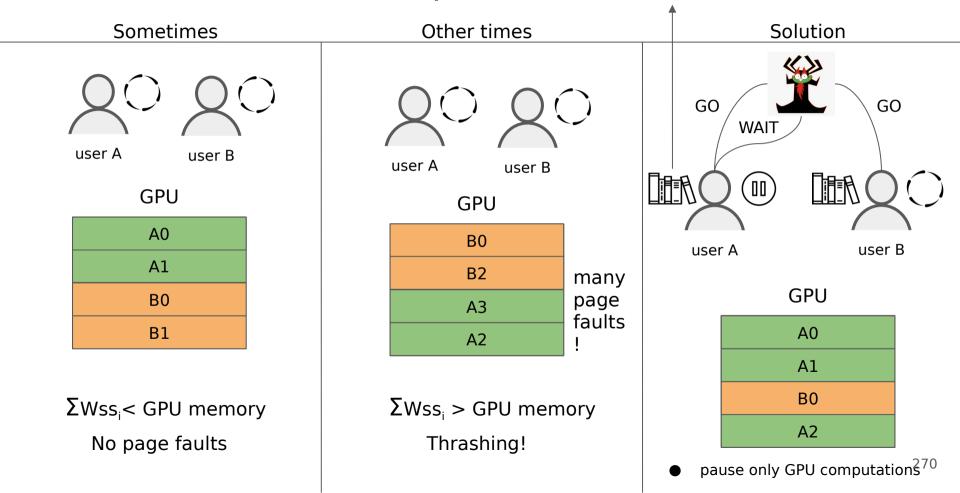


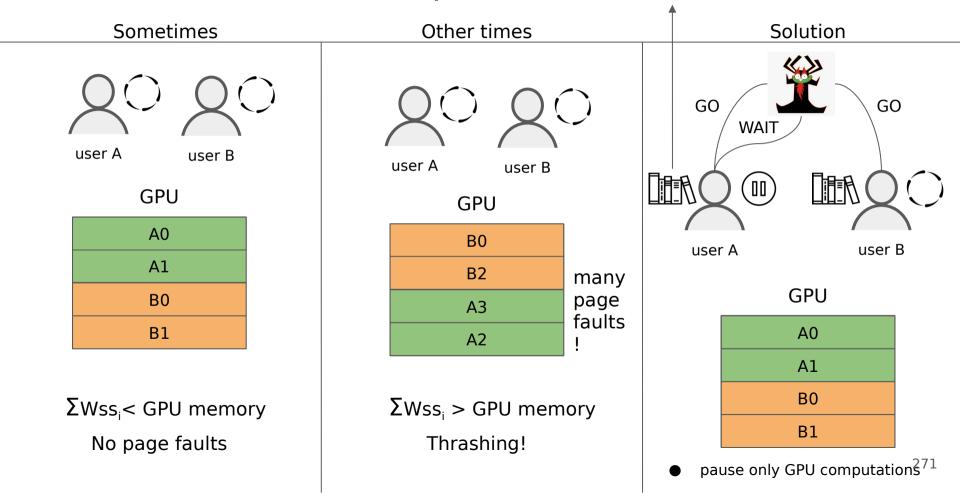


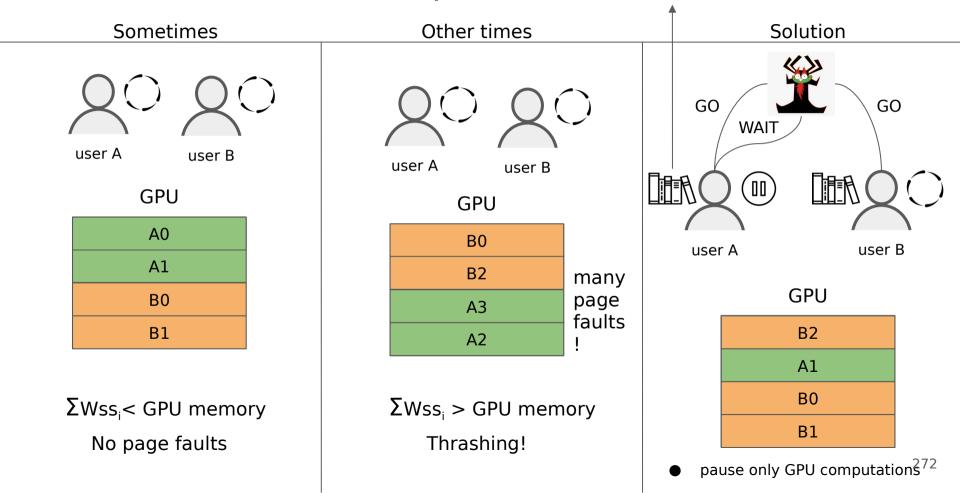


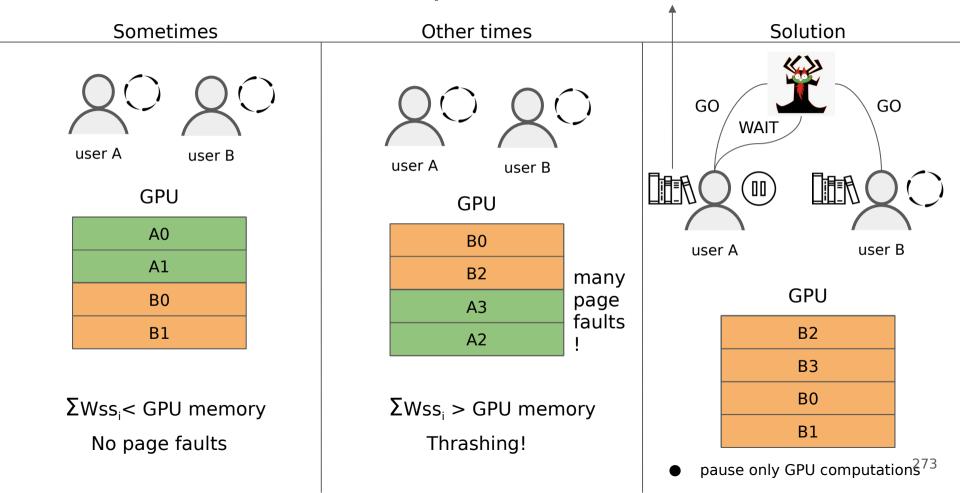


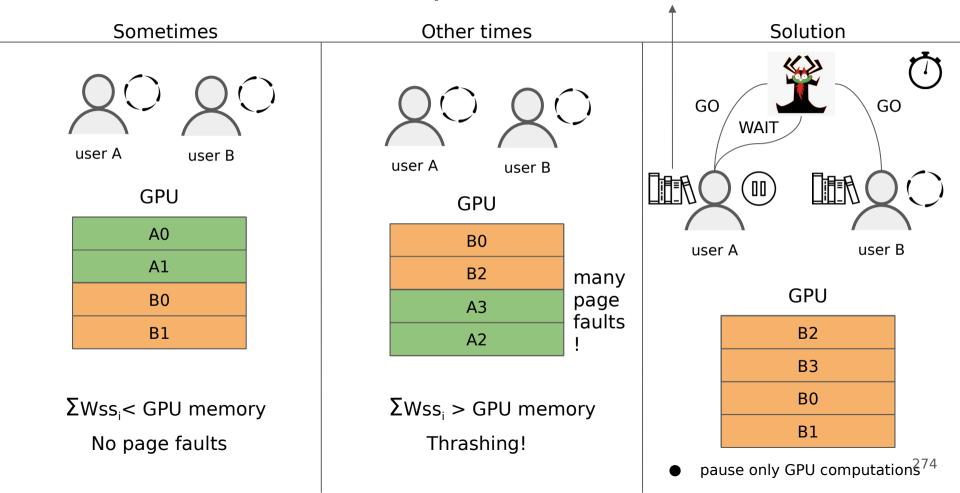


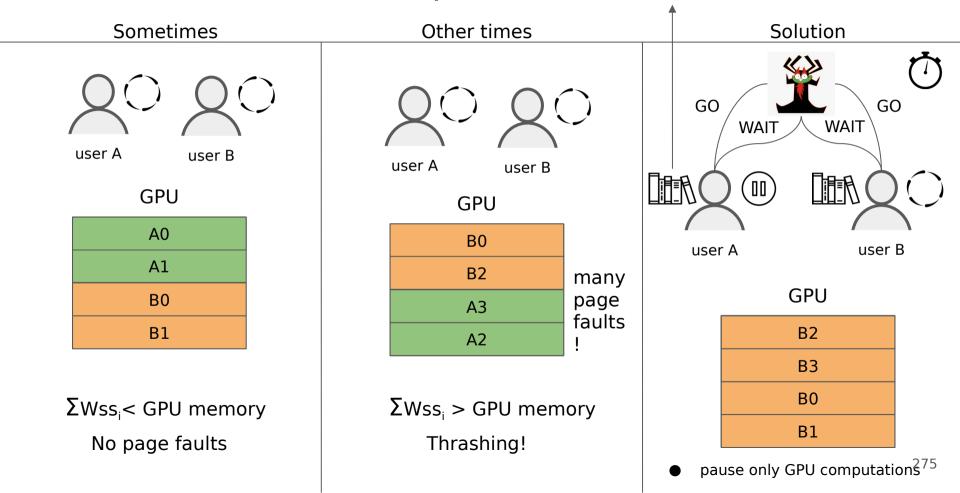








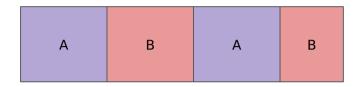




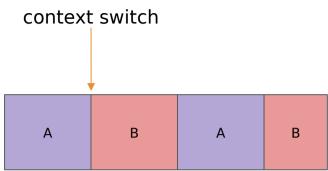
● WSS > GPU Memory

● WSS > GPU Memory

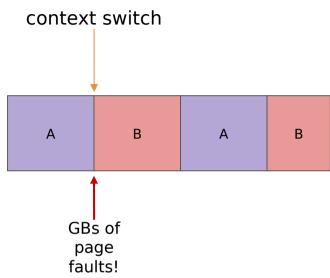
● WSS > GPU Memory



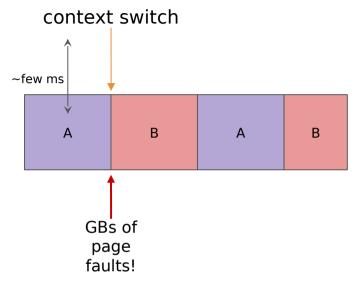
● WSS > GPU Memory



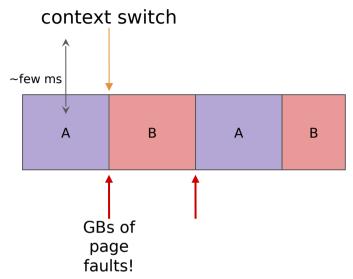
● WSS > GPU Memory



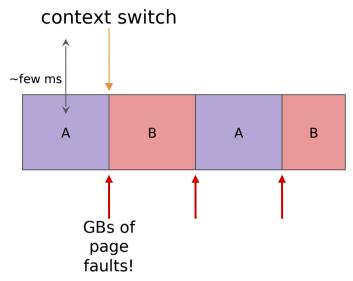
● WSS > GPU Memory



● WSS > GPU Memory



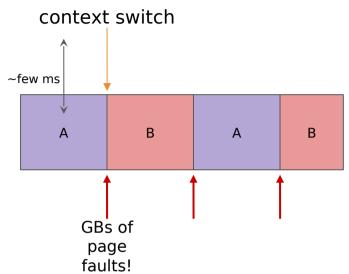
● WSS > GPU Memory



● WSS > GPU Memory

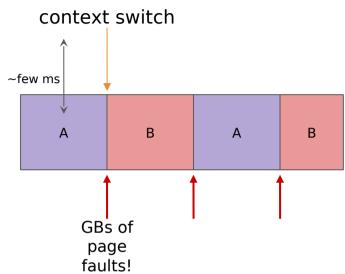
Default case:

memory swaps happen too frequently



● WSS > GPU Memory

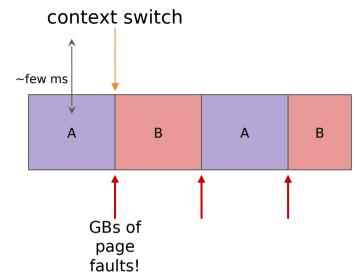
- memory swaps happen too frequently
- Thrashing!



● WSS > GPU Memory

Default case:

- memory swaps happen too frequently
- Thrashing!

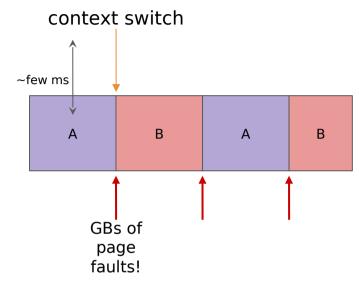


anti-thrashing mechanism:

WSS > GPU Memory

Default case:

- memory swaps happen too frequently
- Thrashing!

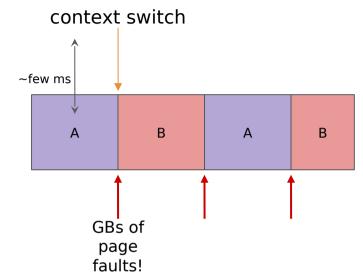


anti-thrashing mechanism:

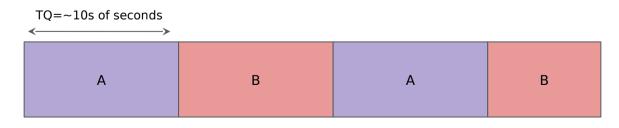


Default case:

- memory swaps happen too frequently
- Thrashing!

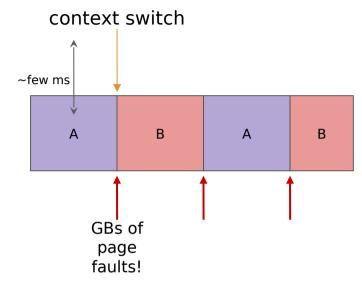


anti-thrashing mechanism:



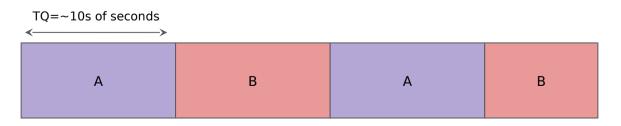
Default case:

- memory swaps happen too frequently
- Thrashing!



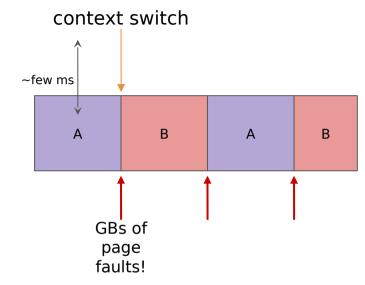
anti-thrashing mechanism:

 orders of magnitude fewer memory swaps



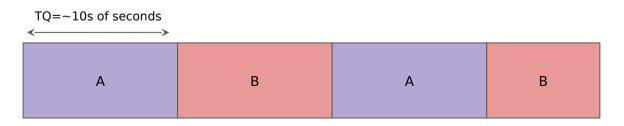
Default case:

- memory swaps happen too frequently
- Thrashing!



anti-thrashing mechanism:

- orders of magnitude fewer memory swaps
- Cooperative Scheduling

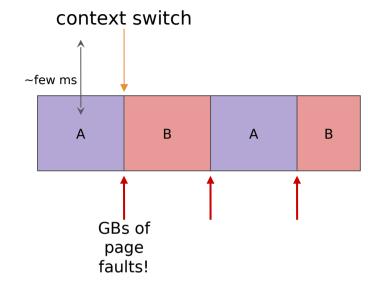


Anti-thrashing idea

WSS > GPU Memory

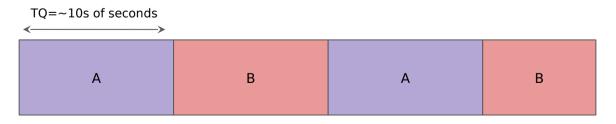
Default case:

- memory swaps happen too frequently
- Thrashing!



anti-thrashing mechanism:

- orders of magnitude fewer memory swaps
- Cooperative Scheduling
- No Thrashing!



Demo

• 16-core CPU (Intel Xeon 2.3 GHz - Haswell), 104 GB of RAM and an NVIDIA Tesla P100 GPU (16 GB).

- 16-core CPU (Intel Xeon 2.3 GHz Haswell), 104 GB of RAM and an NVIDIA Tesla P100 GPU (16 GB).
- non-interactive (conventional) tasks

- 16-core CPU (Intel Xeon 2.3 GHz Haswell), 104 GB of RAM and an NVIDIA Tesla P100 GPU (16 GB).
- non-interactive (conventional) tasks
- measure Total Completion Time

- 16-core CPU (Intel Xeon 2.3 GHz Haswell), 104 GB of RAM and an NVIDIA Tesla P100 GPU (16 GB).
- non-interactive (conventional) tasks
- measure Total Completion Time

Programs

Name	Working Set Size	GPU/CPU ratio
small_90	7.2 GB	90/10
small_50	7.2 GB	50/50
big_90	15.3 GB	90/10
big_50	15.3 GB	50/50

- 16-core CPU (Intel Xeon 2.3 GHz Haswell), 104 GB of RAM and an NVIDIA Tesla P100 GPU (16 GB).
- non-interactive (conventional) tasks
- measure Total Completion Time

Programs

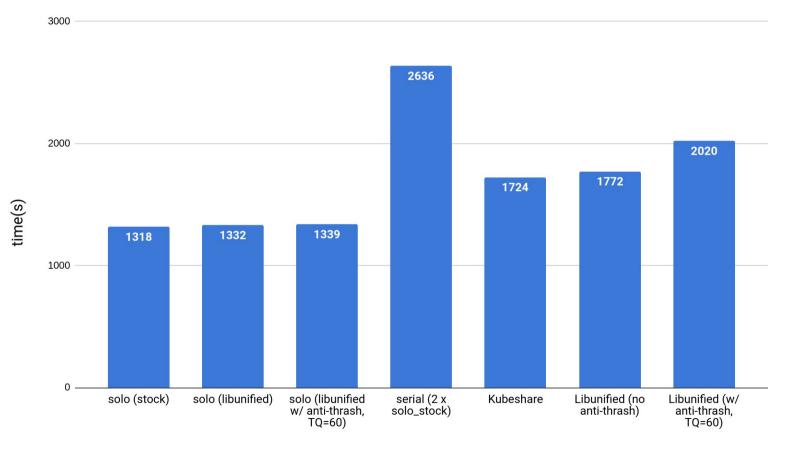
Name	Working Set Size	GPU/CPU ratio
small_90	7.2 GB	90/10
small_50	7.2 GB	50/50
big_90	15.3 GB	90/10
big_50	15.3 GB	50/50

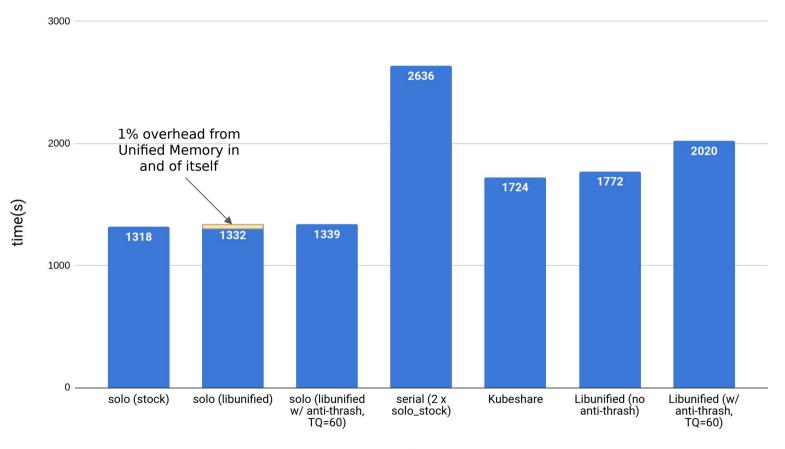
- 16-core CPU (Intel Xeon 2.3 GHz Haswell), 104 GB of RAM and an NVIDIA Tesla P100 GPU (16 GB).
- non-interactive (conventional) tasks
- measure Total Completion Time

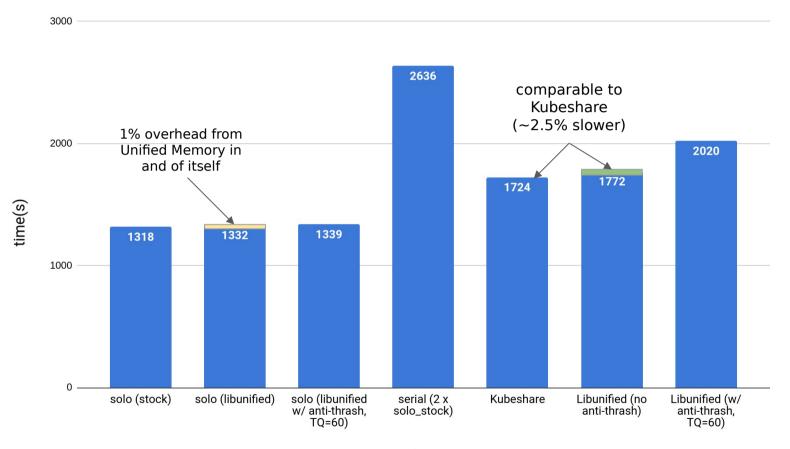
Programs

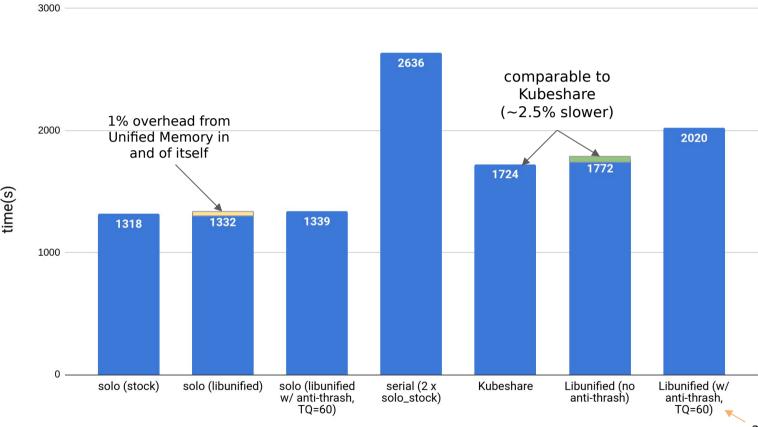
Name	Working Set Size	GPU/CPU ratio
small_90	7.2 GB	90/10
small_50	7.2 GB	50/50
big_90	15.3 GB	90/10
big_50	15.3 GB	50/50

Running 2 x big_{50,90} in parallel causes thrashing!

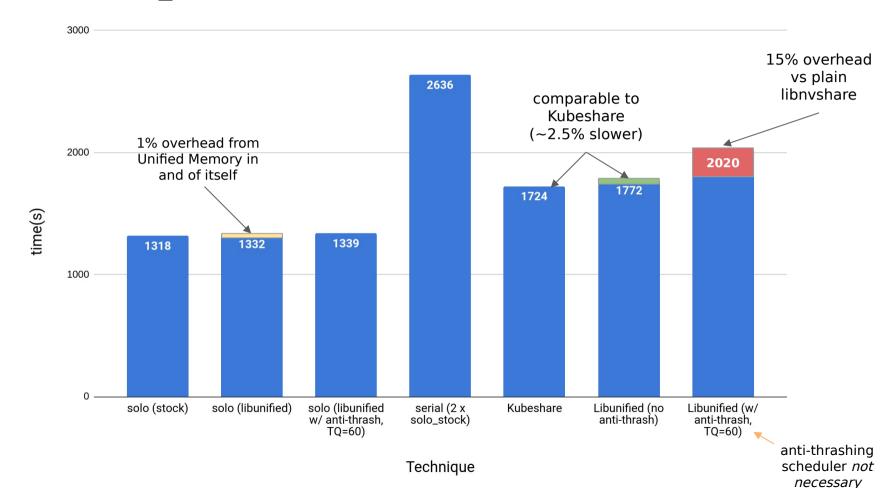


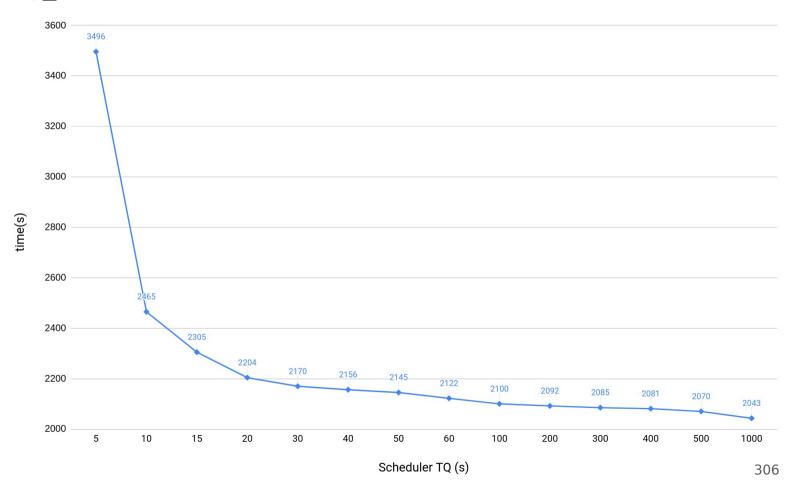


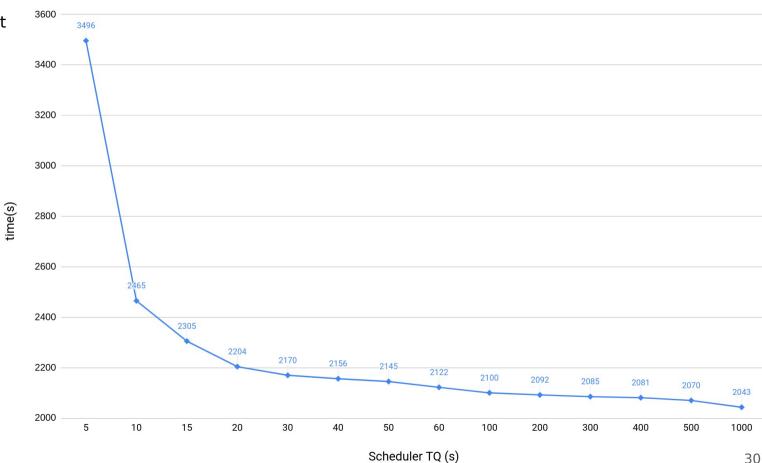




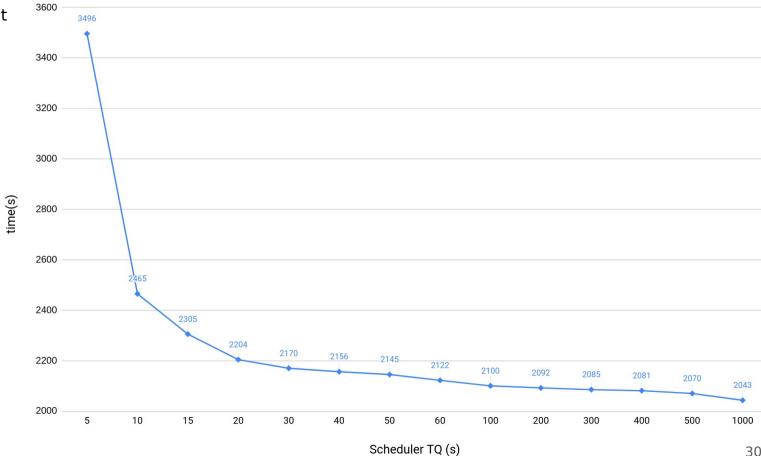
anti-thrashing scheduler *not necessary*



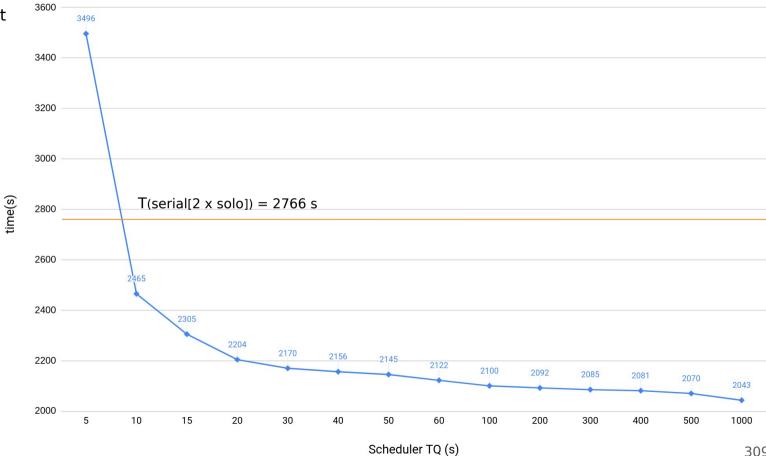




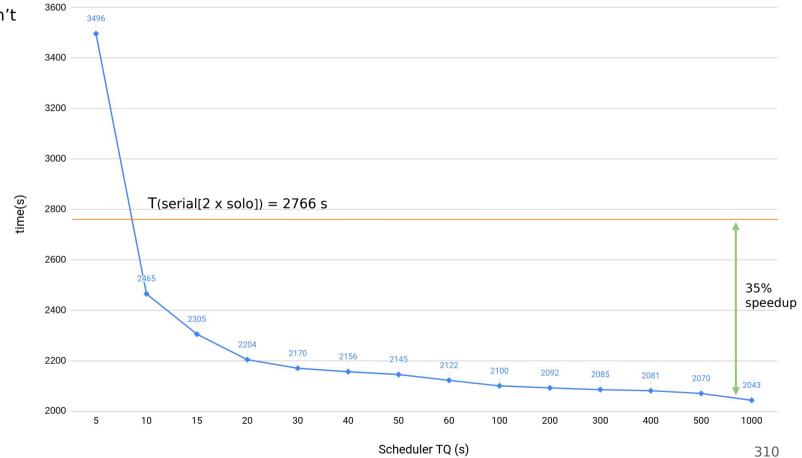
T(libnvshare, no anti-thrash) = 11757



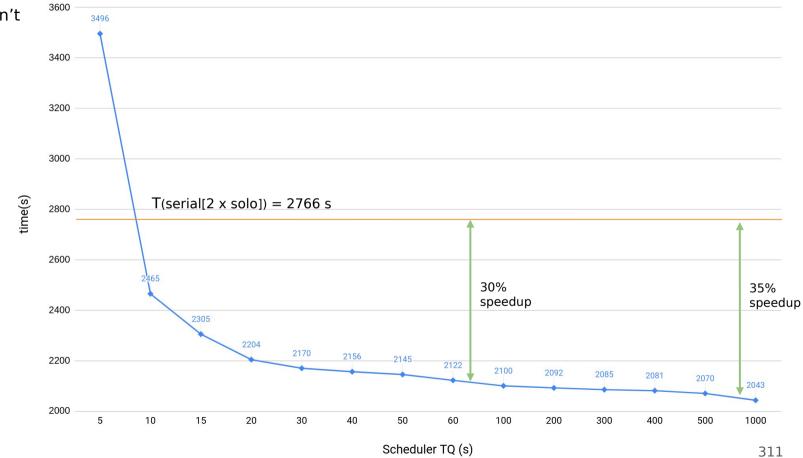
T(libnvshare, no anti-thrash) = 11757



T(libnvshare, no anti-thrash) = 11757

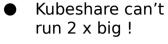


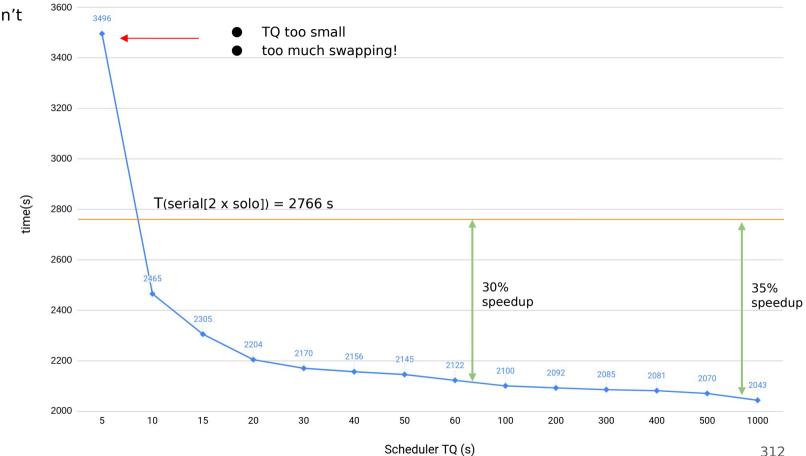
T(libnvshare, no anti-thrash) = 11757



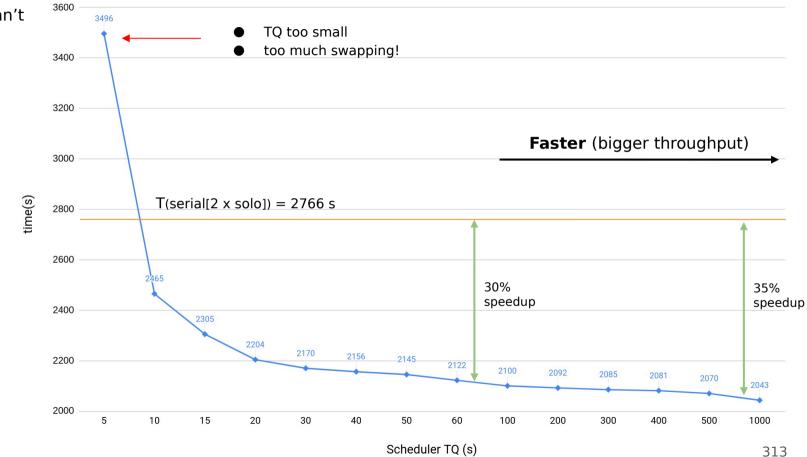


T(libnvshare, no anti-thrash) = 11757

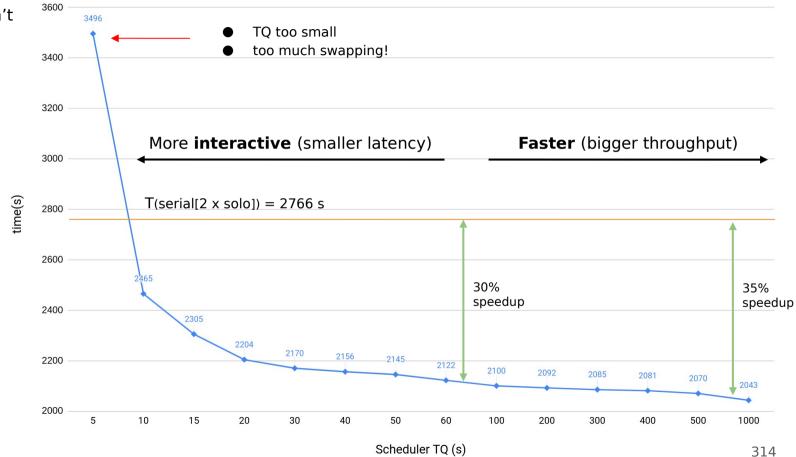




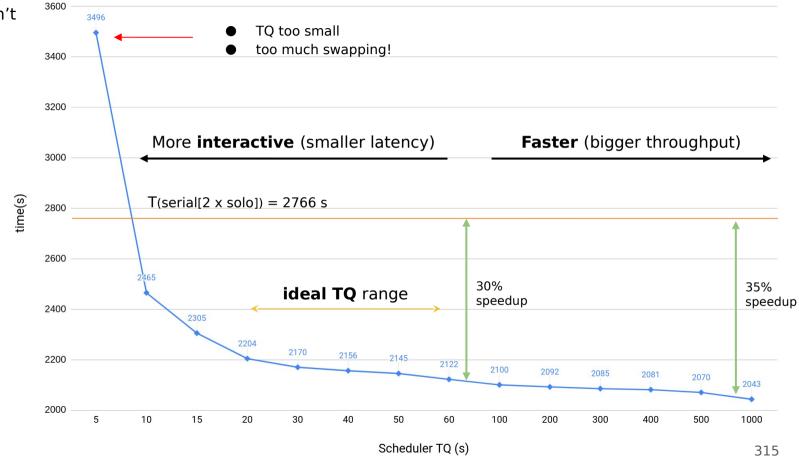
T(libnvshare, no anti-thrash) = 11757



T(libnvshare, no anti-thrash) = 11757



T(libnvshare, no anti-thrash) = 11757



of co-located processes limited by system RAM

of co-located processes limited by system RAM

each process can use the whole GPU memory

of co-located processes limited by system RAM

each process can use the whole GPU memory

GPU utilization increases even for non-interactive jobs

of co-located processes limited by system RAM

each process can use the whole GPU memory

- GPU utilization increases even for non-interactive jobs
- No code changes to user programs

of co-located processes limited by system RAM

each process can use the whole GPU memory

- GPU utilization increases even for non-interactive jobs
- No code changes to user programs
- Sensible default policies:
 - anti-thrashing scheduler always on
 - default TQ = 20 sec

Future Work

- Deploy to production environments
- Support multiple GPUs per node
- Create heuristics (e.g. PCle traffic) to automatically enable/disable the anti-thrashing mechanism
- Publish our work

- Later on: Intra-node job migration from one GPU to another
- Later on: Inter-node GPU job migration (Checkpoint-restart)

Thank you! Questions?