1. Description

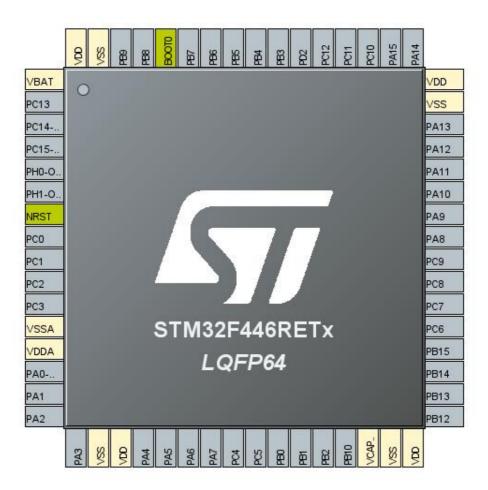
1.1. Project

Project Name	test
Board Name	custom
Generated with:	STM32CubeMX 5.4.0
Date	12/02/2019

1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F446
MCU name	STM32F446RETx
MCU Package	LQFP64
MCU Pin number	64

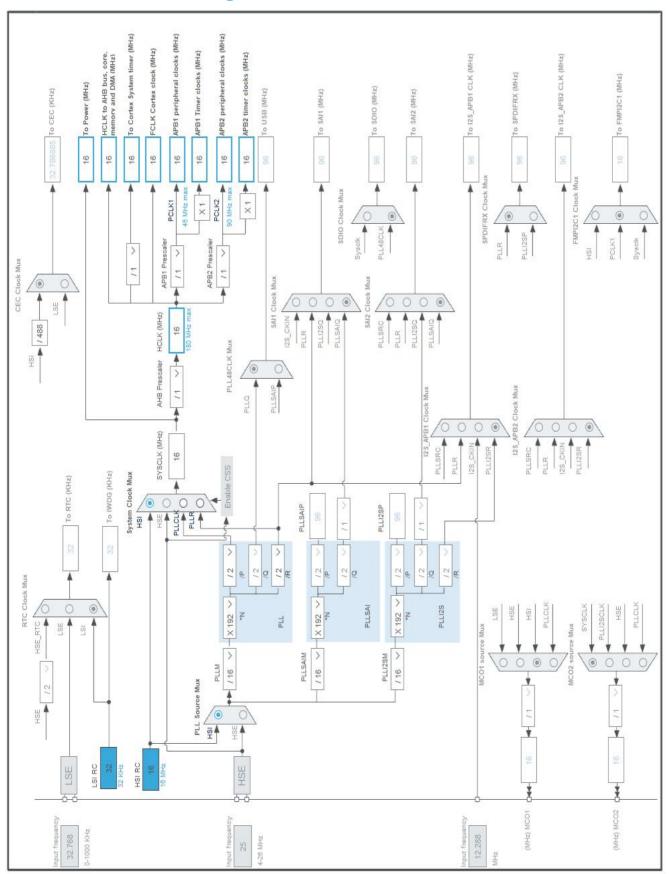
2. Pinout Configuration



3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
7	NRST	Reset		
12	VSSA	Power		
13	VDDA	Power		
18	VSS	Power		
19	VDD	Power		
30	VCAP_1	Power		
31	VSS	Power		
32	VDD	Power		
47	VSS	Power		
48	VDD	Power		
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		

4. Clock Tree Configuration



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5. Software Project

5.1. Project Settings

Name	Value		
Project Name	test		
Project Folder	C:\Users\Antoine\STM32CubeIDE\workspace_1.1.0\test		
Toolchain / IDE	STM32CubeIDE		
Firmware Package Name and Version STM32Cube FW_F4 V1.24.1			

5.2. Code Generation Settings

Name	Value		
STM32Cube MCU packages and embedded software	Copy only the necessary library files		
Generate peripheral initialization as a pair of '.c/.h' files	No		
Backup previously generated files when re-generating	No		
Delete previously generated files when not re-generated	Yes		
Set all free pins as analog (to optimize the power	No		
consumption)			

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F4
Line	STM32F446
мси	STM32F446RETx
Datasheet	027107 Rev6

6.2. Parameter Selection

Temperature	25
17/00	3.3

7. IPs and Middleware Configuration 7.1. SYS

Timebase Source: SysTick

^{*} User modified value

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull	Max	User Label
				down	Speed	

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Pre-fetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	0	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			
FPU global interrupt	unused			

^{*} User modified value

9. Software Pack Report