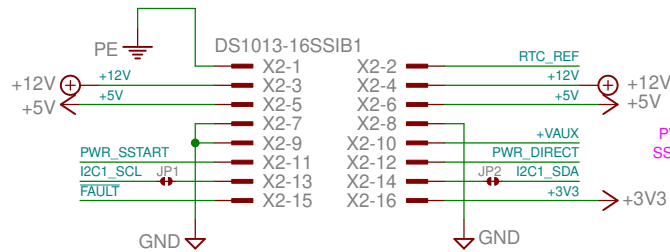


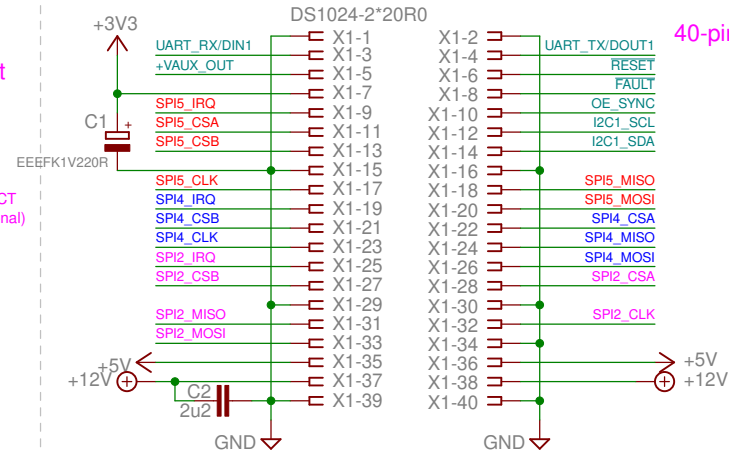
Input connector (power, soft start, fan controller)



16-pin DIB AUX PS socket

PE 1	2 AC freq out
+12V 3	4 +12V
+5V 5	6 +5V
Gnd 7	8 Gnd
Gnd 9	10 +VAUX
PWR_SSTART 11	12 PWR_DIRECT
SSCL (optional) 13	14 SSDA (optional)
FAULT 15	16 +3V3

Output connector (3 x SPI modules)

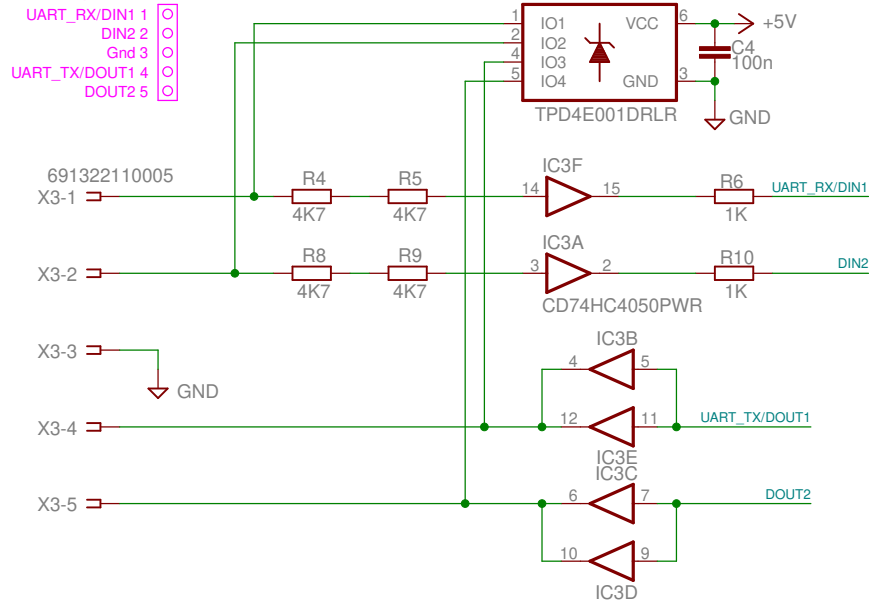


40-pin DIB v1.0 backplane socket

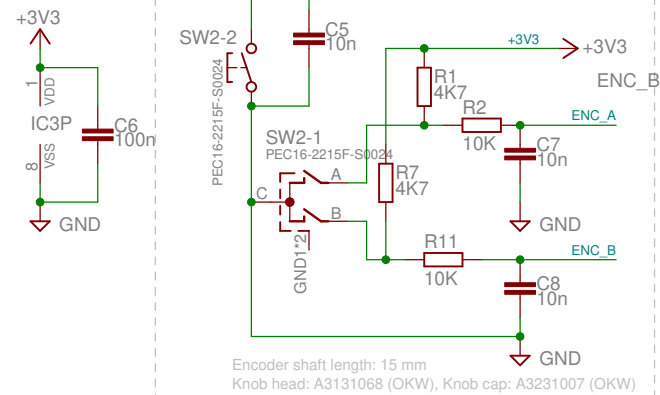
Gnd 1	2 Gnd
UART_RX 3	4 UART_TX
+Vaux 5	6 NRESET
+3V3 7	8 NFAULT
SPI3_IRQ 9	10 OE_SYNC
SPI3_CSA 11	12 I2C_SCL
SPI3_CSB 13	14 I2C_SDA
Gnd 15	16 Gnd
SPI3_SCLK 17	18 SPI3_MISO
SPI2_IRQ 19	20 SPI3_MOSI
SPI2_CSB 21	22 SPI2_CSA
SPI2_SCLK 23	24 SPI2_MISO
SPI1_IRQ 25	26 SPI2_MOSI
SPI1_CSB 27	28 SPI1_CSA
Gnd 29	30 Gnd
SPI1_MISO 31	32 SPI1_SCLK
SPI1_MOSI 33	34 SPI3_CSC
+5V 35	36 +5V
+12V 37	38 +12V
Gnd 39	40 Gnd

Digital I/O with protections

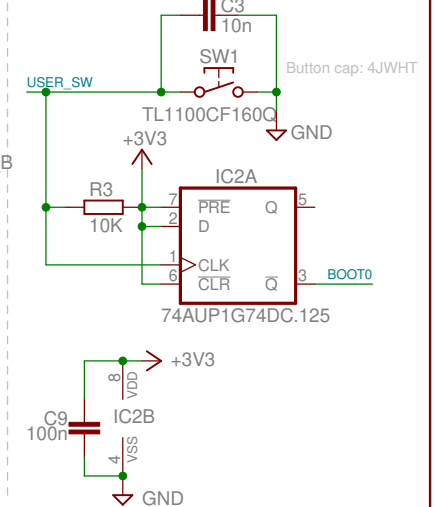
5-pin I/O connector



Encoder with switch



User / Boot0 switch



I/O connectors, User SW, Encoder, Digital I/O

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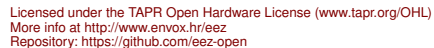
HR000002

Licensed under the TAPR Open Hardware License (www.tapr.org/OHL)

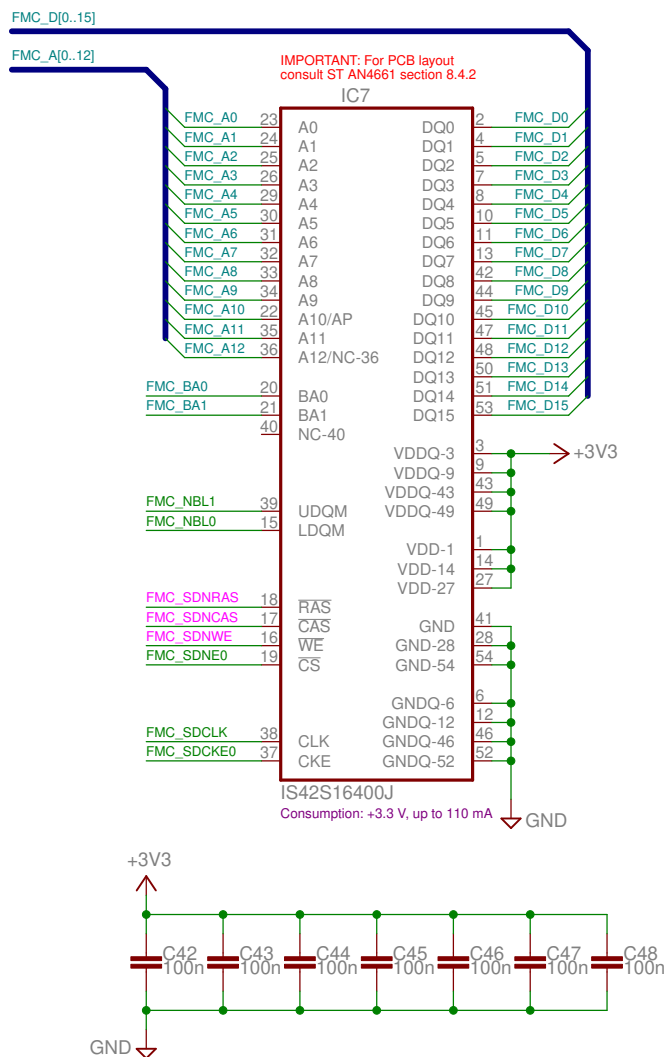
More info at <http://www.envox.hr/eez>

Repository: <https://github.com/eez-open>

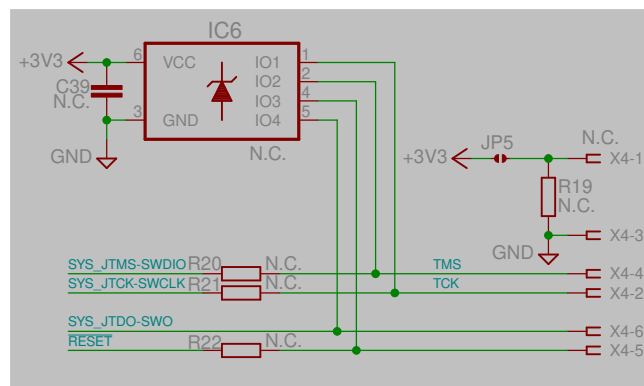




SDRAM



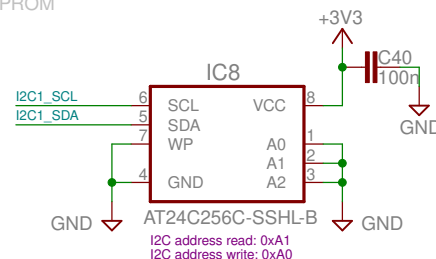
JTAG/SWD (optional)



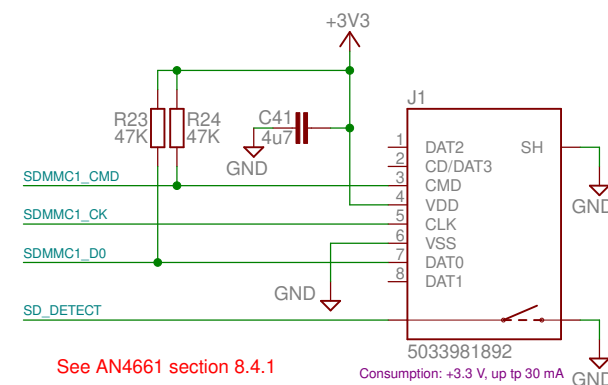
STM-32 board SWD header

Vdd target 1	
SWCLK 2	
Gnd 3	
SWDIO 4	
NRST 5	
SWO 6	

I2C EEPROM

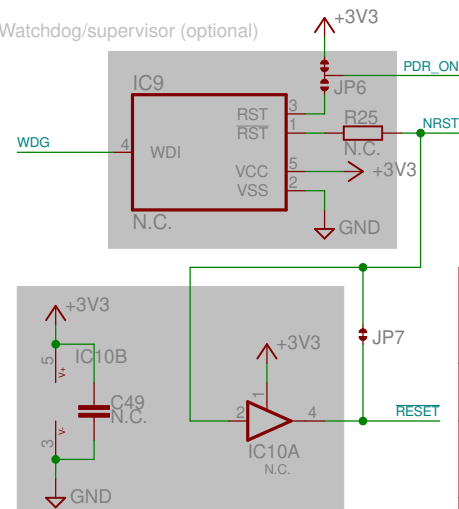


Micro SD card socket



See AN4661 section 8.4.1

Watchdog/supervisor (optional)



SDRAM, JTAG, I2C EEPROM, SD Card

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