



Sym	Nº	Mils	MM	Qty	Plated
+	1	12	0.30	277	YES
×	2	22	0.55	2	NOT
□	3	31	0.80	4	NOT
◇	4	40	1.02	56	YES
⊗	5	126	3.20	2	NOT

Class	RF	Type	Coated Coplanar Waveguide With Ground 1		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP BOTTOM	50 Ohms	26 mils		N/A	6 mils
Class	USB	Type	Diff Coated Coplanar Waveguide With Ground		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils
Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP BOTTOM	100 Ohms	10 mils		6 mils	6 mils

A cross-sectional diagram of a 4-layer printed circuit board (PCB). The diagram shows a central core surrounded by four layers. The top layer is labeled 'LAYER 1 LT1 TOP' and the bottom layer is labeled 'LAYER 4 LT6 BOT TOM'. The core is labeled 'CORE'. The thickness of the core is indicated as 1.061 MM +/- 10%. The top and bottom layers are made of 18 UM CU + 12.5 UM PLATING (30.5 UM). The core material is 1.0 MM CORE FR-4 LG 135 INCLUDING COPPER.

1.061 MM +/- 10%

LAYER 1 LT1 TOP

CORE

LAYER 4 LT6 BOT TOM

18 UM CU + 12.5 UM PLATING (30.5 UM)

1.0 MM CORE FR-4 LG 135 INCLUDING COPPER

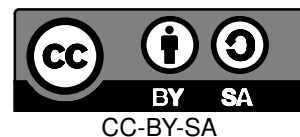
18 UM CU + 12.5 UM PLATING (30.5 UM)

1. PRINTED CIRCUIT BOARD MADE FROM NEMA GRADE FR-4 TO 135 EPOXY LAMINATE WITH 18 UM COPPER PLATING AND 1 MM THICKNESS.
2. ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE.
3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.
4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
5. BOARD PLATED USING REFLOW OR SIMILAR METHOD.
6. BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY.
EQUIVALENT WET OR DRY FILM MAY BE USED.
7. SILKSCREEN BOARD USING BLACK INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANTS IS NOT ACCEPTABLE.
8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:

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Solder side (.SOL)

Rev. A



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