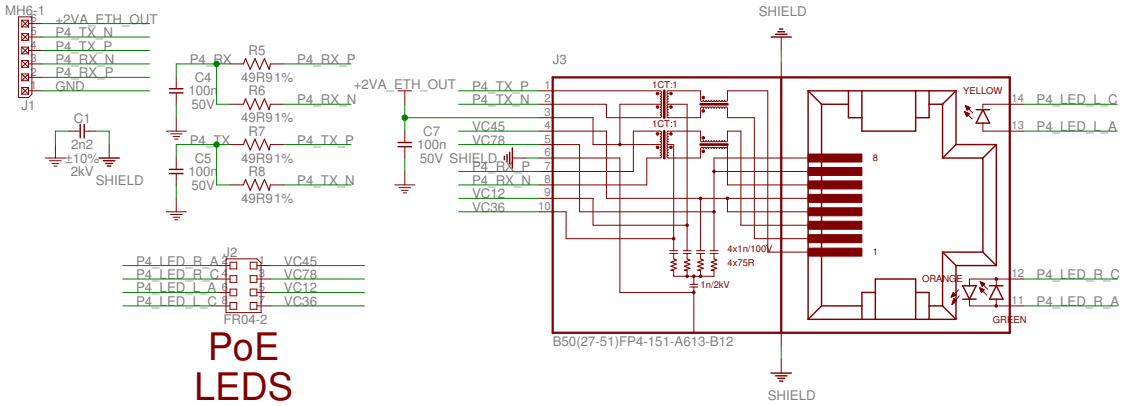


PoE LEDS

WAN on port P4, DTE wiring (1=T+, 2=T-, 3=R+, 6=R-)

Resistors are 5% 1/16W 0402 unless otherwise specified
Ceramic capacitors are ±10% 50V X7R dielectric 0402 unless otherwise specified

Domino Core
Domino Pi
Domino Qi

Domino Single Ethernet

22/07/14 23:51

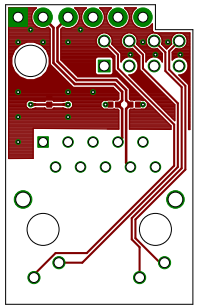
Sheet: 1/1

Rev.A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

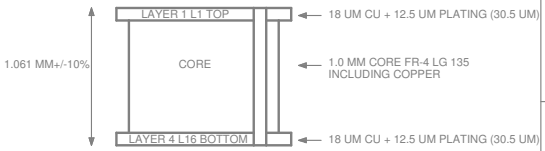
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
⌖	8	128	3.25	2	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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EQUIVALENT WET OR DRY FILM MAY BE USED.
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Domino Core

Domino Pi

Domino Qi

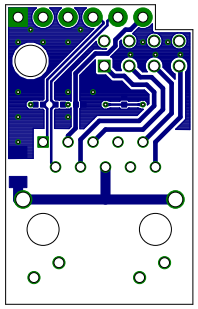
Domino Single Ethernet

22/07/14 23:53

Component Side (.CMP)

Rev. A

LAYER-STACK



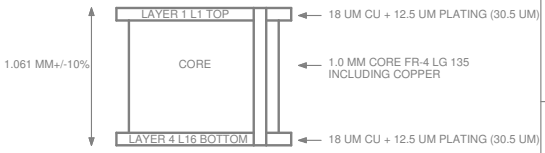
DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
⌖	8	128	3.25	2	NOT

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B	
Layer	Impedance	Trace Width	Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils	6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core
Domino Pi
Domino Qi

Domino Single Ethernet

22/07/14 23:53

Solder side (.SOL)

Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
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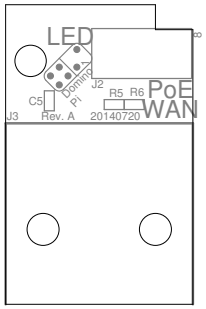
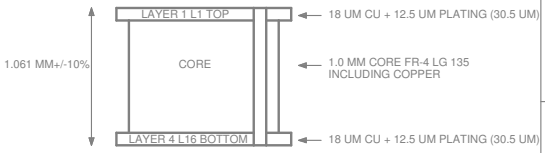


LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

Domino Single Ethernet

22/07/14 23:53

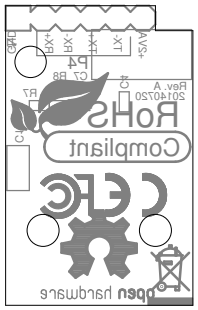
Silk screen CMP (.PLC)

Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

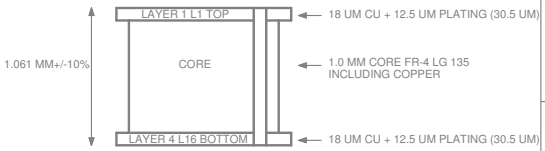
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
⌖	8	128	3.25	2	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core | Domino Pi | Domino Qi

Domino Single Ethernet

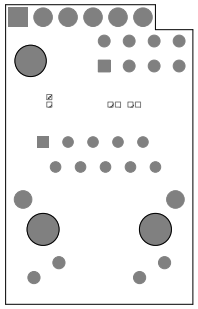
22/07/14 23:53

Silk screen SOL (.PLS) | Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

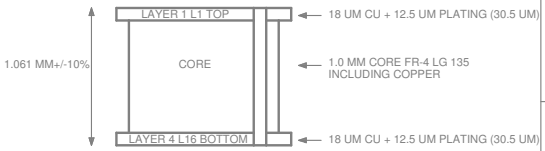
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
⌖	8	128	3.25	2	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

Domino Single Ethernet

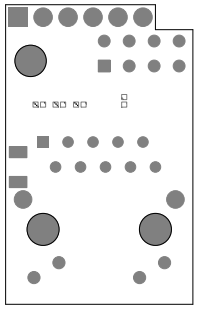
22/07/14 23:53

Solder stop mask CMP (.STC) | Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

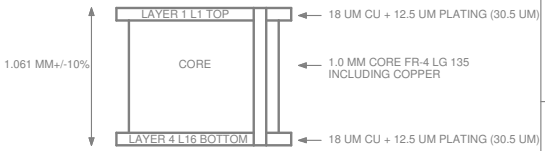
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
⌖	8	128	3.25	2	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B	
Layer	Impedance	Trace Width	Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils	6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

Domino Single Ethernet

22/07/14 23:53

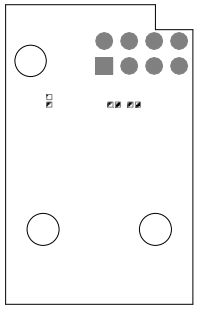
Solder stop mask SOL (.STS)

Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

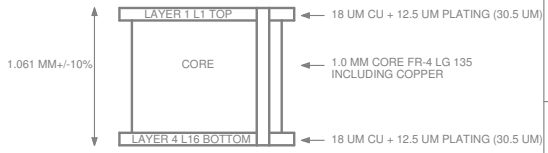
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
⌖	8	128	3.25	2	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

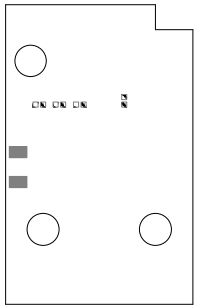
Domino Single Ethernet

22/07/14 23:53

Cream frame CMP (.CRC)

Rev. A

LAYER-STACK



DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
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×	2	35	0.90	10	YES
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◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
⌖	8	128	3.25	2	NOT

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

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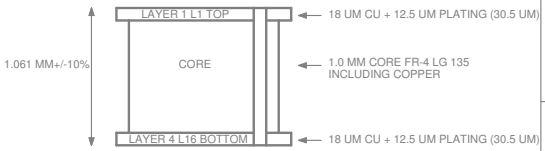

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Domino Core

Domino Pi

Domino Qi

Domino Single Ethernet

22/07/14 23:53

Cream frame SOL (.CRS)

Rev. A

LAYER-STACK

01-16

01-20

DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
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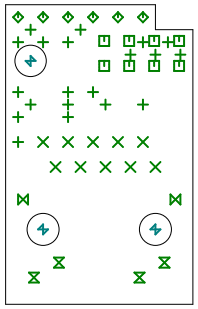
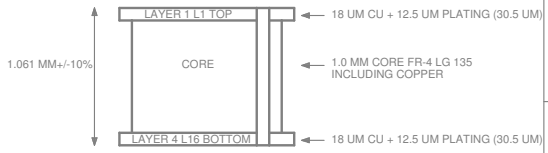
ϟ	8	128	3.25	2	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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2. ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE.
3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.
4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
5. BOARD PLATED USING REFLOW OR SIMILAR METHOD.
6. BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY.
EQUIVALENT WET OR DRY FILM MAY BE USED.
7. SILKSCREEN BOARD USING BLACK INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE
8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:
   * 2 COPPER LAYERS
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   * REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

Domino Core

Domino Pi

Domino Qi

Domino Single Ethernet

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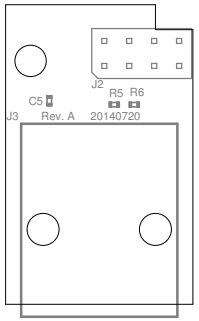
Drill data (.DRD)

Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

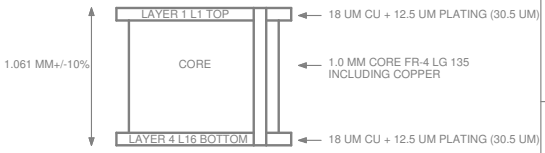
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
⌖	8	128	3.25	2	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

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Component Assembly CMP (.ASC) | Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
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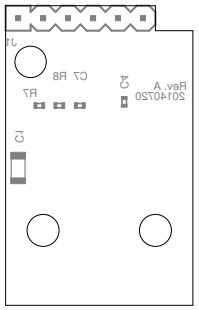
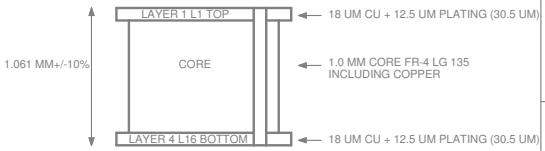


LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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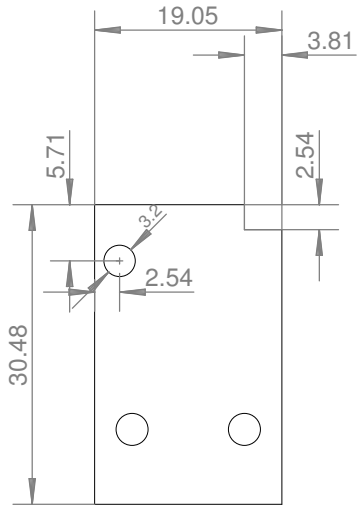
Component Assembly SOL (.ASS) | Rev. A

LAYER-STACK



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×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
⌖	7	126	3.20	1	NOT
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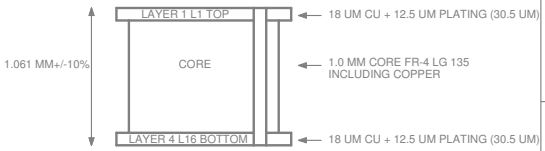
LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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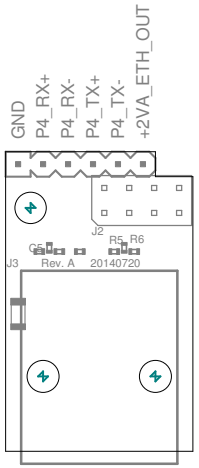
Outline (.OLN) | Rev. A

LAYER-STACK

01-20

DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	20	YES
×	2	35	0.90	10	YES
◳	3	39	1.00	8	YES
◇	4	40	1.02	6	YES
⧖	5	40	1.02	4	YES
⋈	6	59	1.50	2	YES
ϟ	7	126	3.20	1	NOT
ϟ	8	128	3.25	2	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils		6 mils	6 mils

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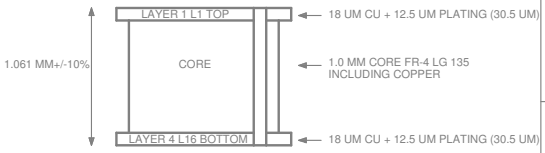

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Pinout (.PIN) | Rev. A

Domino Single Ethernet Rev. A

Item	Qty	Value	Manufacturer	Device	Package	Reference	Description	Remarks
1	1	2n2	ANY	C1210_2n2_X7R_10%_CER_2kV	C1206	C1	CAP CER 2200PF 1KV 10% X7R 1210	
2	3	100n	ANY	C0402_100n_X7R_10%_CER_50V	C0402	C4, C5, C7	CAP CER 0.1UF 50V 10% X7R 0402	
3	1	MH6-1	ANY	MH6-1-0.1	MH6-1-0.1	J1	CONN HEADER VERT .100 1ROW 6POS 8.08 HEAD 3.05 TAIL 15AU	
4	1	FR04-2	ANY	FR04-2-0.1	FR04-2-0.1	J2	CONN RECEPT VERT .100 2ROWS 4POS 2.92 TAIL 8.64 BODY 15AU	
5	1	B50(27-51)FP4-151-A613-B12	BROADTOP ELECTRONIC	B50(27-51)FP4-151-A613-B12	B50(27-51)FP4-151-A613-B12	J3	CONN MAGJACK 1PORT 100 BASE-T	
6	4	49R9	ANY	R0402_49R9_1%_62.5mW	R0402	R5, R6, R7, R8	RES 49.9 OHM 1/16W 1% 0402 SMD	