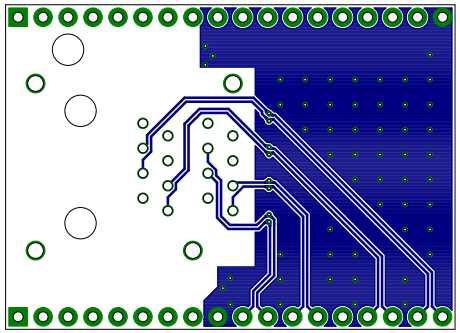
LAYER-STACK

DRILL CHART: TOP TO BOTTOM

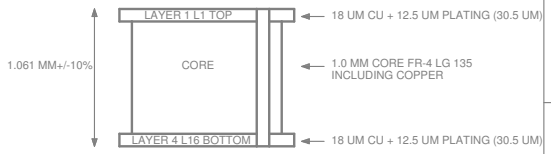
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	61	YES
×	2	35	0.90	16	YES
◳	3	40	1.02	36	YES
◇	4	63	1.60	4	YES
⧖	5	126	3.20	3	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B	
Layer	Impedance	Trace Width	Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils	6 mils	6 mils

STACK-UP FOR REFERENCE



NOTES:

1. PRINTED CIRCUIT BOARD MADE FROM NEMA GRADE FR-4 TG 135 EPOXY LAMINATE WITH 18 UM COPPER PLATING AND 1 MM THICKNESS.

2. ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE.

3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.

4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.

5. BOARD PLATED USING REFLOW OR SIMILAR METHOD.

6. BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY.
EQUIVALENT WET OR DRY FILM MAY BE USED.

7. SILKSCREEN BOARD USING BLACK INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE

8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:

* 2 COPPER LAYERS
* 1 MM BOARD THICKNESS
* REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

Domino Core
Domino Pi
Domino Qi

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Domino Dual Ethernet

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Solder side (.SOL)

Rev. B