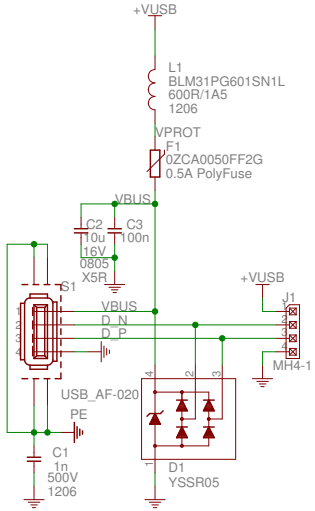


Resistors are 5% 1/16W 0402 unless otherwise specified
Ceramic capacitors are ±10% 50V X7R dielectric 0402 unless otherwise specified

Domino Core
Domino Pi
Domino Qi

Domino Single USB

23/07/14 00:14

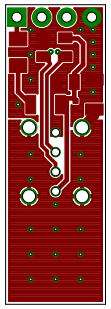
Sheet: 1/1

Rev.A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

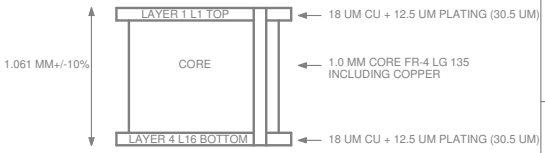
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◇	4	53	1.35	4	YES



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



NOTES:

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2. ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE.
3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.
4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
5. BOARD PLATED USING REFLOW OR SIMILAR METHOD.
6. BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY.
EQUIVALENT WET OR DRY FILM MAY BE USED.
7. SILKSCREEN BOARD USING BLACK INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE
8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:
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   * 1 MM BOARD THICKNESS
   * REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

Domino Core
Domino Pi
Domino Qi

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Domino Single USB

23/07/14 00:17

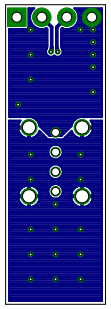
Component Side (.CMP)

Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

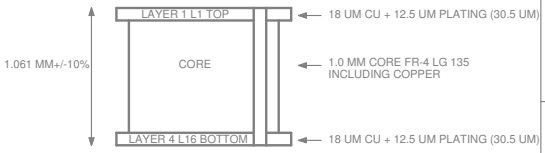
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◇	4	53	1.35	4	YES



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

Domino Single USB

23/07/14 00:17

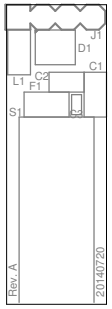
Solder side (.SOL)

Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

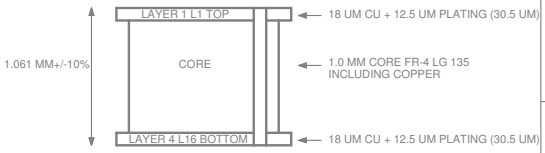
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◈	4	53	1.35	4	YES



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

Domino Single USB

23/07/14 00:17

Silk screen CMP (.PLC) | Rev. A

LAYER-STACK

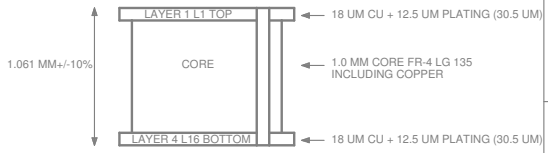
DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◈	4	53	1.35	4	YES

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core
Domino Pi
Domino Qi

Domino Single USB

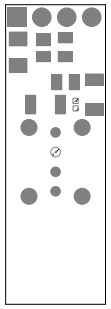
23/07/14 00:17

Silk screen SOL (.PLS) | Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

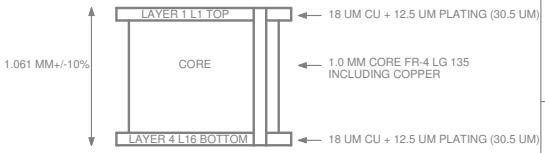
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◈	4	53	1.35	4	YES



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Qi

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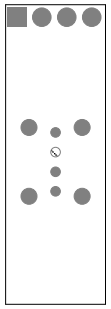
23/07/14 00:17

Solder stop mask CMP (.STC) | Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

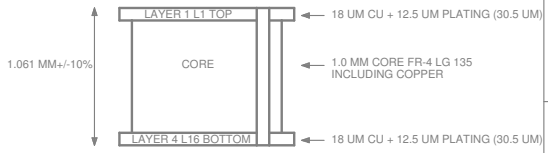
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◈	4	53	1.35	4	YES



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core
Domino Pi
Domino Qi

Domino Single USB

23/07/14 00:17

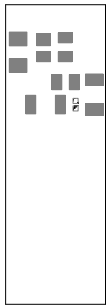
Solder stop mask SOL (.STS)

Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

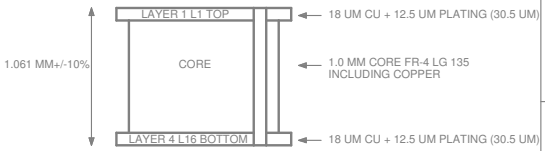
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◇	4	53	1.35	4	YES



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

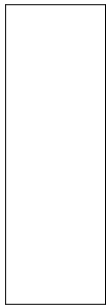
Domino Single USB

23/07/14 00:17

Cream frame CMP (.CRC)

Rev. A

LAYER-STACK



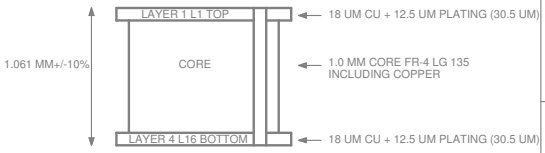
DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◈	4	53	1.35	4	YES

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core

Domino Pi

Domino Qi

Domino Single USB

23/07/14 00:17

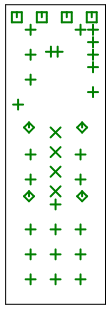
Cream frame SOL (.CRS)

Rev. A

LAYER-STACK
01-16

DRILL CHART: TOP TO BOTTOM

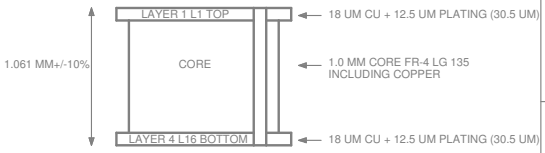
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◈	4	53	1.35	4	YES



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Domino Core
Domino Pi
Domino Qi

Domino Single USB

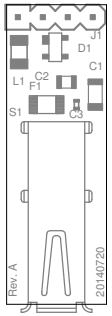
23/07/14 00:17

Drill data (.DRD) | Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

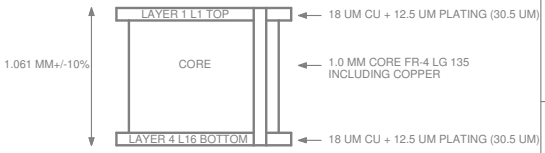
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◈	4	53	1.35	4	YES



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



NOTES:

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3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.
4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
5. BOARD PLATED USING REFLOW OR SIMILAR METHOD.
6. BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY.
EQUIVALENT WET OR DRY FILM MAY BE USED.
7. SILKSCREEN BOARD USING BLACK INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE
8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:
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   * 1 MM BOARD THICKNESS
   * REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

Domino Core

Domino Pi

Domino Qi

Domino Single USB

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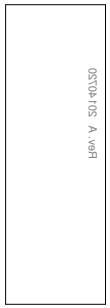
Component Assembly CMP (.ASC) | Rev. A

LAYER-STACK



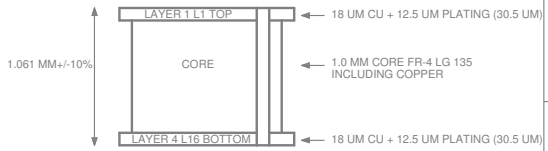
DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
◈	4	53	1.35	4	YES

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	USB	Type	Diff Coated Coplanar Waveguide With Ground 1B		
Layer	Impedance	Trace Width		Trace Separation	Ground Separation
TOP, BOTTOM	90 Ohms	14.5 mils		6 mils	6 mils

STACK-UP FOR REFERENCE



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Component Assembly SOL (.ASS) | Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	26	YES
×	2	31	0.80	4	YES
◳	3	40	1.02	4	YES
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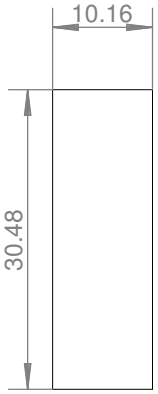

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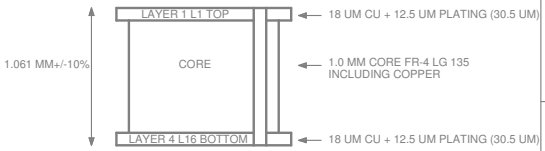

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Outline (.OLN) | Rev. A

LAYER-STACK

DRILL CHART: TOP TO BOTTOM

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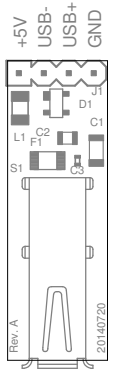
◈	4	53	1.35	4	YES



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Layer	Impedance	Trace Width		Trace Separation	Ground Separation
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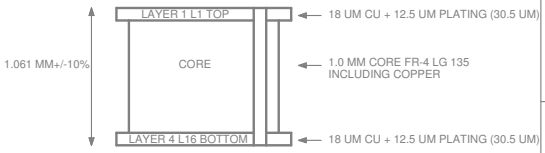

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Pinout (.PIN)

Rev. A

Domino Single USB Rev. A

Item	Qty	Value	Manufacturer	Device	Package	Reference	Description	Remarks
1	1	1n	ANY	C1206_1n_X7R_10%_CER_500V	C1206	C1	CAP CER 1000PF 500V 10% X7R 1206	
2	1	10u	ANY	C0805_10u_X5R_10%_CER_16V	C0805	C2	CAP CER 10UF 16V 10% X5R 0805	
3	1	100n	ANY	C0402_100n_X7R_10%_CER_50V	C0402	C3	CAP CER 0.1UF 50V 10% X7R 0402	
4	1	YSSR05	YEASHIN	YSSR05	SOT143B	D1	TVS DIODE ARRAY 2CH 5V SOT143	
5	1	0ZCA0050FF2G	BEL FUSE INC	0ZCG0110FF2C	PTC1206	F1	PTC RESETTBLE 1.10A 8V CHIP 1812	
6	1	MH4-1	ANY	MH4-1-0.1	MH4-1-0.1	J1	CONN HEADER VERT .100 1ROW 4POS 8.08 HEAD 3.05 TAIL 15AU	
7	1	BLM31PG601SN1L	MURATA	BLM31PG601SN1L	FB1206	L1	FERRITE CHIP 600 OHM 1500MA 1206	
8	1	USB_AF-020	SZJUSTWELL ELECTRONICS	USB_AF-020	USB_AF-020	S1	CONN USB A RECPT T/H R/A VERT	