

This figure displays a technical drawing of a Domino Dual Ethernet board, showing its dimensions, layer stack, drill chart, impedance chart, and various notes.

The board is labeled "Domino Core", "Domino Pi", and "Domino Qi". It features a Creative Commons Attribution-ShareAlike license (CC-BY-SA).

LAYER-STACK

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	39	YES
x	2	35	0.90	20	YES
□	3	39	1.00	12	YES
◇	4	40	1.02	10	YES
⊠	5	40	1.02	8	YES
⌘	6	59	1.50	4	YES
+̣	7	126	3.20	1	NOT
+̣	8	128	3.25	4	NOT

DRILL CHART: TOP TO BOTTOM

The drill chart shows a grid of holes with symbols corresponding to the Layer Stack table. The grid includes a central area with various hole patterns and four circular holes at the bottom corners.

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B
Layer	Impedance	Trace Width	Trace Separation
TOP, BOTTOM	100 Ohms	10 mils	6 mils

STACK-UP FOR REFERENCE

The stack-up diagram shows a cross-section of the board with the following layers:

- LAYER 1 L1 TOP: 18 UM CU + 12.5 UM PLATING (30.5 UM)
- CORE: 1.0 MM CORE FR-4 LG 135 INCLUDING COPPER
- LAYER 4 L16 BOTTOM: 18 UM CU + 12.5 UM PLATING (30.5 UM)

The total thickness is indicated as 1.061 MM +/- 10%.

NOTES:

- PRINTED CIRCUIT BOARD MADE FROM NEMA GRADE FR-4 TG 135 EPOXY LAMINATE WITH 18 UM COPPER PLATING AND 1 MM THICKNESS.
- ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE.
- CIRCUIT PATHS ARE FOR REFERENCE ONLY.
- HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
- BOARD PLATED USING REFLOW OR SIMILAR METHOD.
- BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY. EQUIVALENT WET OR DRY FILM MAY BE USED.
- SILKSCREEN BOARD USING WHITE INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE.
- THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:
 - * 2 COPPER LAYERS
 - * 1 MM BOARD THICKNESS
 - * REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

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Domino Dual Ethernet

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Drill data (.DRD)

Rev. A