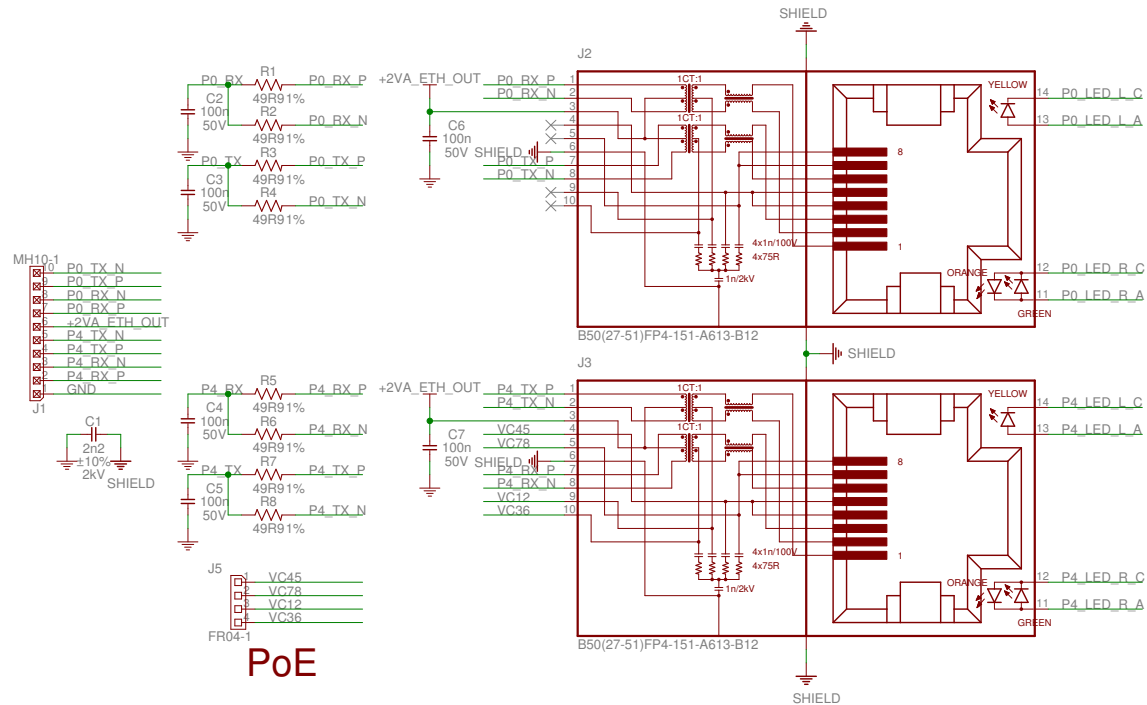
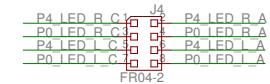


LAN on port P0, DCE wiring (1=R+, 2=R-, 3=T+, 6=T-)

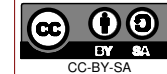


LEDS



WAN on port P4, DTE wiring (1=T+, 2=T-, 3=R+, 6=R-)

Resistors are 5% 1/16W 0402 unless otherwise specified
Ceramic capacitors are ±10% 50V X7R dielectric 0402 unless otherwise specified



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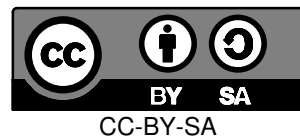
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Domino Dual Ethernet
22/07/14 23:18
Sheet: 1 / 1 Rev.A

DRILL CHART: TOP TO BOTTOM

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

STACK-UP FOR REFERENCE

NOTES:



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Domino Dual Ethernet

22/07/14 23:27

Component Side (.CMP)

Rev. A

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	39	YES
×	2	35	0.90	20	YES
□	3	39	1.00	12	YES
◇	4	40	1.02	10	YES
×	5	40	1.02	8	YES
⊗	6	59	1.50	4	YES
⋈	7	126	3.20	1	NOT
⋈	8	128	3.25	4	NOT

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B	
Layer	Impedance	Trace Width	Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils	6 mils	6 mils

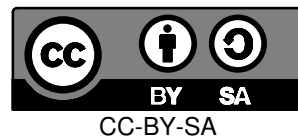
10%

LAYER 1 L1 TOP ← 18 UM CU + 12.5 UM PLATING (30.5 UM)

CORE ← 1.0 MM CORE FR-4 LG 135 INCLUDING COPPER

LAYER 4 L16 BOTTOM ← 18 UM CU + 12.5 UM PLATING (30.5 UM)

1. PRINTED CIRCUIT BOARD MADE FROM NEMA GRADE FR-4 TO 135 EPOXY LAMINATE WITH 18 UM COPPER PLATING AND 1 MM THICKNESS.
2. ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE
3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.
4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
5. BOARD PLATED USING REFLOW OR SIMILAR METHOD.
6. BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY.
EQUIVALENT WET OR DRY FILM MAY BE USED.
7. SILKSCREEN BOARD USING WHITE INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE
8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:
 - * 2 COPPER LAYERS
 - * 1 MM BOARD THICKNESS
 - * REQUIRES TOP AND BOTTOM SIDE SILKSCREENS



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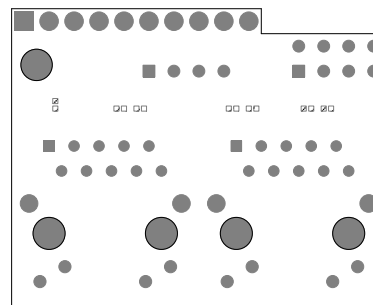
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Silk screen SOL (.PLS)

Rev. A

DRILL CHART: TOP TO BOTTOM

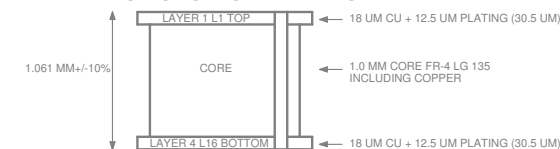
Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	39	YES
×	2	35	0.90	20	YES
□	3	39	1.00	12	YES
◇	4	40	1.02	10	YES
⊗	5	40	1.02	8	YES
⊠	6	59	1.50	4	YES
✕	7	126	3.20	1	NOT
⦿	8	128	3.25	4	NOT



LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B	
Layer	Impedance	Trace Width	Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils	6 mils	6 mils

STACK-UP FOR REFERENCE



NOTES:

1. PRINTED CIRCUIT BOARD MADE FROM NEMA GRADE FR-4 TO 135 EPOXY LAMINATE WITH 18 UM COPPER PLATING AND 1 MM THICKNESS.
2. ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE.
3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.
4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
5. BOARD PLATED USING REFLOW OR SIMILAR METHOD.
6. BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY.
EQUIVALENT WET OR DRY FILM MAY BE USED.
7. SILKSCREEN BOARD USING WHITE INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE.
8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:

- * 2 COPPER LAYERS
- * 1 MM BOARD THICKNESS
- * REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

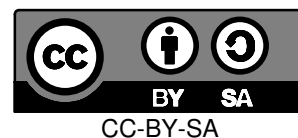
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Solder stop mask CMP (.STC)

Rev. A



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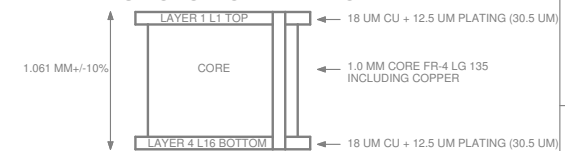
DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	39	YES
×	2	35	0.90	20	YES
□	3	39	1.00	12	YES
◇	4	40	1.02	10	YES
⊗	5	40	1.02	8	YES
⊗	6	59	1.50	4	YES
⊕	7	126	3.20	1	NOT
⊕	8	128	3.25	4	NOT

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B	
Layer	Impedance	Trace Width	Trace Separation	Ground Separation
TOP, BOTTOM	100 Ohms	10 mils	6 mils	6 mils

STACK-UP FOR REFERENCE



NOTES:

1. PRINTED CIRCUIT BOARD MADE FROM NEMA GRADE FR-4 TG 135 EPOXY LAMINATE WITH 18 UM COPPER PLATING AND 1 MM THICKNESS.
2. ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE.
3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.
4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
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8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:

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- * 1 MM BOARD THICKNESS
- * REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

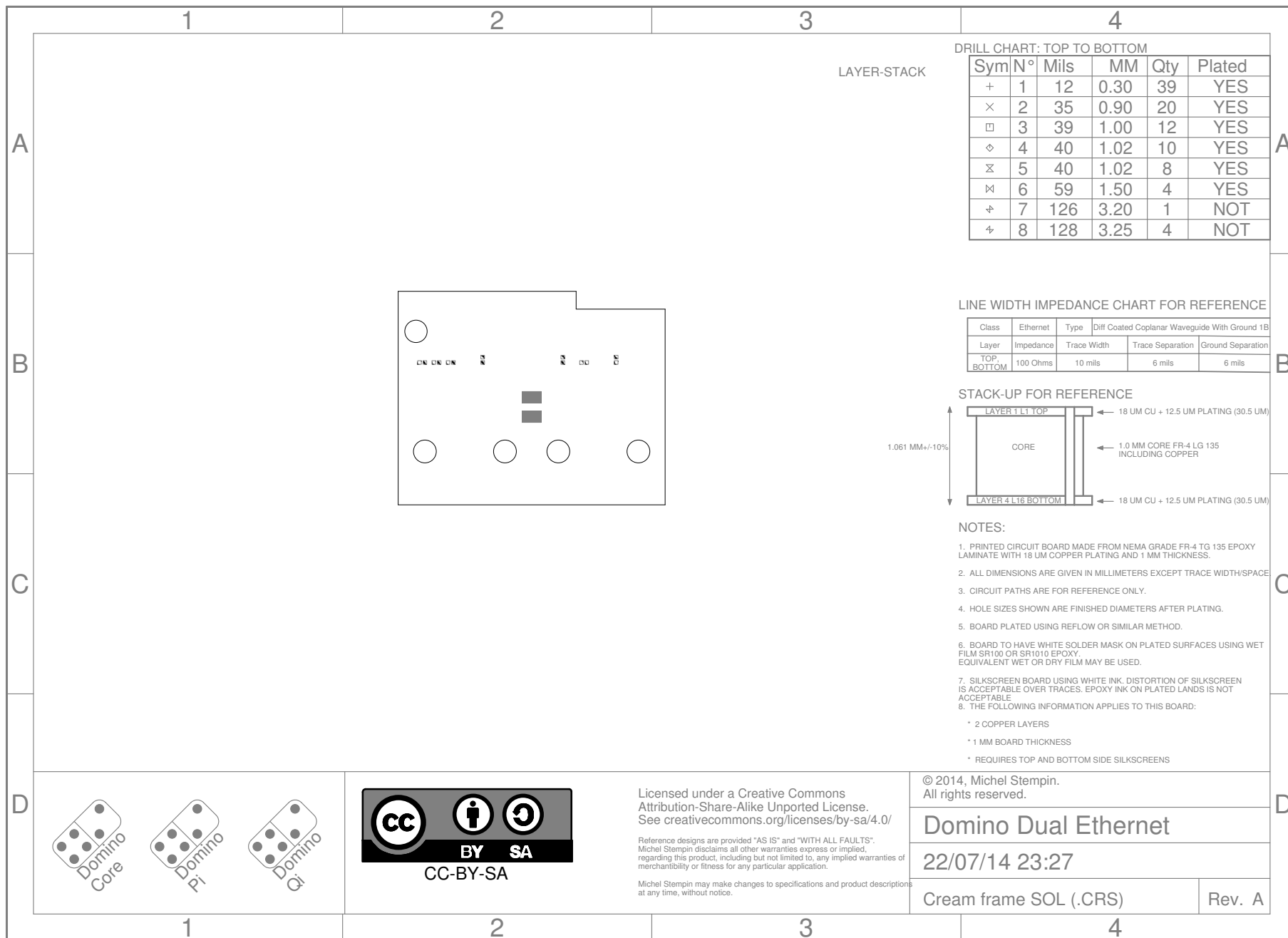
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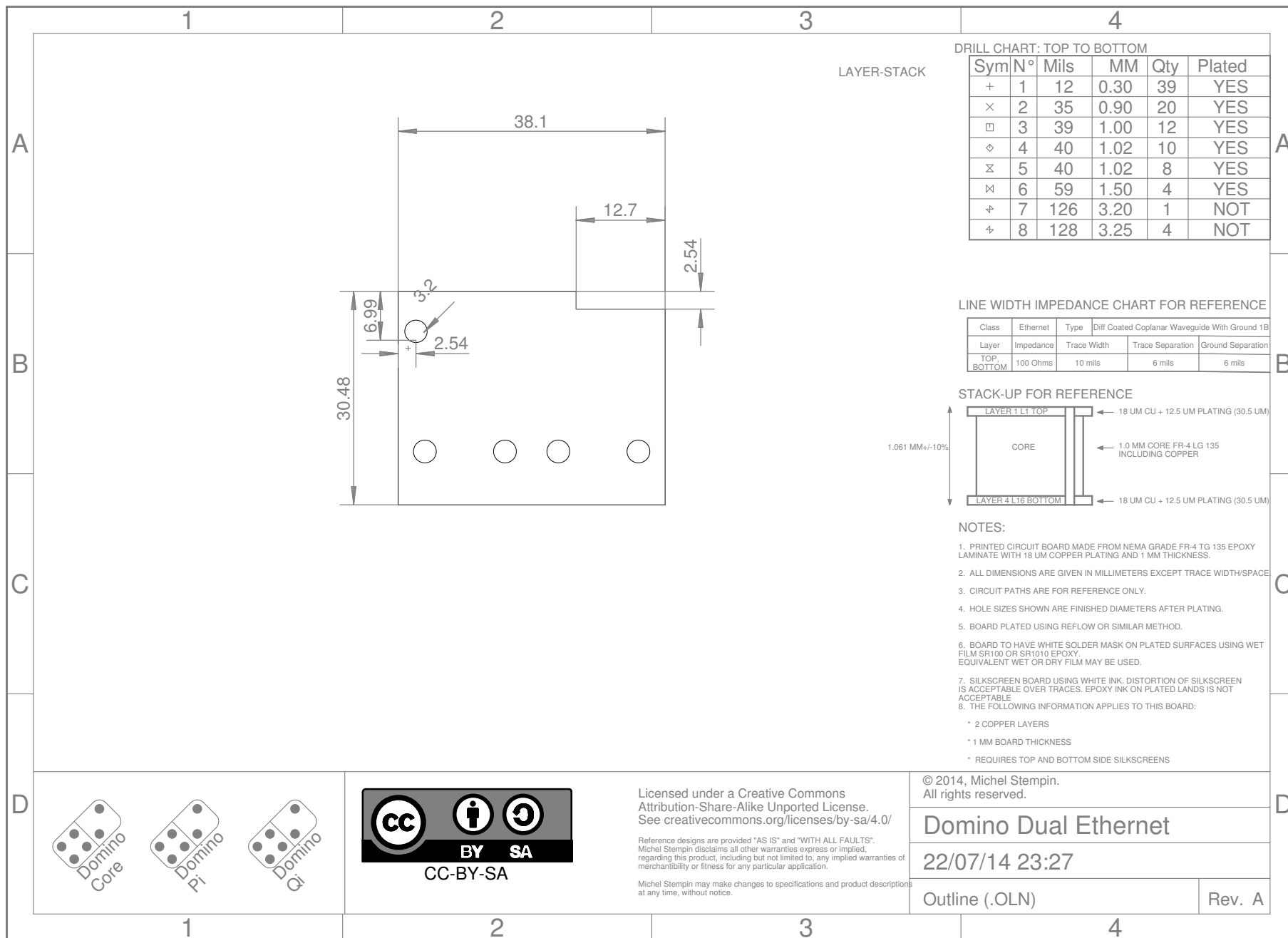
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Solder stop mask SOL (.STS)

Rev. A





Domino Dual Ethernet Rev. A

Item	Qty	Value	Manufacturer	Device	Package	Reference	Description	Remarks
1	1	2n2	ANY	C1210_2n2_X7R_10%_CER_2kV	C1210	C1	CAP CER 2200PF 1KV 10% X7R 1210	
2	6	100n	ANY	C0402_100n_X7R_10%_CER_50V	C0402	C2, C3, C4, C5, C6, C7	CAP CER 0.1UF 50V 10% X7R 0402	
3	1	MH10-1	ANY	MH10-1-0.1	MH10-1-0.1	J1	CONN HEADER VERT .100 1ROW 10POS 8.08 HEAD 3.05 TAIL 15AU	
4	2	B50(27-51)FP4-151-A613-B12	BROADTOP ELECTRONIC	B50(27-51)FP4-151-A613-B12	B50(27-51)FP4-151-A613-B12	J2, J3	CONN MAGJACK 1PORT 100 BASE-T	
5	1	FR04-2	ANY	FR04-2-0.1	FR04-2-0.1	J4	CONN RECEPT VERT .100 2ROWS 4POS 2.92 TAIL 8.64 BODY 15AU	
6	1	FR04-1	ANY	FR04-1-0.1	FR04-1-0.1	J5	CONN RECEPT VERT .100 1ROW 4POS 2.92 TAIL 8.64 BODY 15AU	
7	8	49R9	ANY	R0402_49R9_1%_62.5mW	R0402	R1, R2, R3, R4, R5, R6, R7, R8	RES 49.9 OHM 1/16W 1% 0402 SMD	