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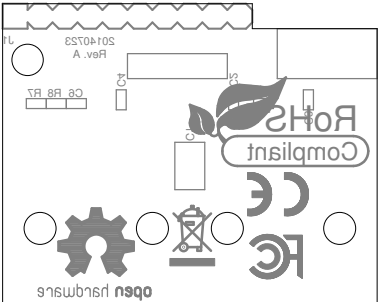
A

B

C

D

LAYER-STACK



DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	39	YES
×	2	35	0.90	20	YES
□	3	39	1.00	12	YES
◇	4	40	1.02	10	YES
⊠	5	40	1.02	8	YES
⊞	6	59	1.50	4	YES
⊕	7	126	3.20	1	NOT
⊗	8	128	3.25	4	NOT

LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	Ethernet	Type	Diff Coated Coplanar Waveguide With Ground 1B
Layer	Impedance	Trace Width	Trace Separation
TOP	100 Ohms	10 mils	6 mils
BOTTOM			6 mils

STACK-UP FOR REFERENCE

1.061 MM +/- 10%

LAYER 1 L1 TOP

CORE

LAYER 4 L16 BOTTOM

18 UM CU + 12.5 UM PLATING (30.5 UM)

1.0 MM CORE FR-4 LG 135 INCLUDING COPPER

18 UM CU + 12.5 UM PLATING (30.5 UM)

NOTES:

1. PRINTED CIRCUIT BOARD MADE FROM NEMA GRADE FR-4 TG 135 EPOXY LAMINATE WITH 18 UM COPPER PLATING AND 1 MM THICKNESS.

2. ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE

3. CIRCUIT PATHS ARE FOR REFERENCE ONLY.

4. HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.

5. BOARD PLATED USING REFLOW OR SIMILAR METHOD.

6. BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM SR100 OR SR1010 EPOXY. EQUIVALENT WET OR DRY FILM MAY BE USED.

7. SILKSCREEN BOARD USING WHITE INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE

8. THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:

* 2 COPPER LAYERS

* 1 MM BOARD THICKNESS

* REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

Domino Core

Domino Pi

Domino Qi

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Domino Dual Ethernet

23/07/14 00:36

Silk screen SOL (.PLS)

Rev. A

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