LI-CHUNG CHIANG

EDUCATION

B.S. in Computer Science and Information Engineering

National Taiwan University, Taiwan — *Sep. 2018—Jan. 2023*

M.S. in Computer Science and Information Engineering

National Taiwan University, Taiwan — *Jan. 2023—Jan. 2025*

WORK EXPERIENCE

Research Assistant

Research Center for IT Innovation, Academia Sinica, Taiwan — *2021—2024*

Teaching Assistant

Introduction to Cryptography, National Taiwan University, Taiwan — *2024 Spring*

Teaching Assistant

Virtual Machines, National Taiwan University, Taiwan — *2024 Fall*

RESEARCH EXPERIENCE

Cache and memory contention side channels on AMD SEV

- Reverse-engineer a caching mechanism on AMD SEV.
- Discover novel side channels related to cache and memory on AMD SEV.
- Exploit the disclosed side channels to attack AES in OpenSSL and build a covert channel in a Spectre attack.

Cache side-channel attack against Romulus

- Devise a cache attack scheme against the lightweight cipher Romulus.

PUBLICATIONS

Li-Chung Chiang and Shih-Wei Li, Reload+Reload: Exploiting Cache and Memory Contention Side Channel on AMD SEV, Proceedings of the 30th ACM International Conference on Architectural Support for Programming Languages and Operating Systems, Volume 2 (**ASPLOS 25**).

SKILLS

Programming: C, Python, Assembly.

Language: Mandarin (native), English

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