

A

A

U_POWER
POWER.SchDoc

U_CLOCK_GENERATOR
CLOCK_GENERATOR.SchDoc

U_CONNECTIONS
CONNECTIONS.SchDoc

U_FPGA
FPGA.SchDoc

U_STM32
STM32.SchDoc

U_AUDIO CODEC
AUDIO CODEC.SchDoc

B

B

U_DAC
DAC.SchDoc

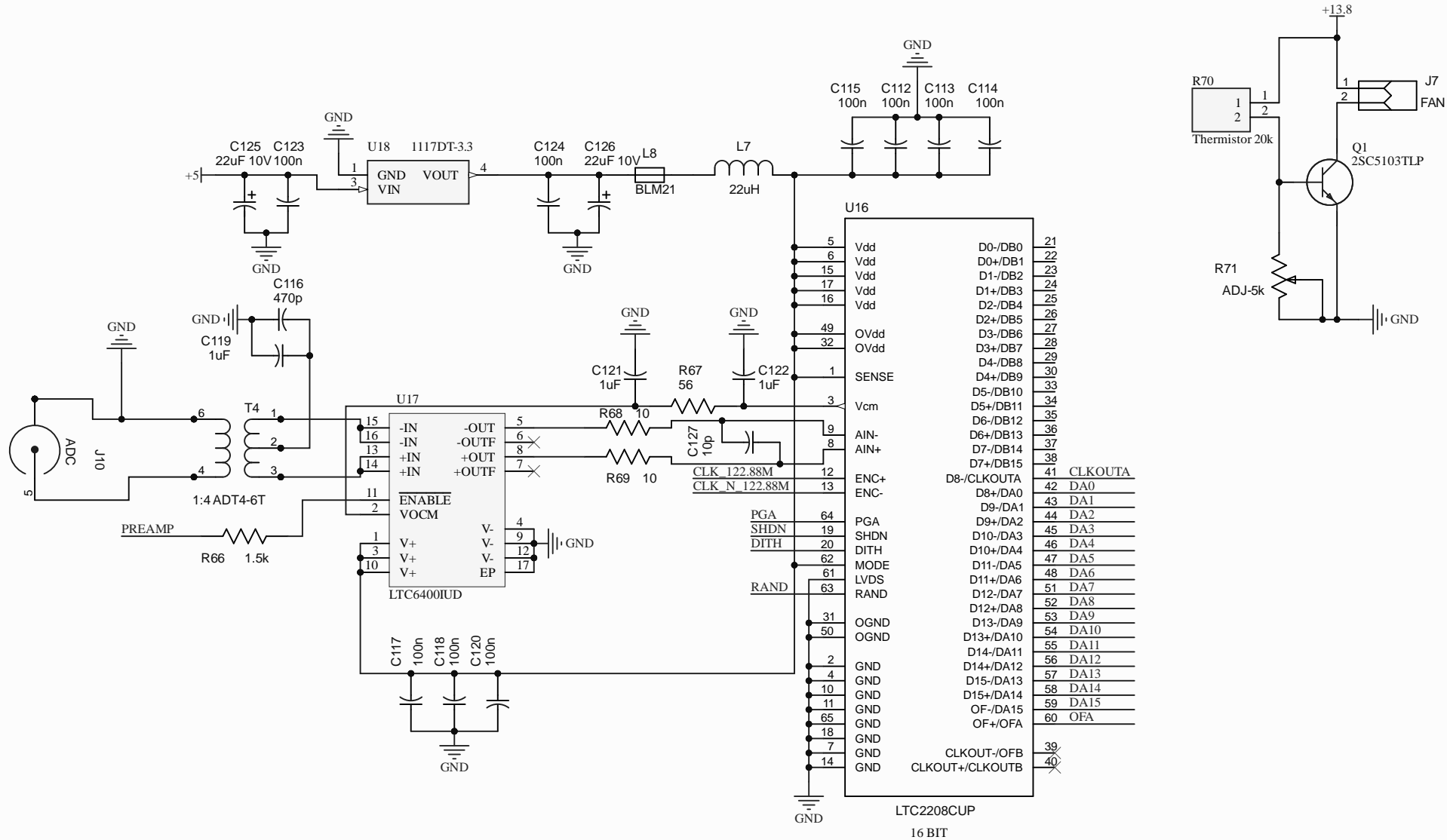
U_ADC
ADC.SchDoc

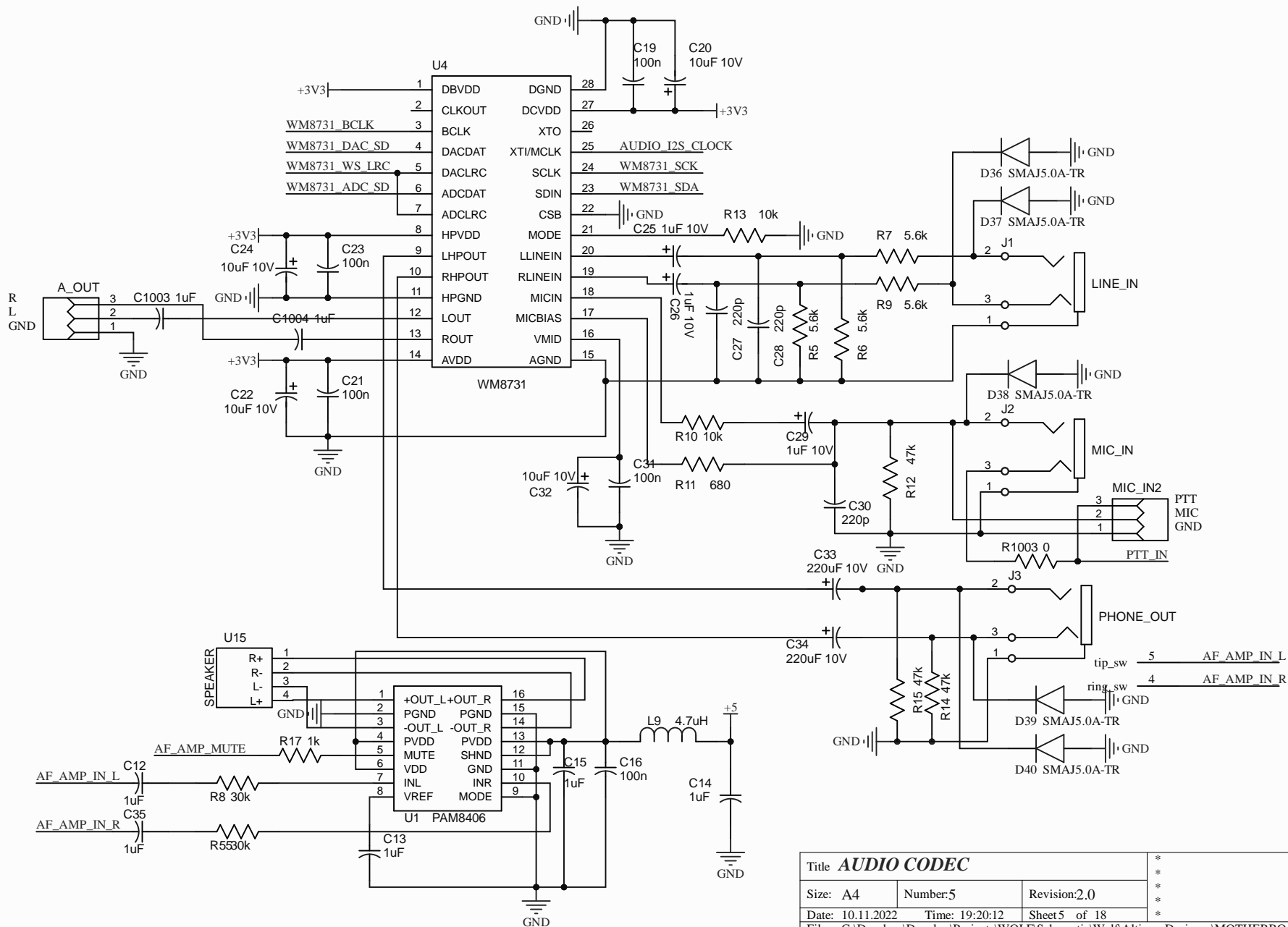
C

C

D

D





Title **AUDIO CODEC**

Size: A4

Number:5

Revision:2,0

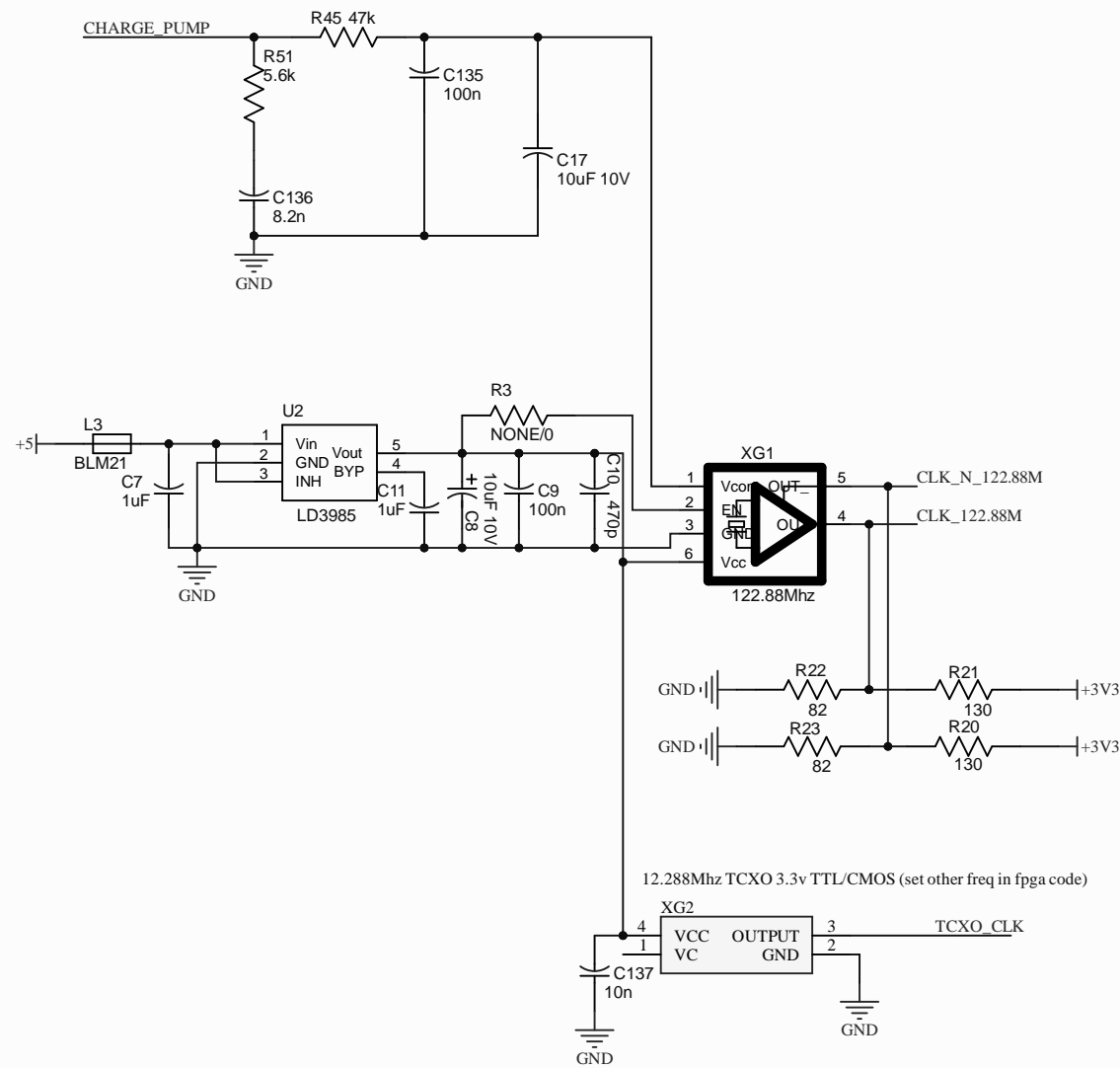
Date: 10.11.2022

Time: 19:20:12

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File: C:\Dropbox\Develop\Projects\WOLF\Schematic\Wolf\Altium_Designer\MOTHERBOARD\AUDIO CODEC.SchDoc





Title **CLOCK_GENERATOR**

Size: A4

Number:6

Revision:2,0

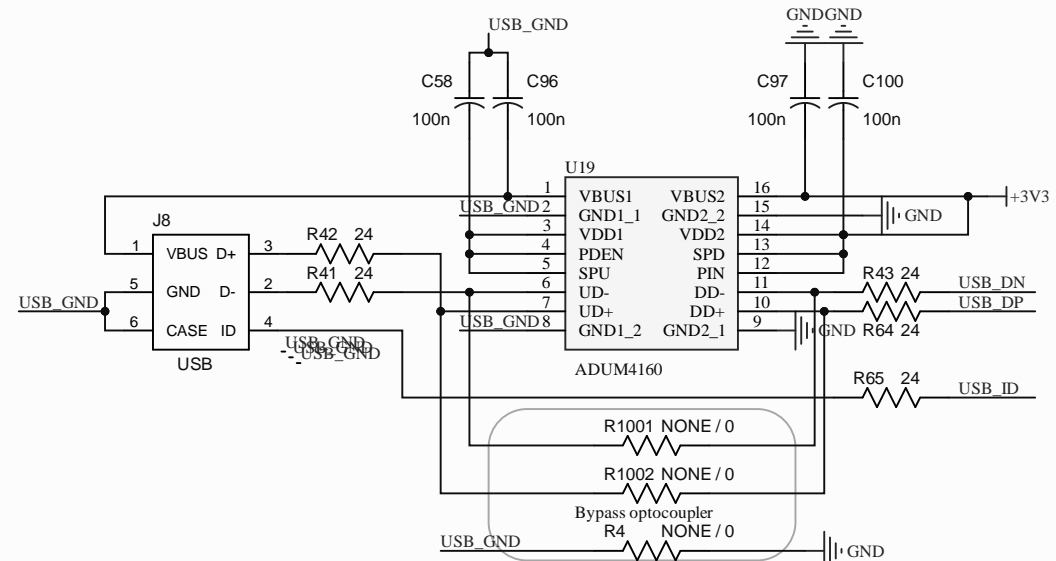
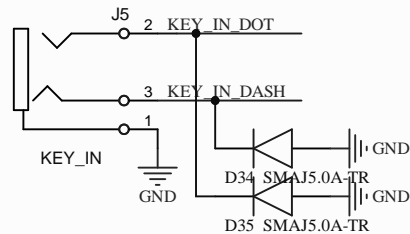
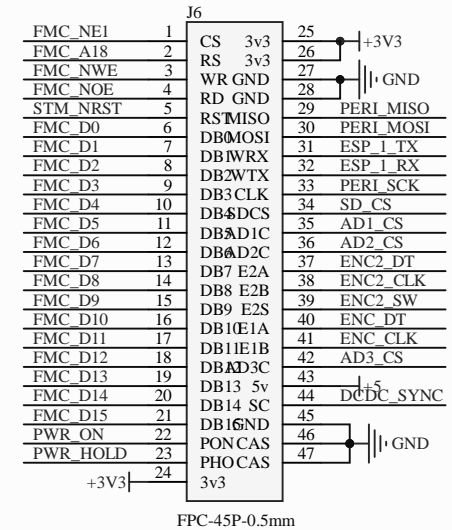
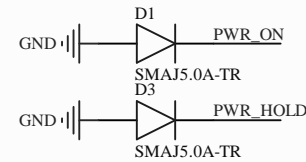
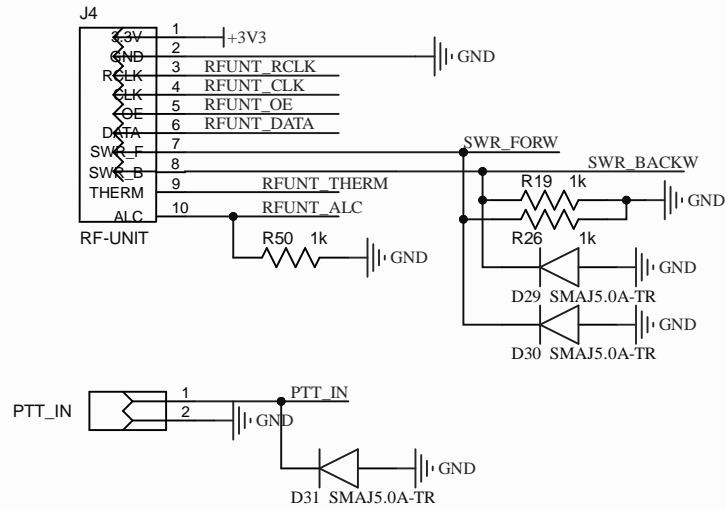
Date: 10.11.2022

Time: 19:20:12

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File: C:\Dropbox\Develop\Projects\WOLF\Schematic\Wolf\Altium_Designer\MOTHERBOARD\CLOCK_GENERATOR.SchD



Title **CONNECTIONS**

Size: A4

Number:7

Revision:2.0

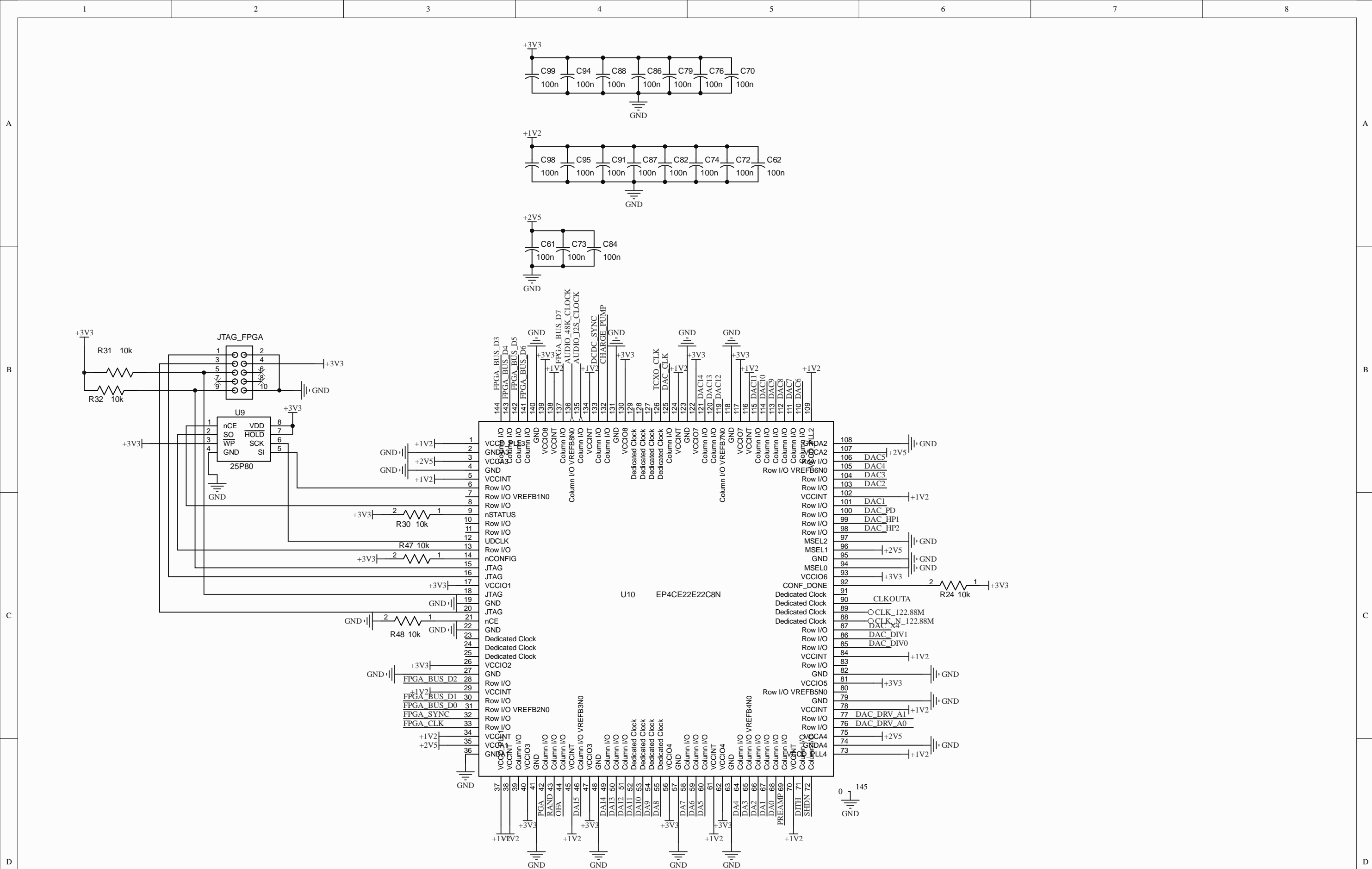
Date: 10.11.2022

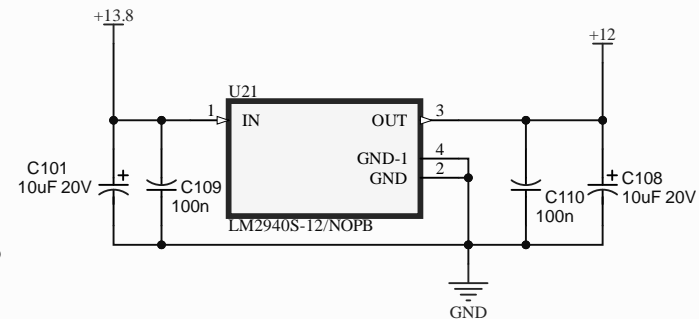
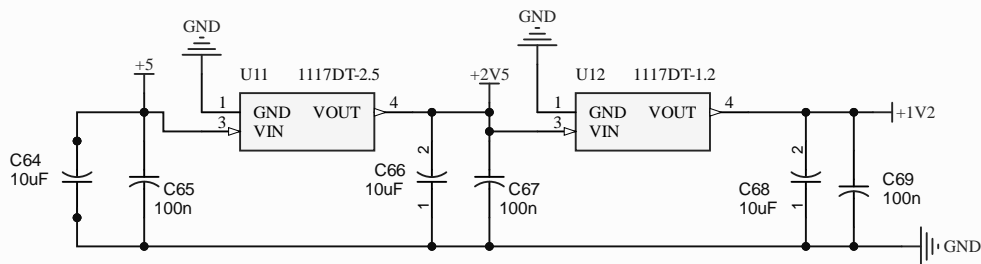
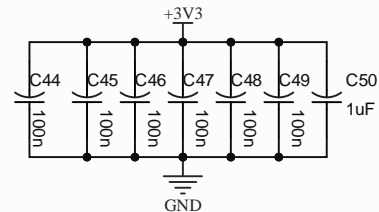
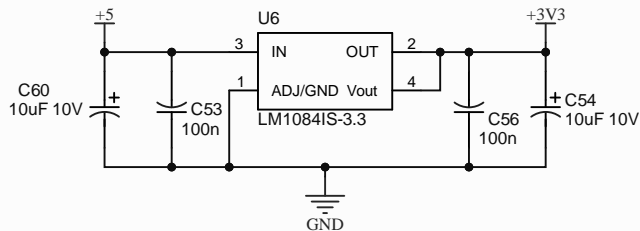
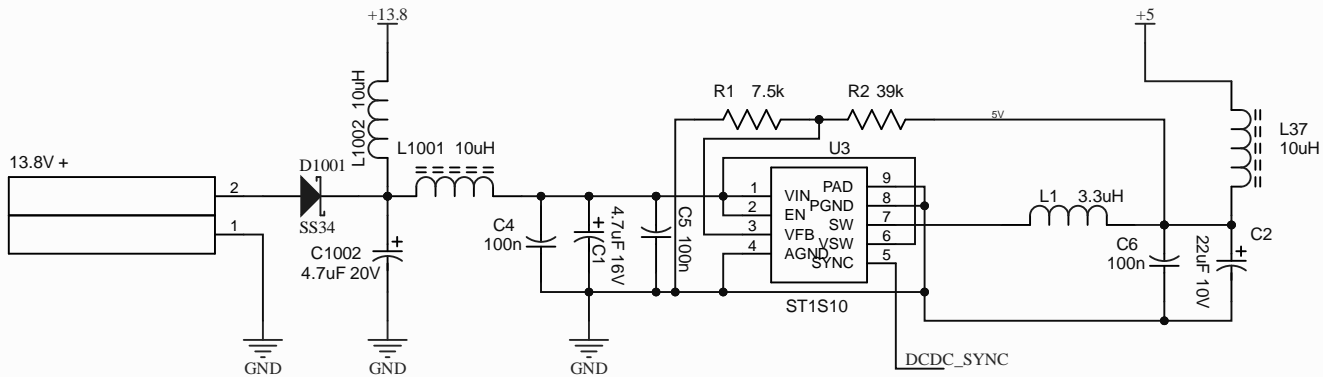
Time: 19:20:12

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File: C:\Dropbox\Develop\Projects\WOLF\Schematic\Wolf\Altium_Designer\MOTHERBOARD\CONNECTIONS.SchDoc







Title **POWER**

Size: A4

Number: 10

Revision: 2.0

Date: 10.11.2022

Time: 19:20:12

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File: C:\Dropbox\Develop\Projects\WOLF\Schematic\Wolf\Altium_Designer\MOTHERBOARD\POWER.SchDoc



