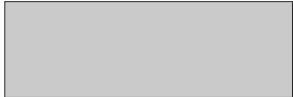
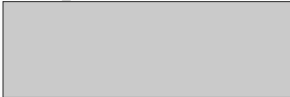


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POWER.SchDoc



U_CLOCK_GENERATOR
CLOCK_GENERATOR.SchDoc



U_CONNECTIONS
CONNECTIONS.SchDoc



U_FPGA
FPGA.SchDoc



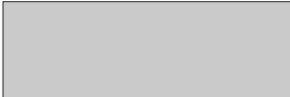
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STM32.SchDoc



U_AUDIO CODEC
AUDIO CODEC.SchDoc

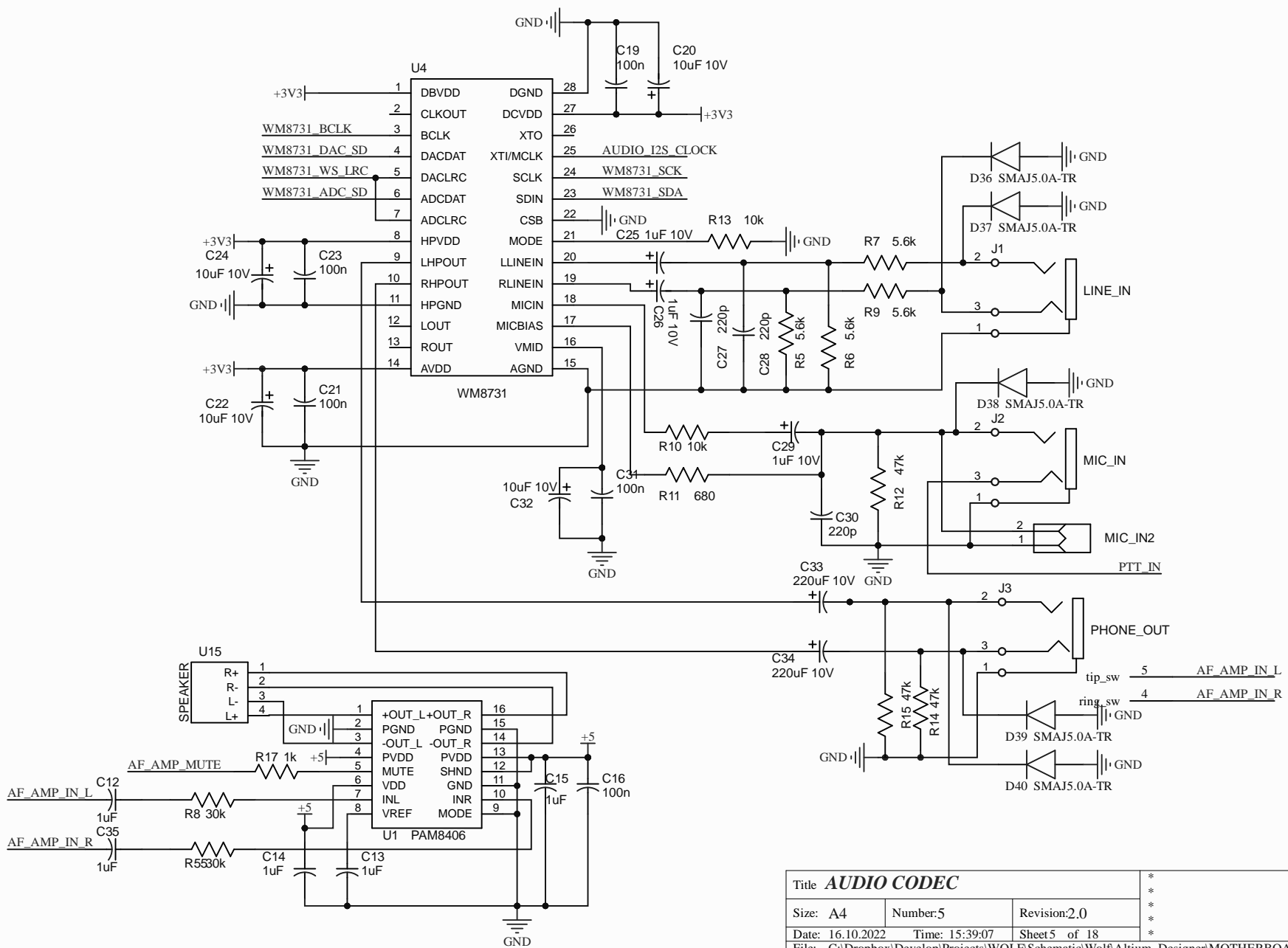


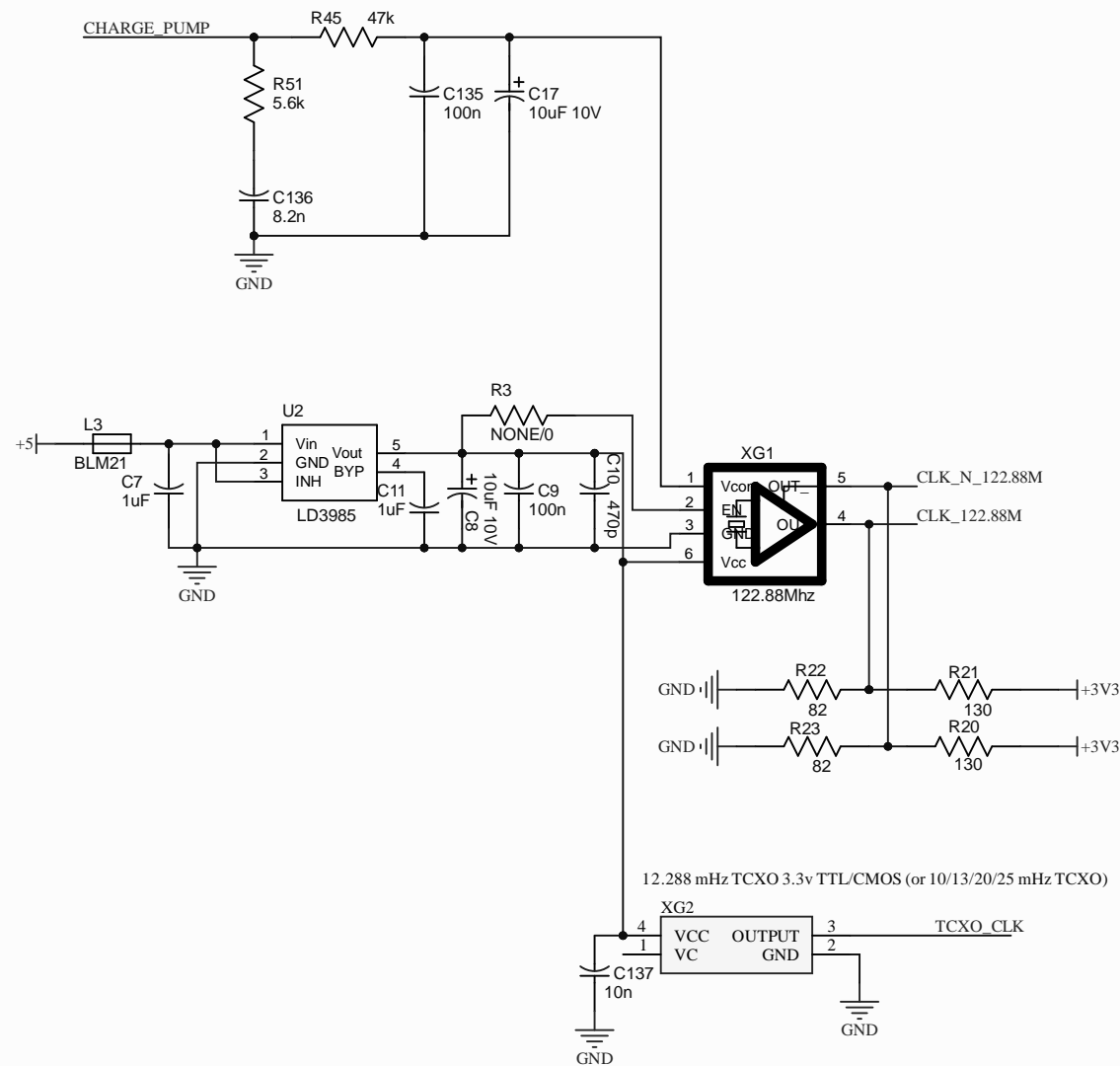
U_DAC
DAC.SchDoc



U_ADC
ADC.SchDoc







Title **CLOCK_GENERATOR**

Size: A4

Number:6

Revision:2,0

Date: 16.10.2022

Time: 15:39:07

Sheet 6 of 18

File: C:\Dropbox\Develop\Projects\WOLF\Schematic\Wolf\Altium_Designer\MOTHERBOARD\CLOCK_GENERATOR.SchD



