

A

A

U_POWER
POWER.SchDoc

U_CLOCK_GENERATOR
CLOCK_GENERATOR.SchDoc

U_CONNECTIONS
CONNECTIONS.SchDoc

U_FPGA
FPGA.SchDoc

U_STM32
STM32.SchDoc

U_AUDIO CODEC
AUDIO CODEC.SchDoc

B

B

U_DAC
DAC.SchDoc

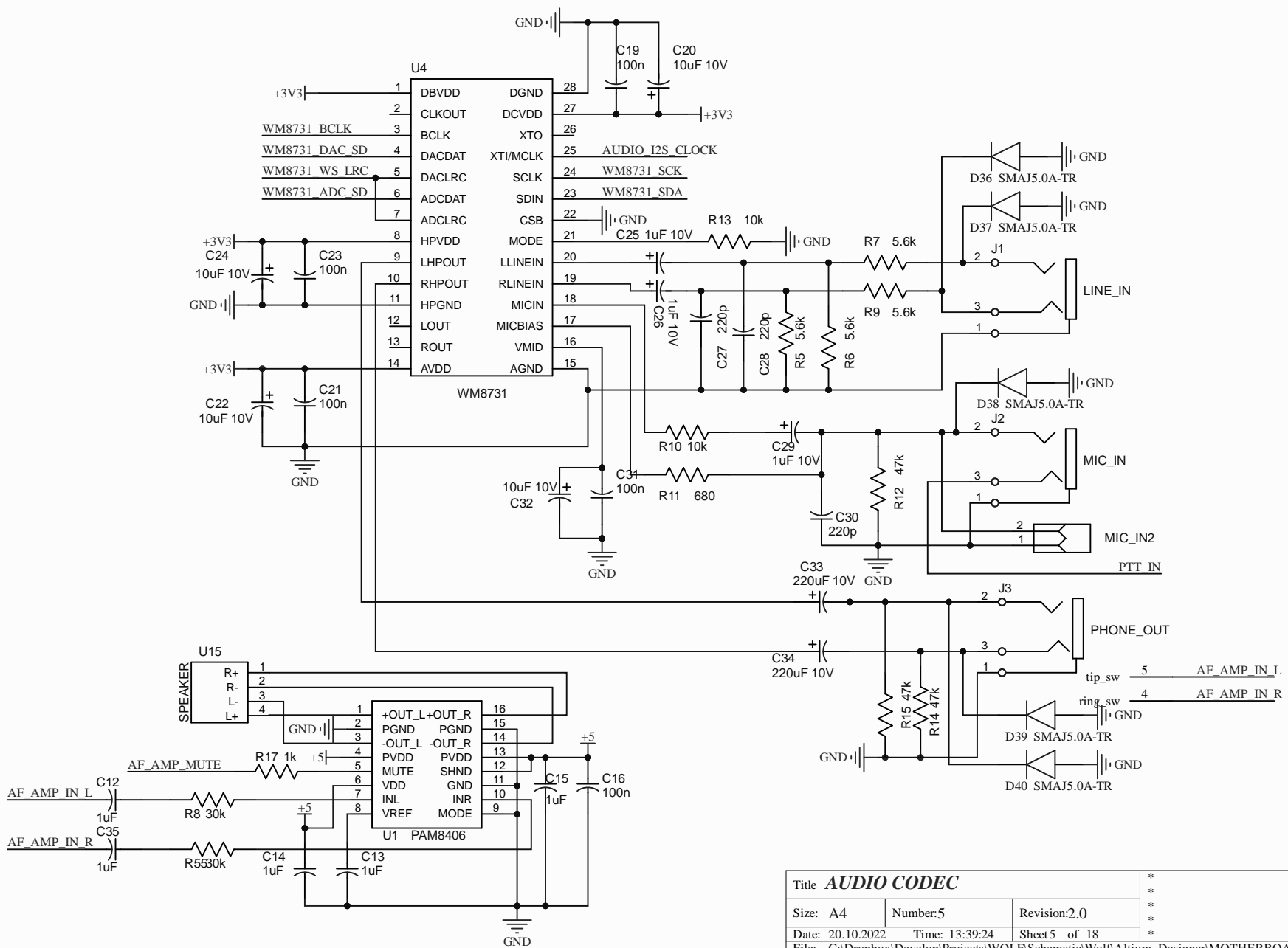
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ADC.SchDoc

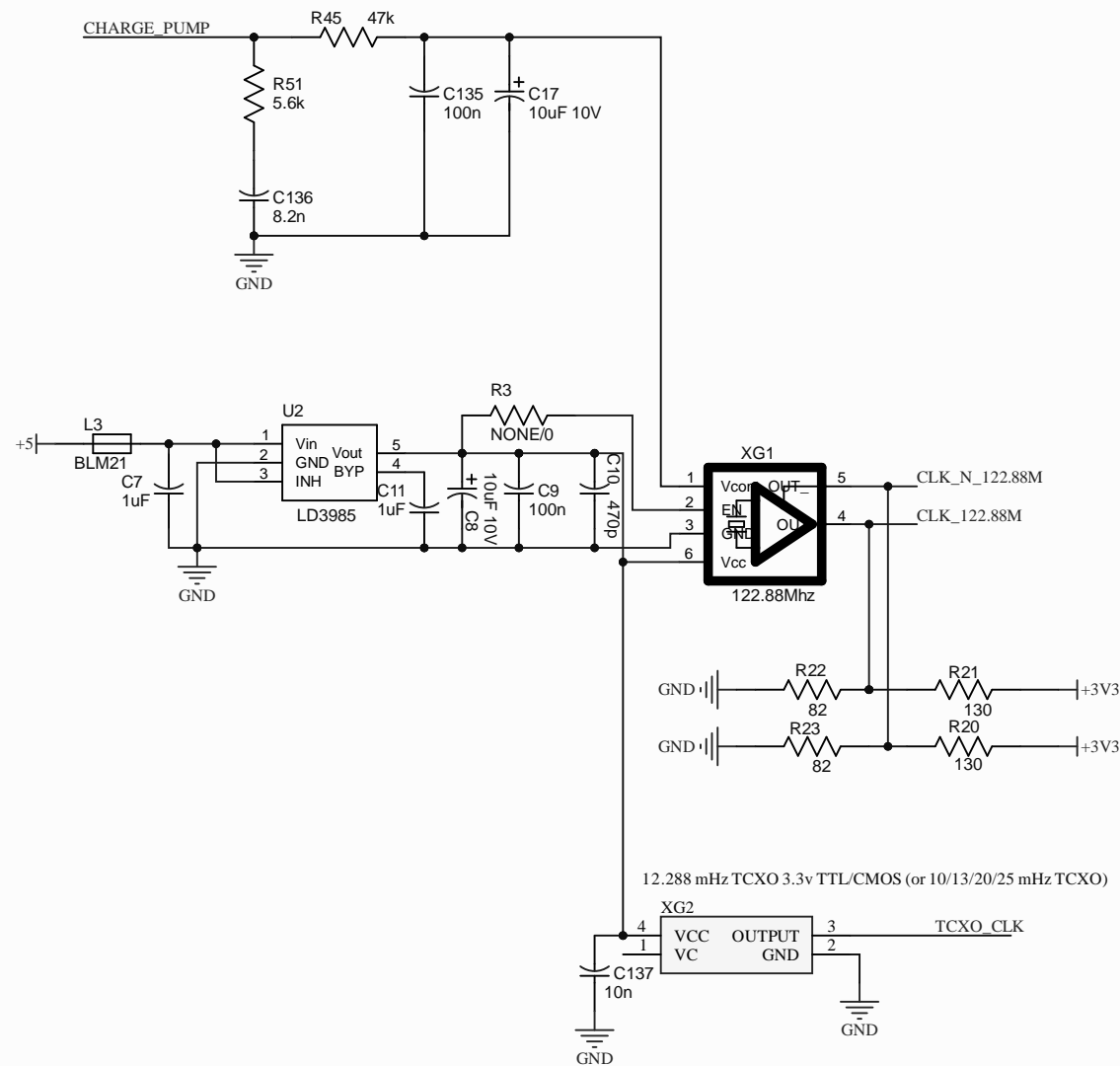
C

C

D

D





Title **CLOCK_GENERATOR**

Size: A4

Number:6

Revision:2,0

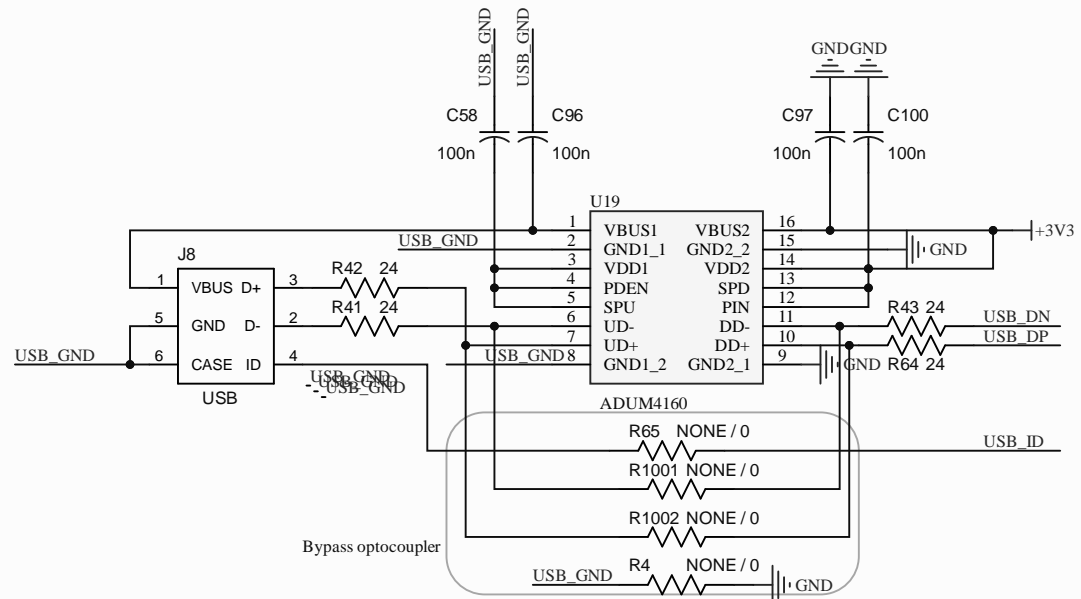
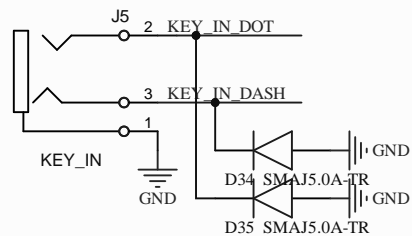
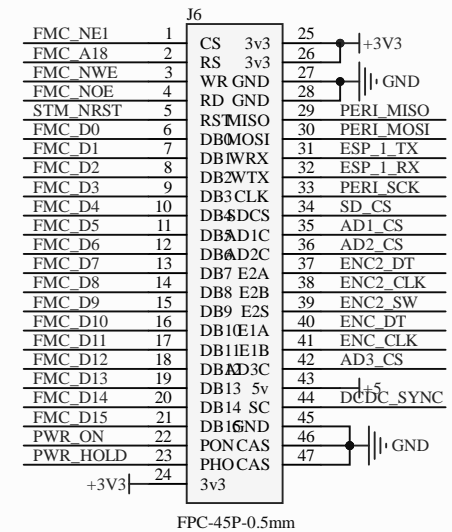
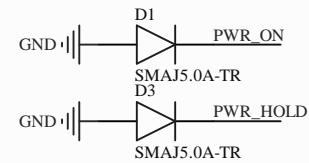
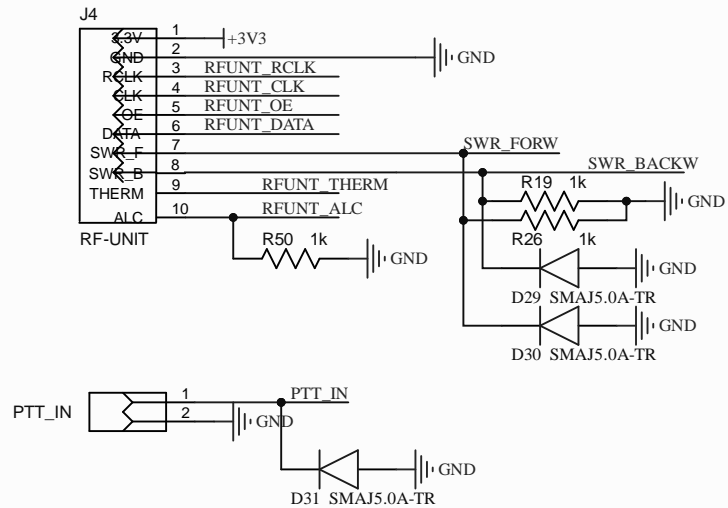
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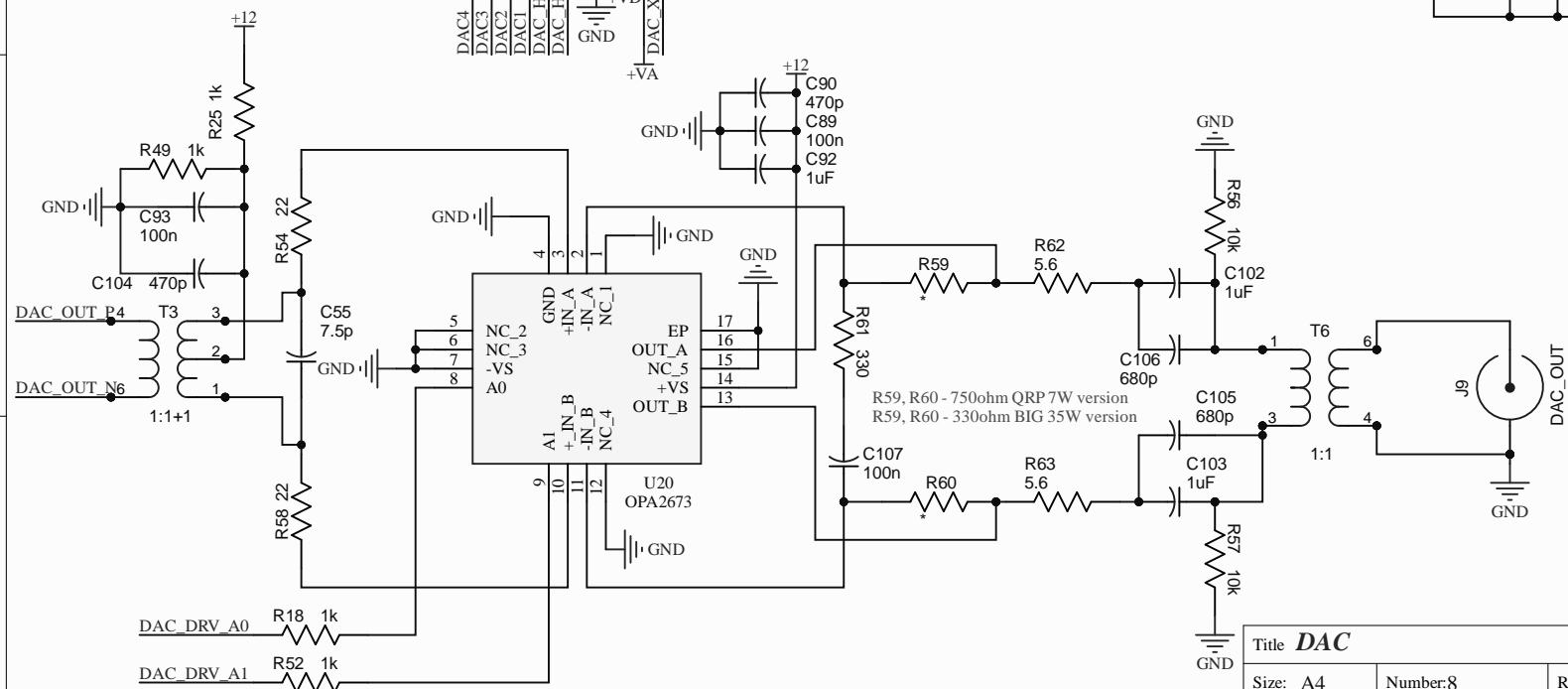
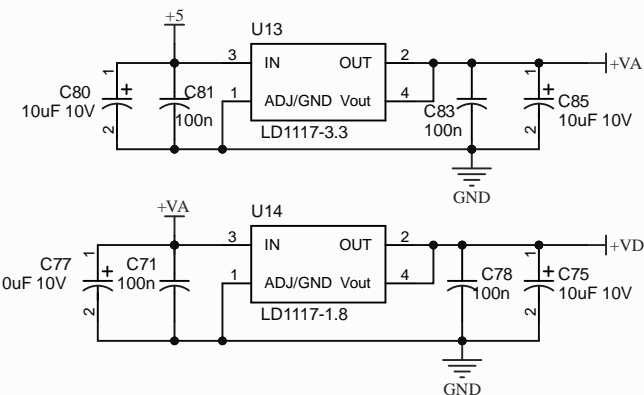
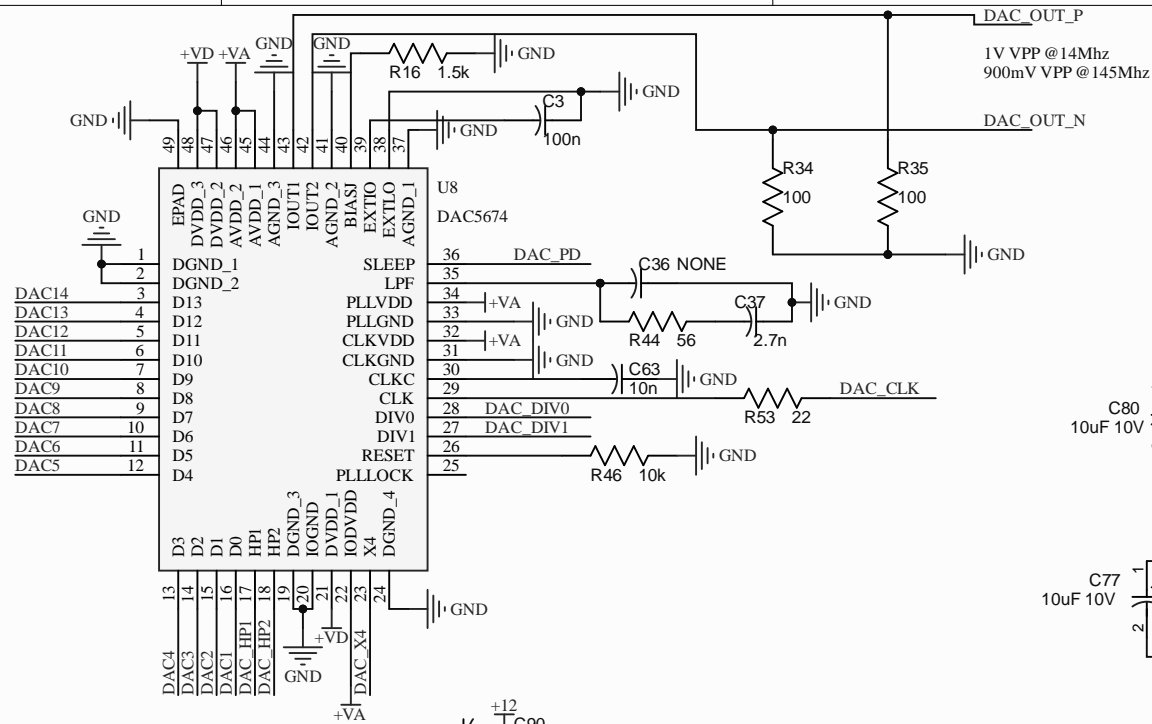
Time: 13:39:24

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File: C:\Dropbox\Develop\Projects\WOLF\Schematic\Wolf\Altium_Designer\MOTHERBOARD\CLOCK_GENERATOR.SchD







10V VPP @ 14Mhz max (250mW)
6V VPP @ 145Mhz max (90mW)

Title DAC			
Size: A4	Number:8	Revision:2.0	
Date: 20.10.2022	Time: 13:39:24	Sheet8 of 18	
File: C:\Dropbox\Develop\Projects\WOLF\Schematic\Wolf\Altium Designer\MOTHERBOARD\DAC.SchDoc			

