

A

A

U_POWER
POWER.SchDoc



U_CLOCK_GENERATOR
CLOCK_GENERATOR.SchDoc



U_CONNECTIONS
CONNECTIONS.SchDoc



U_FPGA
FPGA.SchDoc



U_STM32
STM32.SchDoc



U_AUDIO CODEC
AUDIO CODEC.SchDoc



U_DAC
DAC.SchDoc



U_ADC
ADC.SchDoc



B

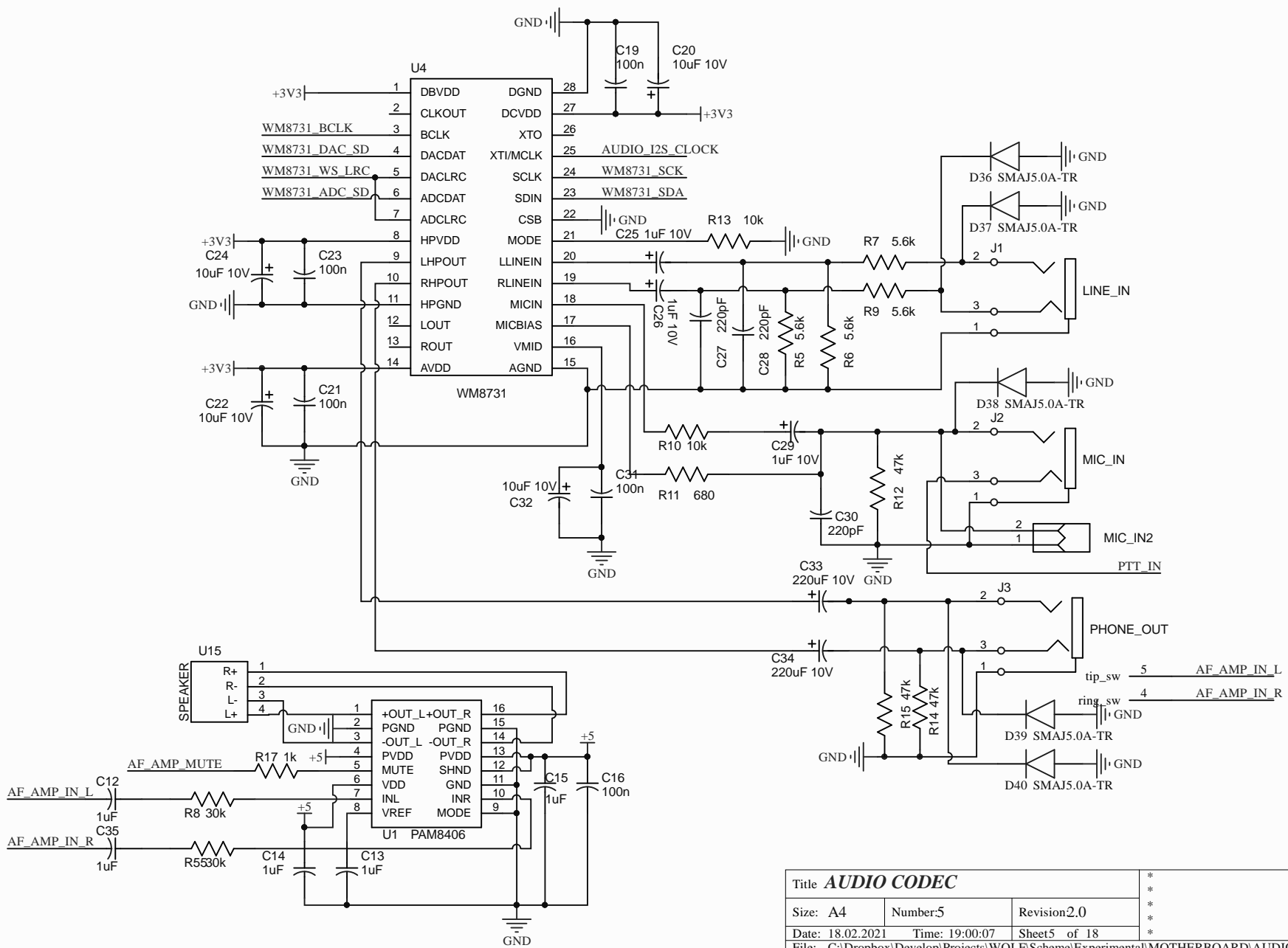
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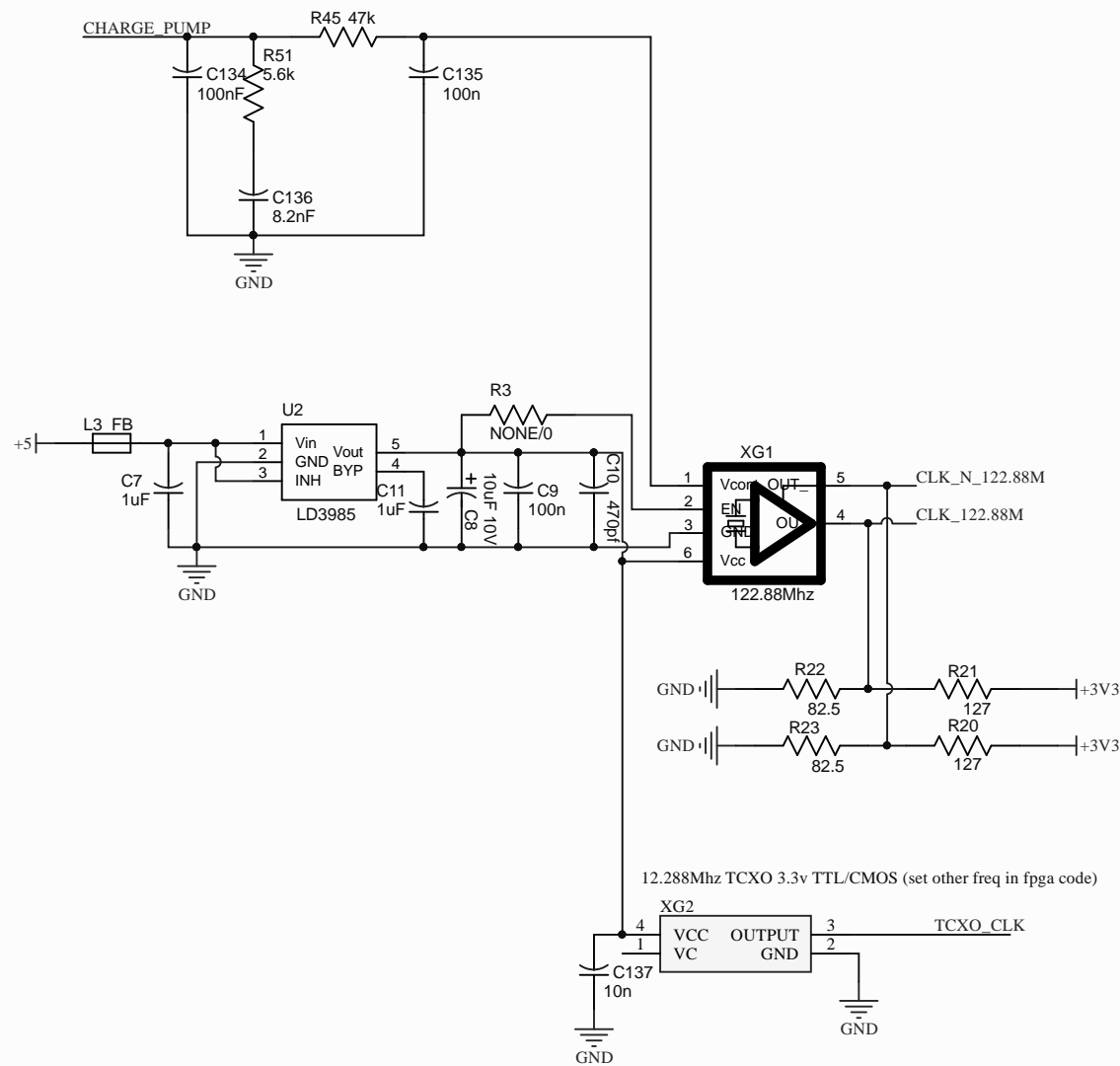
C

C

D

D





Title **CLOCK_GENERATOR**

Size: A4

Number:6

Revision:2.0

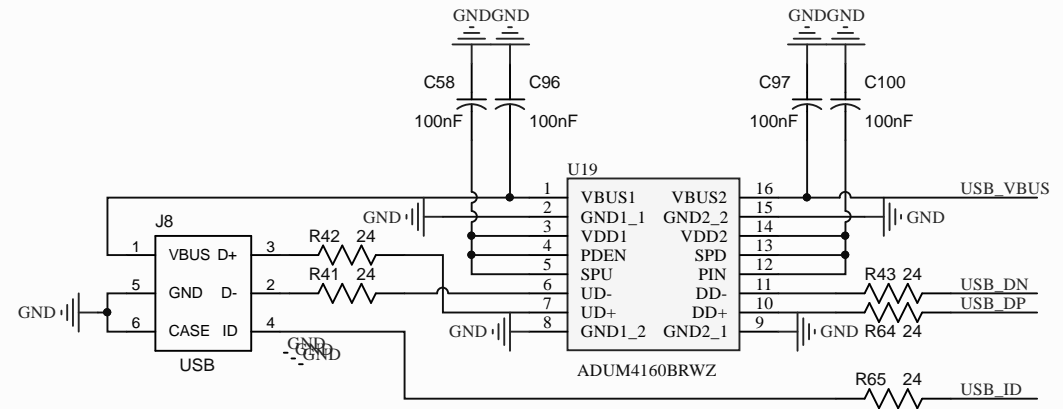
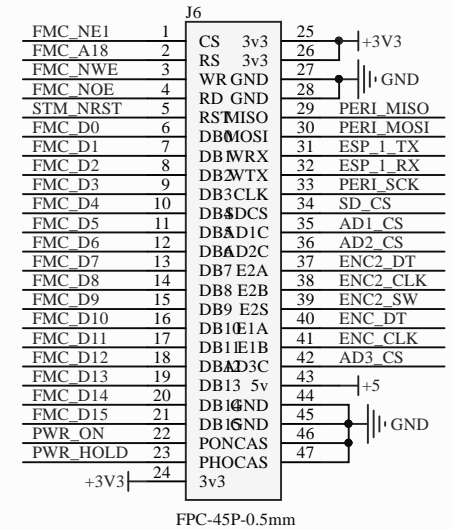
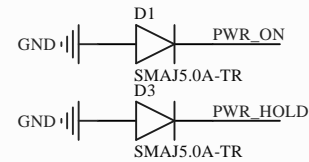
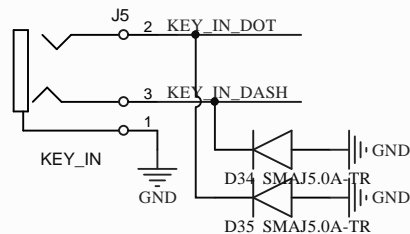
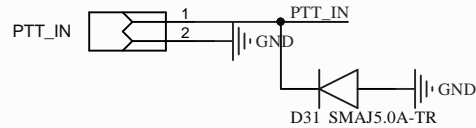
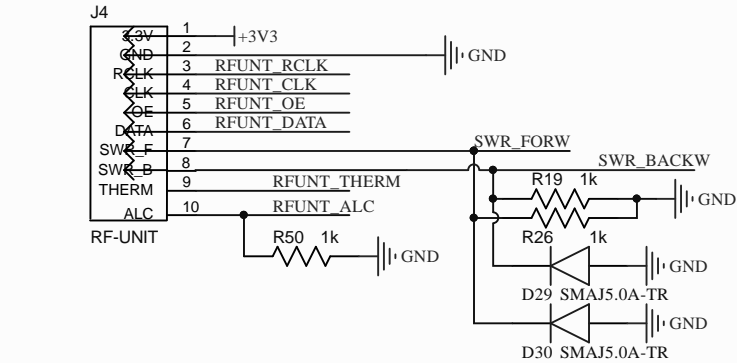
Date: 18.02.2021

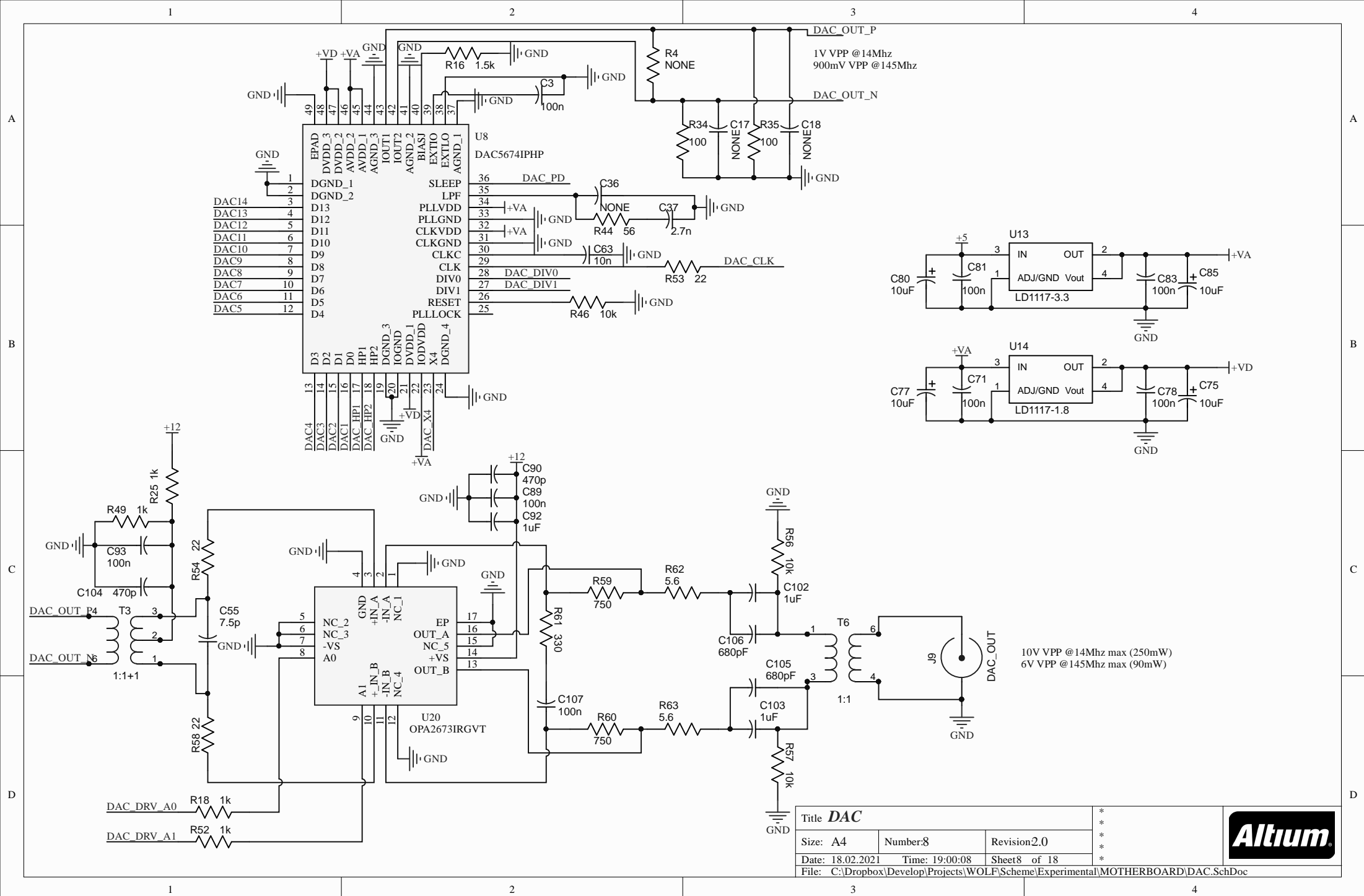
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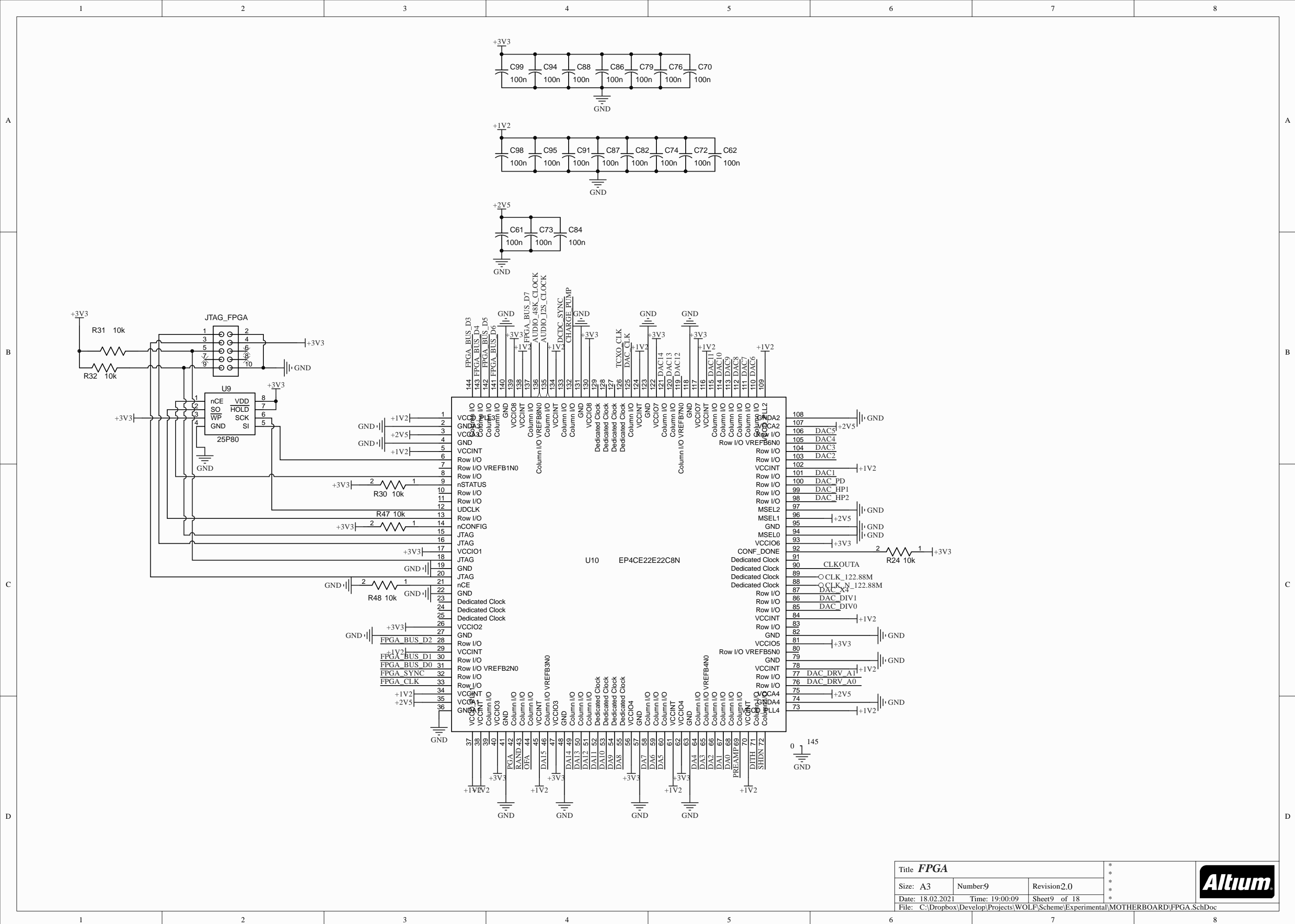
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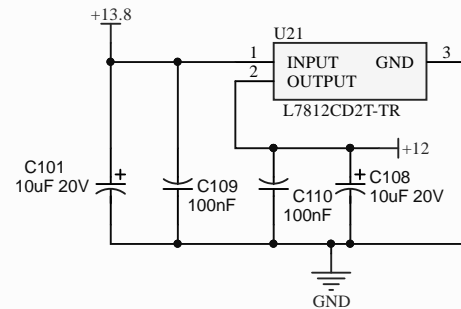
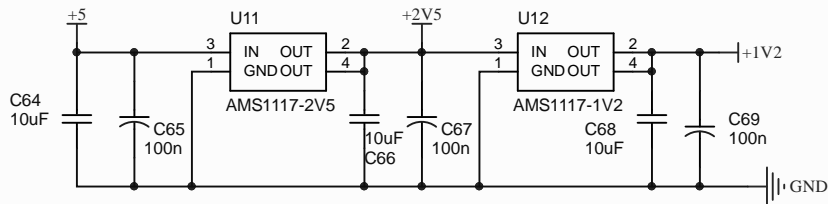
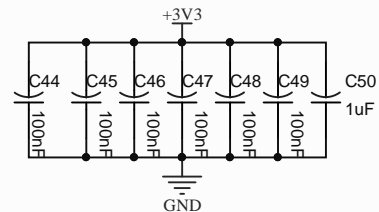
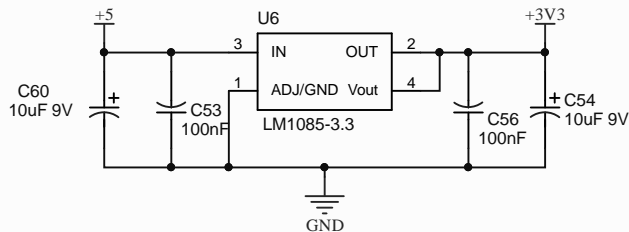
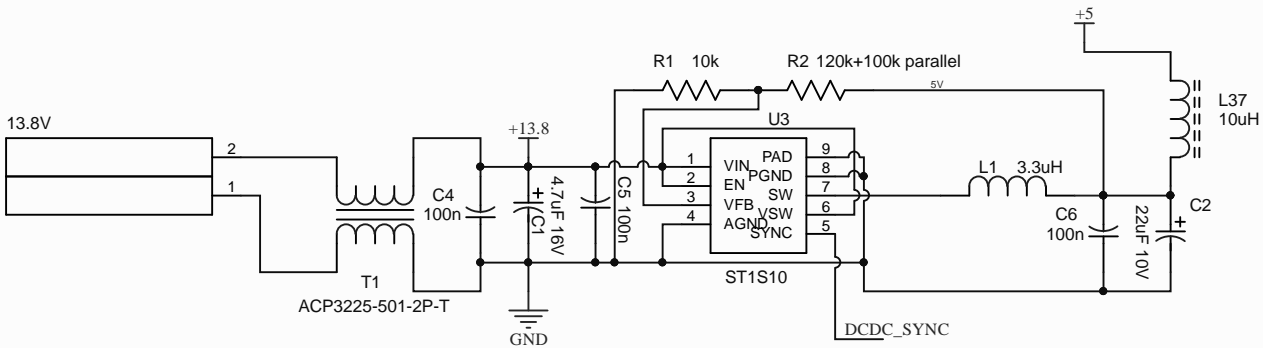
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Title **POWER**

Size: A4

Number:10

Revision:2.0

Date: 18.02.2021

Time: 19:00:09

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