

Count

600

400

200

0

Implementation

CUDA

OpenACC

OpenCL

OpenMP

Opcode
Total Instruction Count
Work-items
Total Barriers Hit
Min ITB
Max ITB
Median ITB
Min IPT
Max IPT
Median IPT
Max SIMD Width
Mean SIMD Width
SD SIMD Width
Total Memory Footprint
Unique Memory Addresses Accessed
Unique Memory Addresses Read
Unique Memory Addresses Written
Unique Read/Write Ratio
Total Reads
Total Writes
Reread Ratio
Rewrite Ratio
90% Memory Footprint
Global Memory Address Entropy
LMAE — Skipped 1 LSBs
LMAE — Skipped 2 LSBs
LMAE — Skipped 3 LSBs
LMAE — Skipped 4 LSBs
LMAE — Skipped 5 LSBs
LMAE — Skipped 6 LSBs
LMAE — Skipped 7 LSBs
LMAE — Skipped 8 LSBs
LMAE — Skipped 9 LSBs
PSL — Skipped 10 LSBs
PSL — Skipped 0 LSBs
PSL — Skipped 1 LSBs
PSL — Skipped 2 LSBs
PSL — Skipped 3 LSBs
PSL — Skipped 4 LSBs
PSL — Skipped 5 LSBs
PSL — Skipped 6 LSBs
PSL — Skipped 7 LSBs
PSL — Skipped 8 LSBs
PSL — Skipped 9 LSBs
Total Unique Branch Instructions
90% Branch Instructions
Yokota Branch Instructions
Average Linear Branch Entropy

Metric