



Target 1

Project

Project: embedded
Target 1
Source Group 1
STARTUP.A5
embeddedc

embeddedd.c*

```
37 cmd(0x06);  
38 cmd(0x01);  
39 cmd(0x80);  
40 }  
41 void cmd(unsigned char a)  
42 {  
43   lcd=a;  
44   rs=0;  
45   rw=0;  
46   en=1;  
47   delay();  
48   en=0;  
49 }  
50 void dat(unsigned char b)  
51 {  
52   lcd=b;  
53   rs=1;  
54   rw=0;  
55   en=1;  
56   delay();  
57   en=0;  
58 }  
59  
60 void lcd_string(char *s)  
61 {  
62   while(*s)  
63   {  
64     dat(*s++);  
65   }
```

Build Output

creating hex file from ".\Objects\embedded"...
".\Objects\embedded" - 0 Error(s), 0 Warning(s).
Build Time Elapsed: 00:00:01

Simulation

L:25 C:5

CAP NUM SCRL OVR R/W



Target 1

Project

Project: embedded
Target 1
Source Group 1
STARTUP.A5
embeddedc

embedded.c*

```
16 lcd_string(" EmbeTronicX ");
17 while(1)
18 {
19     if(GAS)
20     {
21         cmd(0xc0);
22         lcd_string("Gas Detected");
23         delay();
24     }
25     else
26     {
27         cmd(0xc0);
28         lcd_string(" ");
29     }
30 }
31 }
32
33 void lcd_init()
34 {
35     cmd(0x38);
36     cmd(0x0e);
37     cmd(0x06);
38     cmd(0x01);
39     cmd(0x80);
40 }
41 void cmd(unsigned char a)
42 {
43     lcd=a;
44 }
```

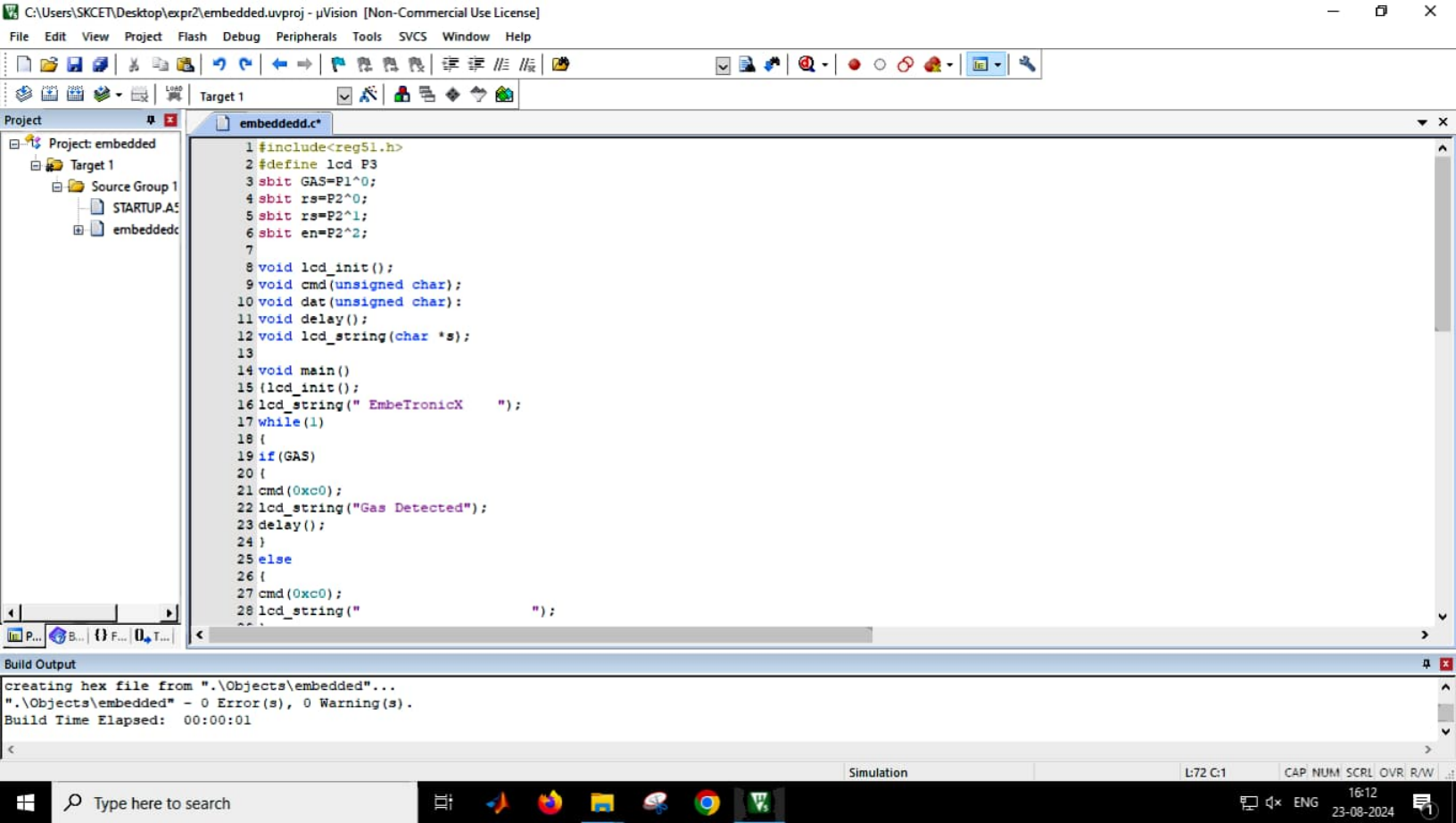
Build Output

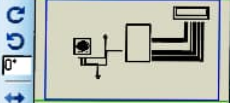
creating hex file from ".\Objects\embedded"...
".\Objects\embedded" - 0 Error(s), 0 Warning(s).
Build Time Elapsed: 00:00:01

Simulation

L:25 C:5

CAP NUM SCRL OVR R/W





TERMINALS

- DEFAULT
- INPUT
- OUTPUT
- BIDIR
- POWER
- GROUND
- CHASSIS
- BUS

