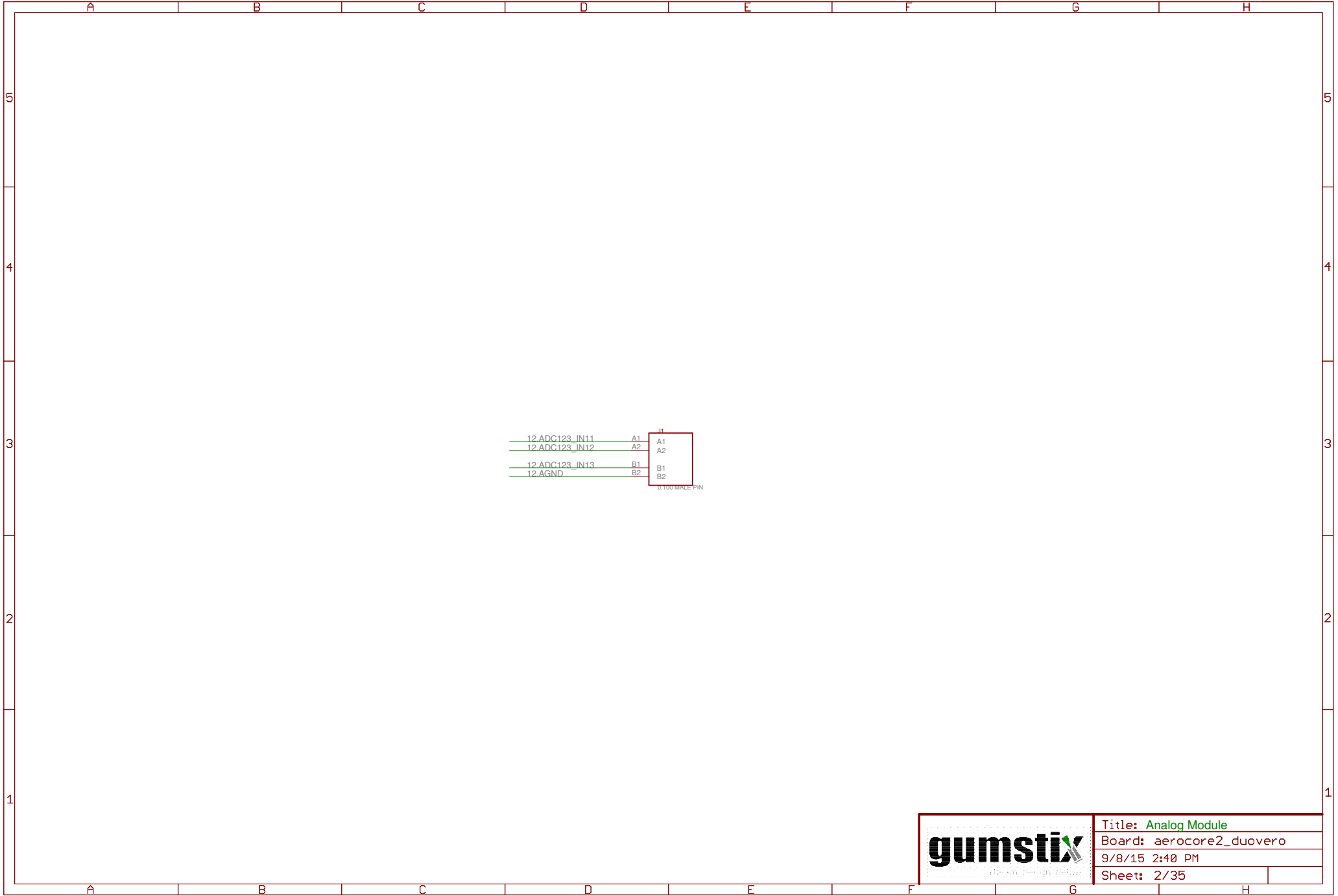


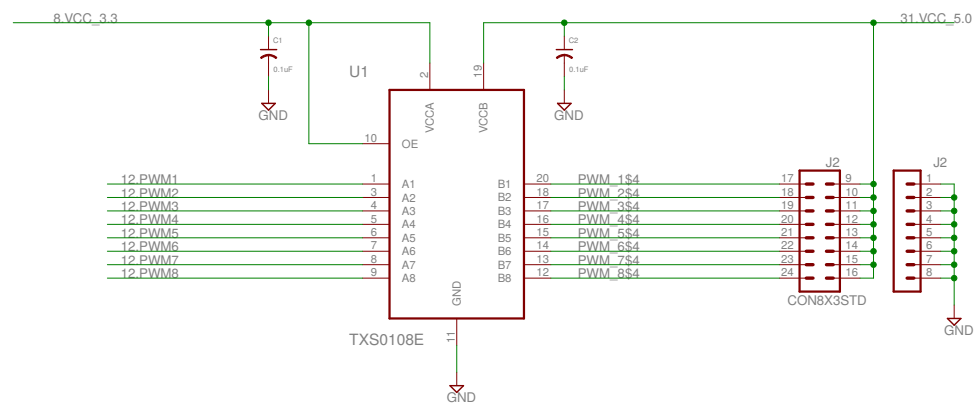
Table of Contents

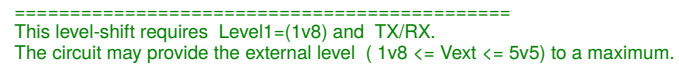
- 2) Analog Module
- 3) OMAP4430 COM
- 4) Octal Servo
- 5) I2C Header
- 6) UART Header
- 7) UART Header
- 8) 3.3V 1500mA Buck
- 9) 1.8V 600mA Switcher
- 10) M4 Microprocessor
- 11) UART Header
- 12) Barometer
- 13) Micro-B Jack
- 14) I2C Header
- 15) UART-UART Bridge
- 16) Red LED
- 17) Blue LED
- 18) Yellow LED
- 19) Pushbutton
- 20) Pushbutton
- 21) SPI Bridge
- 22) Fujitsu Non-Volatile Memory
- 23) IMU
- 24) SPI Header
- 25) 40 Pin GPIO & POWER Header
- 26) USB HOST

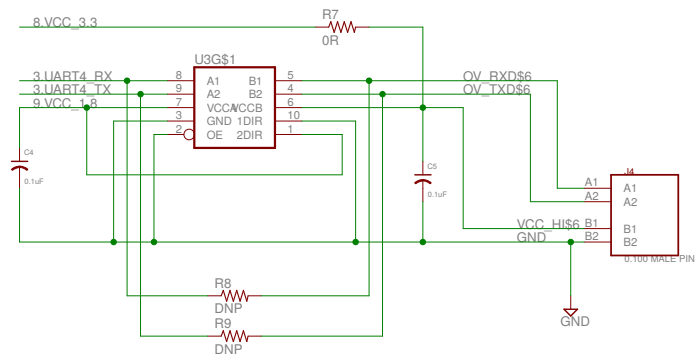
- 27) Li-Po Battery
- 28) Power Source Muxer
- 29) Buzzer
- 30) CAN Header
- 31) Spektrum DSM Header
- 32) Micro-B Jack
- 33) USB to UART
- 34) Battery 2S Balance
- 35) GPS Off-board



Title: Analog Module	
Board: aerocore2_duovero	
9/8/15 2:40 PM	
Sheet: 2/35	







UART

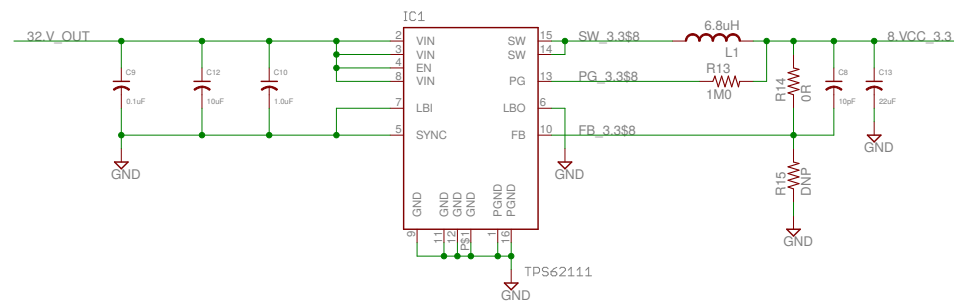
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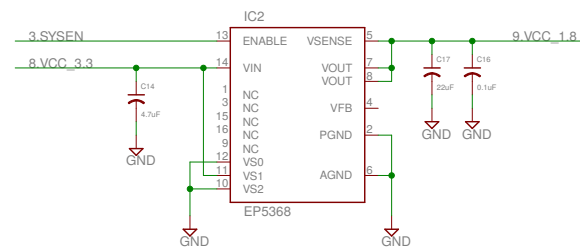
This level-shift requires Level1=(1v8) and TX/RX.

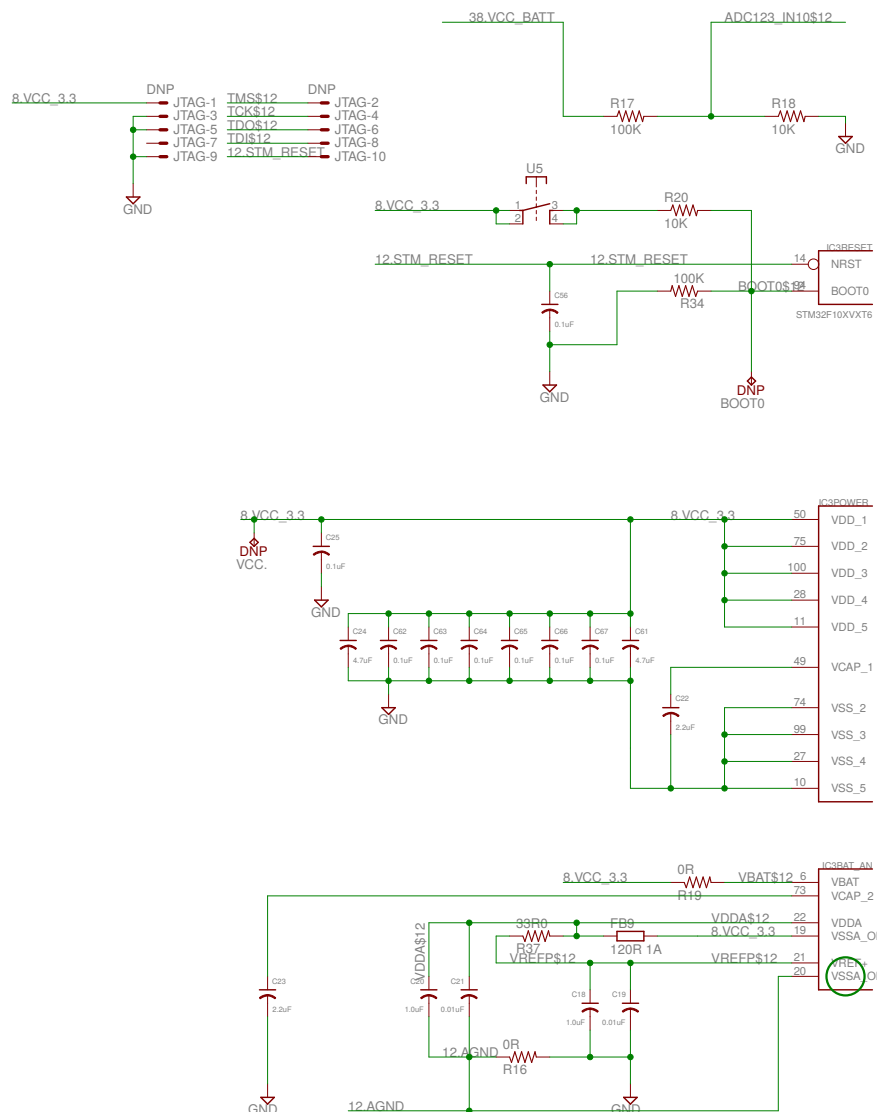
The circuit may provide the external level (1v8 <= Vext <= 5v5) to a maximum.



Title: UART Header
Board: aerocore2_duovero
9/8/15 2:40 PM
Sheet: 7/35

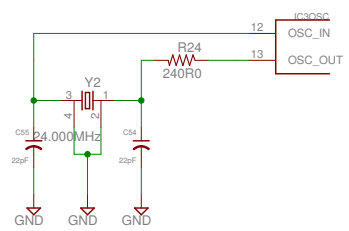






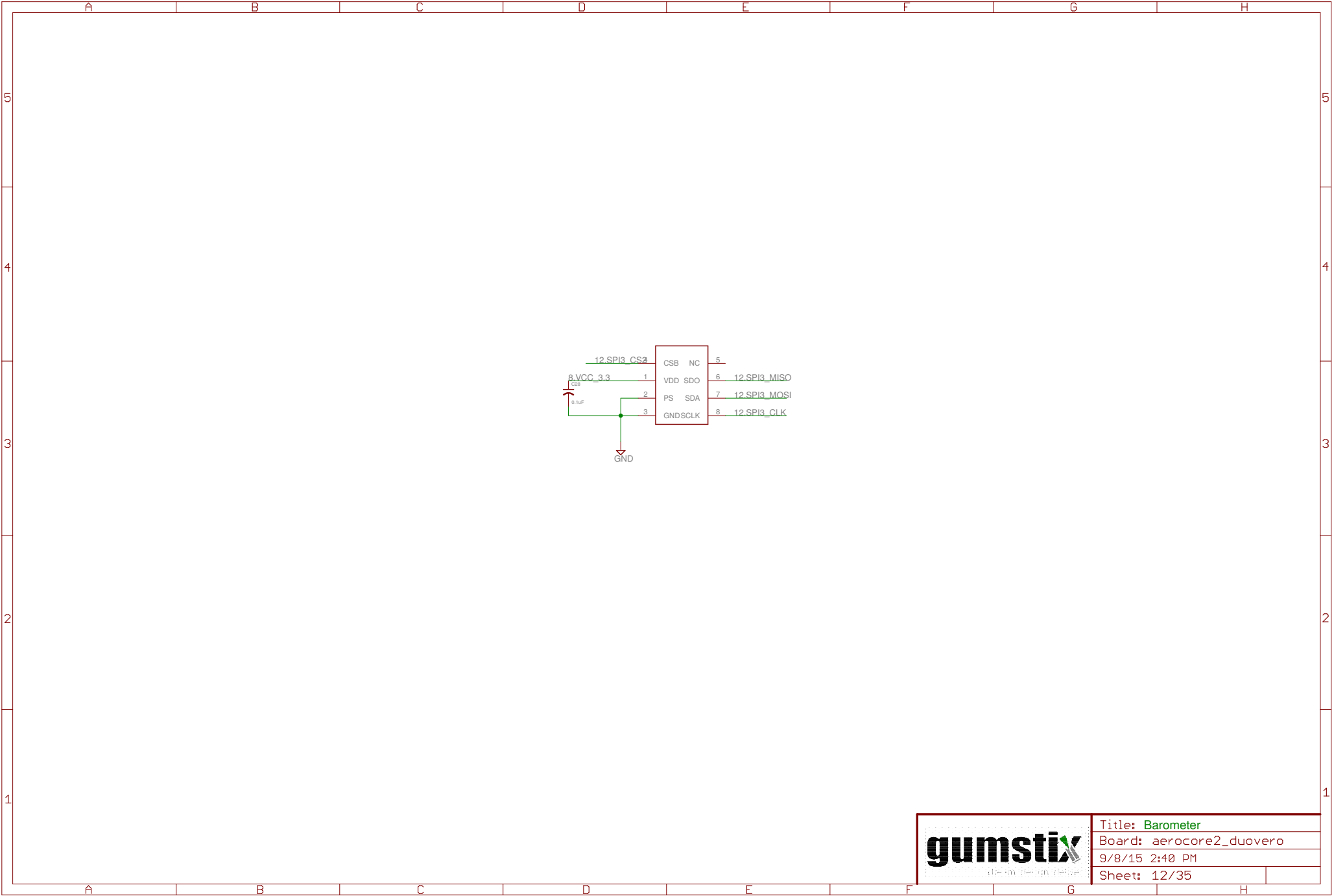
IC3PORTA_		23	12.PWM5
PA0		24	12.PWM6
PA1		25	12.PWM7
PA2		26	12.PWM8
PA3		29	12.SPI1_CS0
PA4		30	12.SPI1_CLK
PA5		31	12.SPI1_MISO
PA6		32	12.SPI1_MOSI
PA7			
IC3PORTA_H		67	12.PA8
PA8		68	
PA9		69	12.PA10
PA10		70	12.USB_DEVICE_N
PA11		71	12.USB_DEVICE_P
PA12		72	TMS\$12
PA13		76	TCK\$12
PA14		77	TDI\$12
IC3PORTB_		35	12.PB0
PB0		36	
PB1		37	BOOT1\$12
PB2		89	TDOS\$12
PB3		90	
PB4		91	12.PB5
PB5		92	12.UART1_TX
PB6		93	12.UART1_RX
PB7			
IC3PORTB_H		95	12.PB8
PB8		96	12.PB9
PB9		47	12.I2C2_SCI
PB10		48	12.I2C2_SDA
PB11		51	12.SPI2_CS0
PB12		52	12.SPI2_CLK
PB13		53	12.SPI2_MISO
PB14		54	12.SPI2_MOSI
PB15			
IC3PORTC_		15	ADC123_IN10\$12
PC0		16	ADC123_IN11
PC1		17	ADC123_IN12
PC2		18	ADC123_IN13
PC3		33	
PC4		34	12.SPI1_MISO
PC5		63	12.PC6
PC6		64	12.PC7
PC7			
IC3PORTC_H		65	12.PC8
PC8		66	12.PC9
PC9		78	12.SPI3_CLK
PC10		79	12.SPI3_MISO
PC11		80	12.SPI3_MOSI
PC12		7	12.PC13
PC13		8	12.PC14
PC14		9	12.PC15
PC15			

IC3PORTD_		81	12.CAN1_RX
PD0		82	12.CAN1_TX
PD1		83	12.PD2
PD2		84	12.PD3
PD3		85	12.PD4
PD4		86	12.UART2_TX
PD5		87	12.UART2_RX
PD6		88	12.PD7
PD7			
IC3PORTD_H		55	12.UART3_TX
PD8		56	12.UART3_RX
PD9		57	12.PD10
PD10		58	12.PD11
PD11		59	12.PWM1
PD12		60	12.PWM2
PD13		61	12.PWM3
PD14		62	12.PWM4
PD15			
IC3PORTE_		97	12.UART8_RX
PE0		98	
PE1		1	12.SPI3_CS0
PE2		2	12.SPI3_CS1
PE3		3	12.SPI3_CS2
PE4		4	12.PE5
PE5		5	12.PE6
PE6		38	12.UART7_RX
PE7			
IC3PORTE_H		39	12.UART7_TX
PE8		40	12.PE9
PE9		41	12.PE10
PE10		42	12.SPI4_CS0
PE11		43	12.SPI4_CLK
PE12		44	12.SPI4_MISO
PE13		45	12.SPI4_MOSI
PE14		46	12.PE15
PE15			

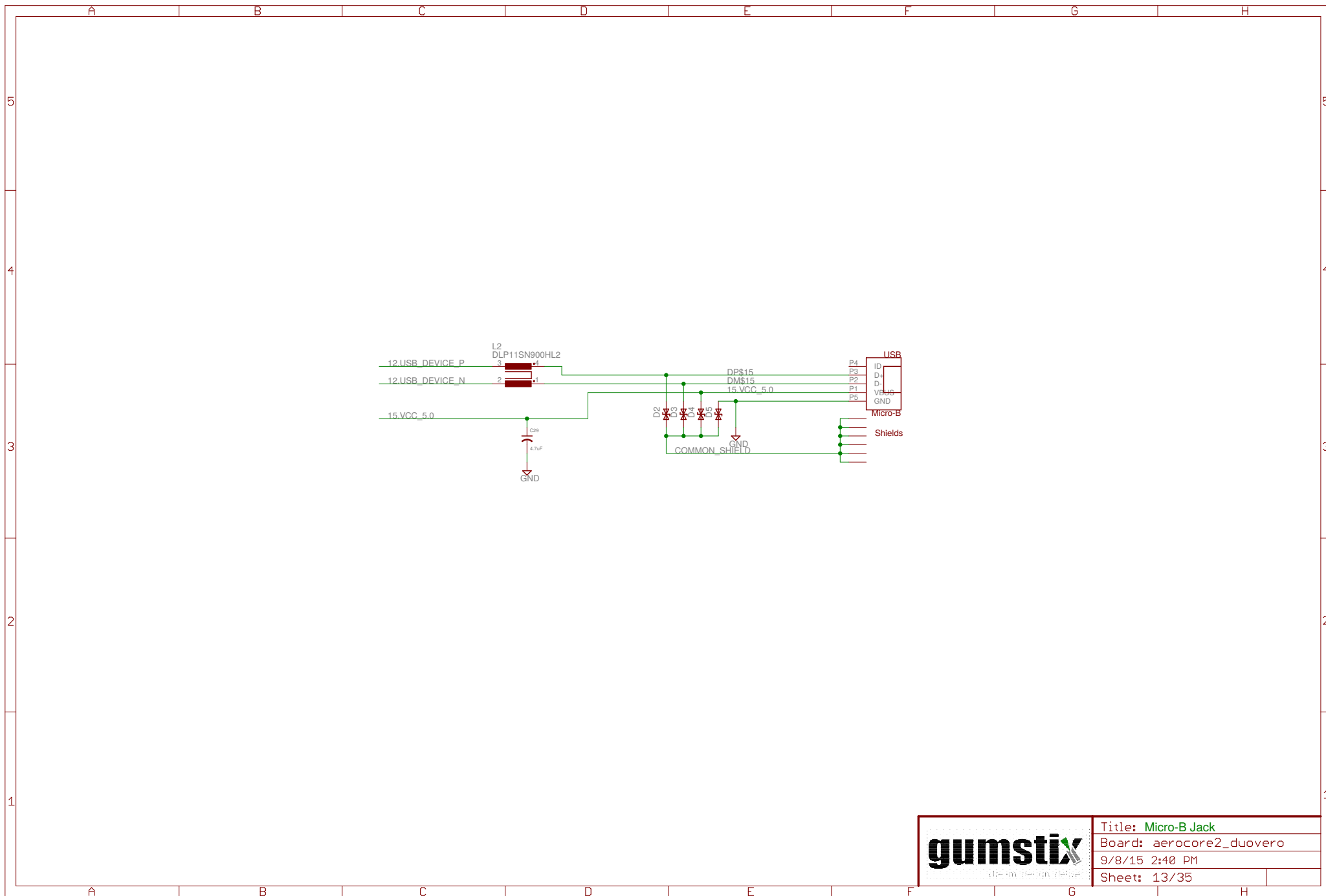


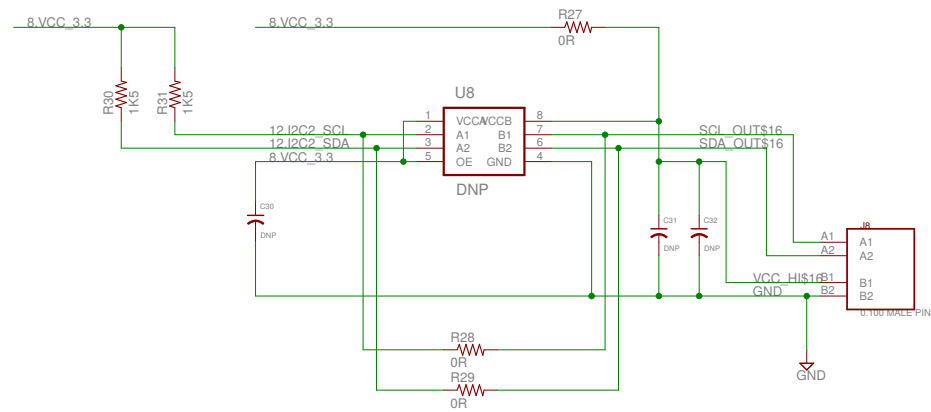


Title: UART Header
Board: aerocore2_duovero
9/8/15 2:40 PM
Sheet: 11/35



Title: Barometer	
Board: aerocore2_duovero	
9/8/15 2:40 PM	
Sheet: 12/35	





I2C

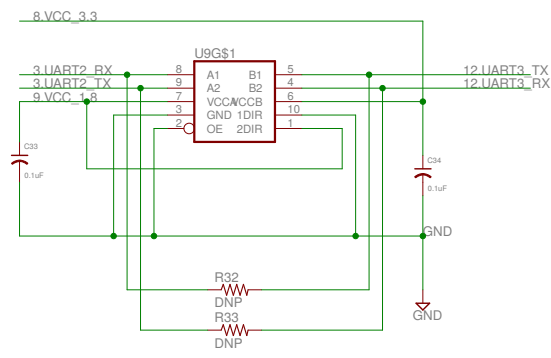
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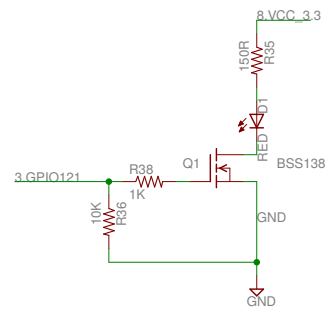
This level-shift requires Level1=(1v8) and TX/RX.

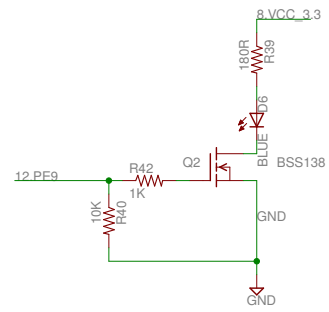
The circuit may provide the external level (1v8 <= Vext <= 5v5) to a maximum.




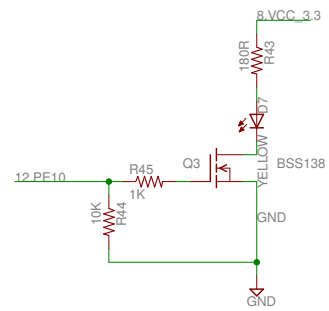
Title: I2C Header
Board: aerocore2_duovero
9/8/15 2:40 PM
Sheet: 14/35




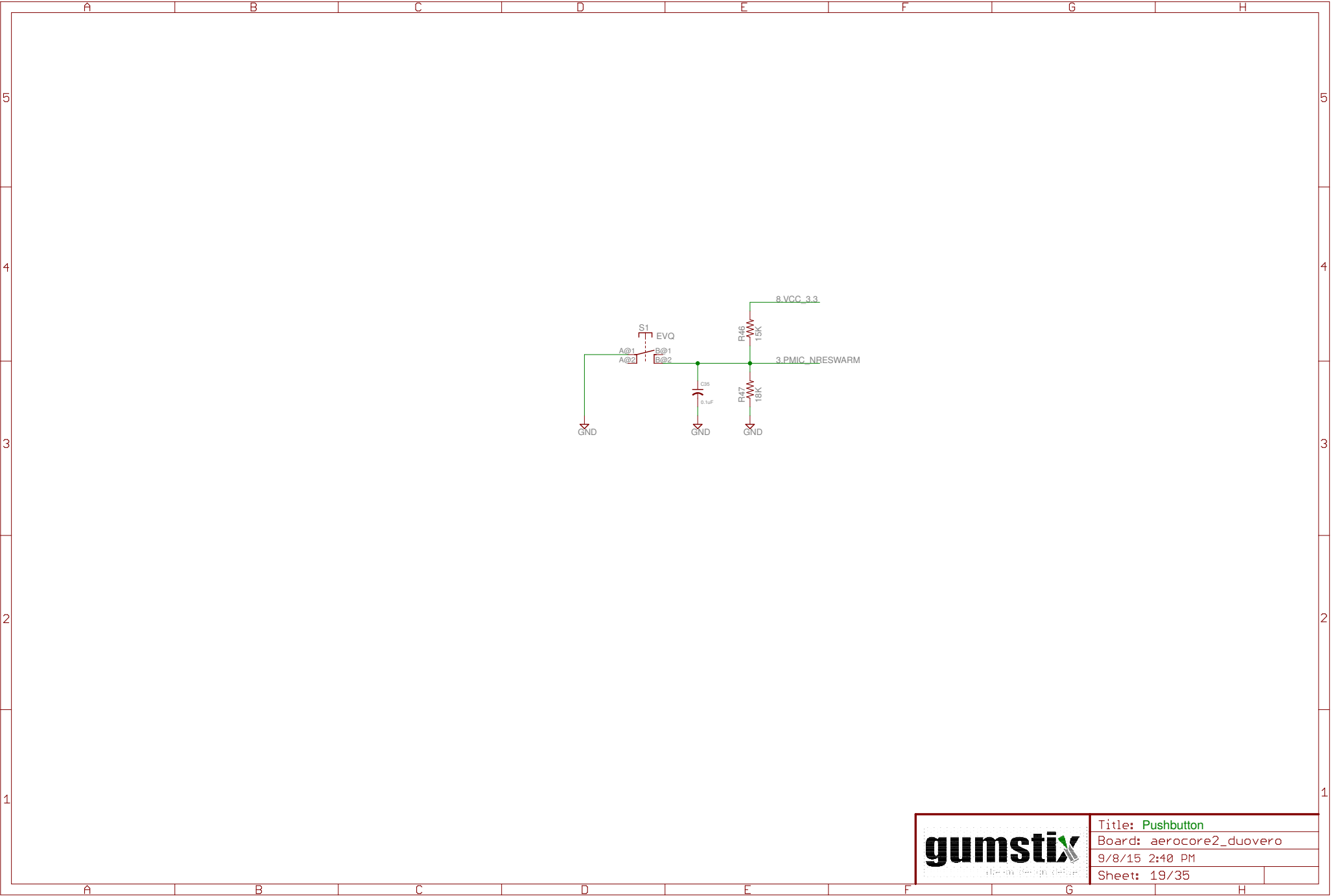




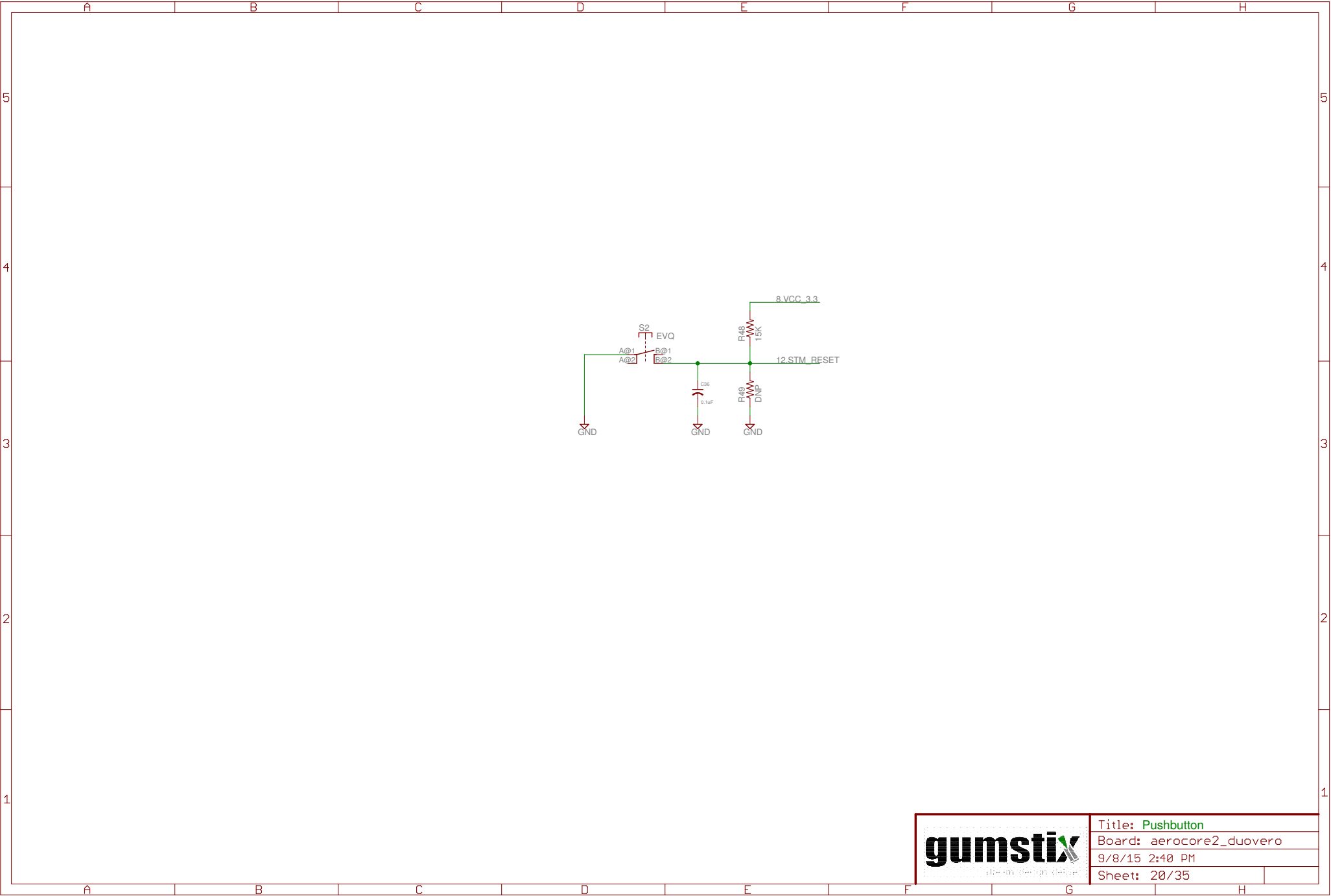
 open design (tm)	Title: Blue LED	
	Board: aerocore2_duovero	
	9/8/15 2:40 PM	
	Sheet: 17/35	

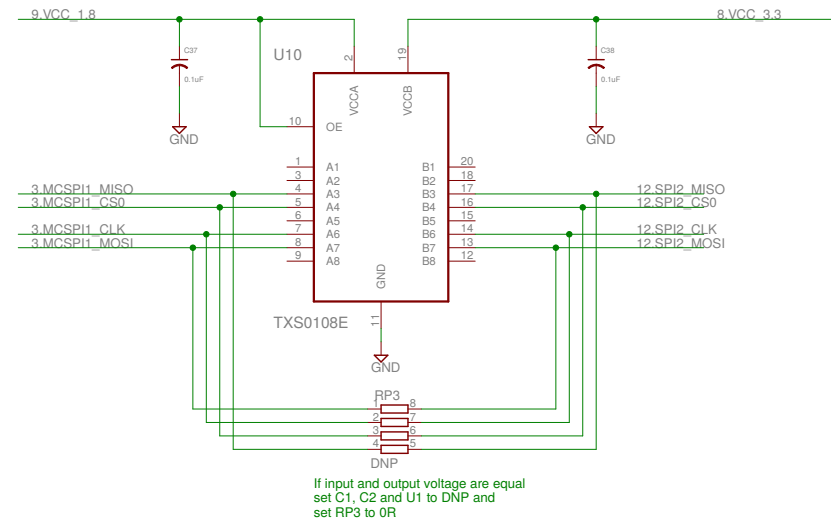


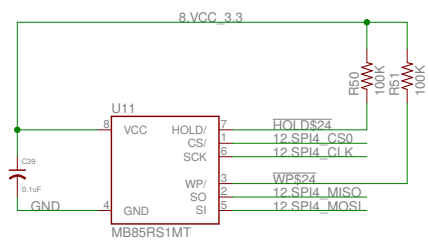
 open design (tm)	Title: Yellow LED	
	Board: aerocore2_duovero	
	9/8/15 2:40 PM	
	Sheet: 18/35	

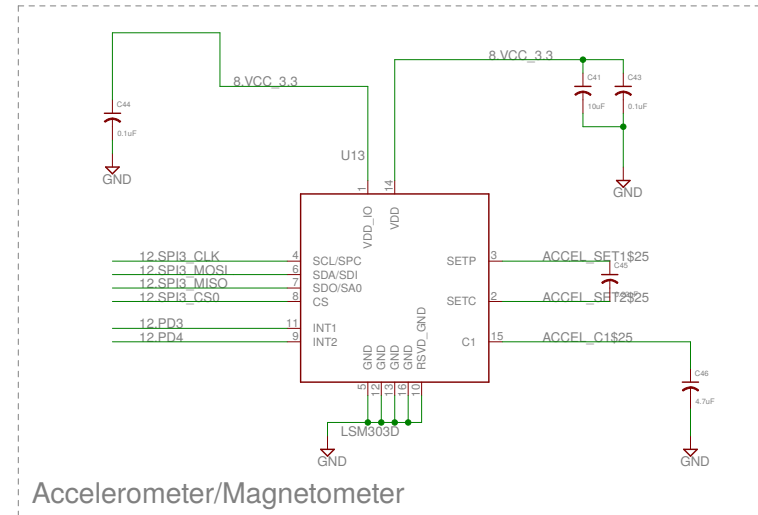
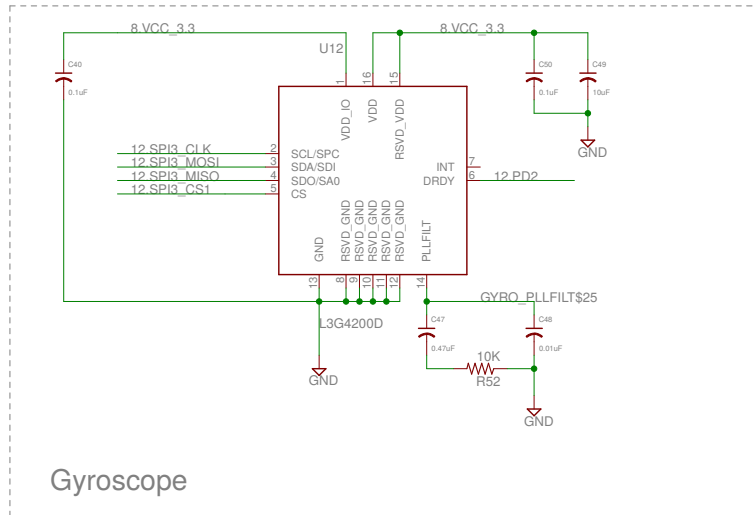


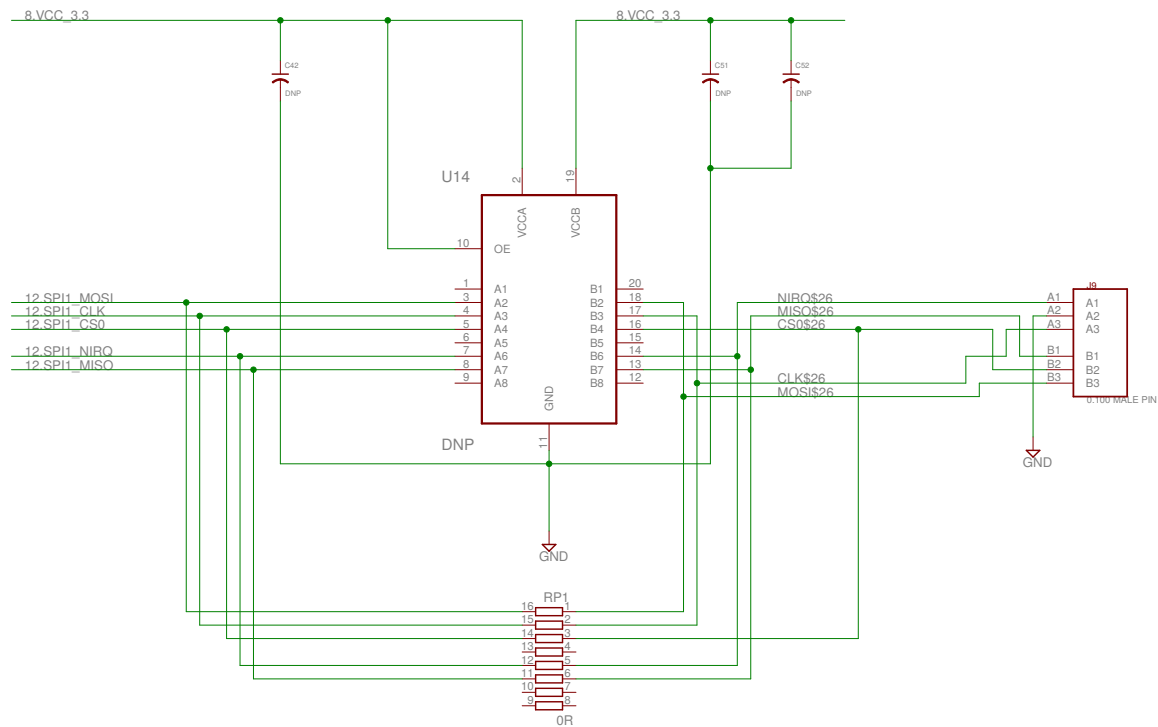
Title: Pushbutton	
Board: aerocore2_duovero	
9/8/15 2:40 PM	
Sheet: 19/35	

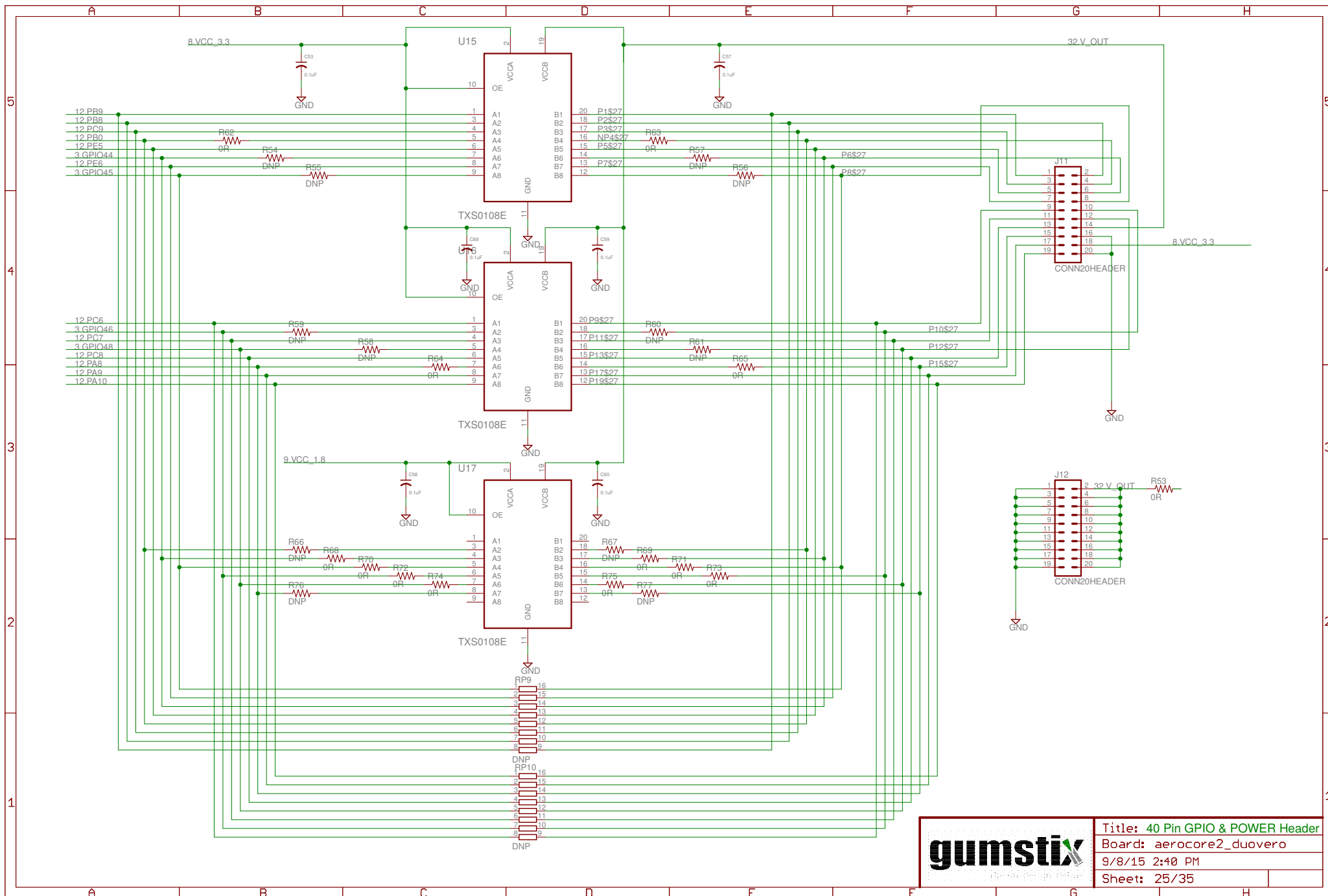


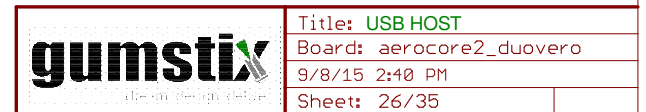


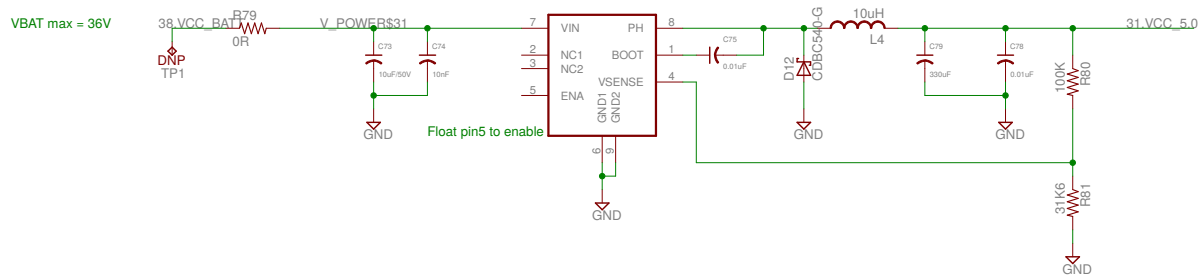


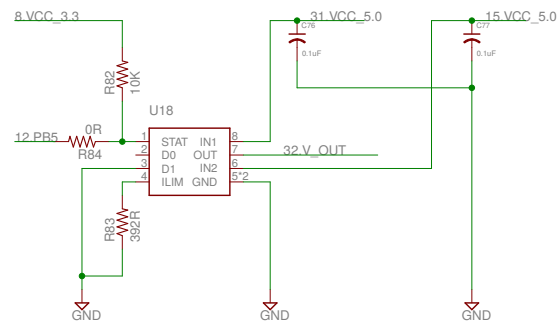


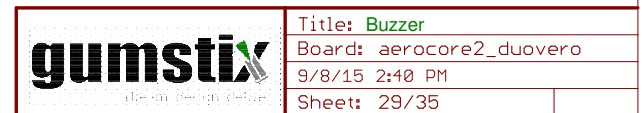


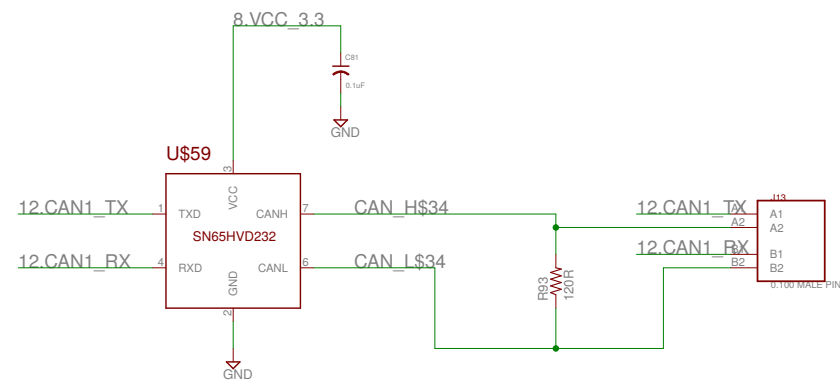


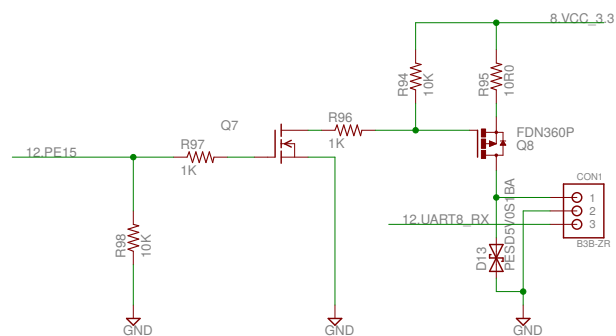




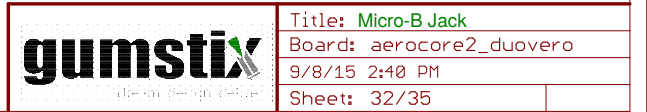


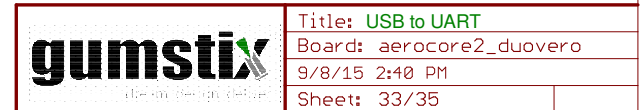


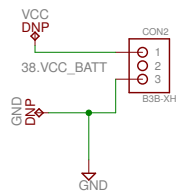


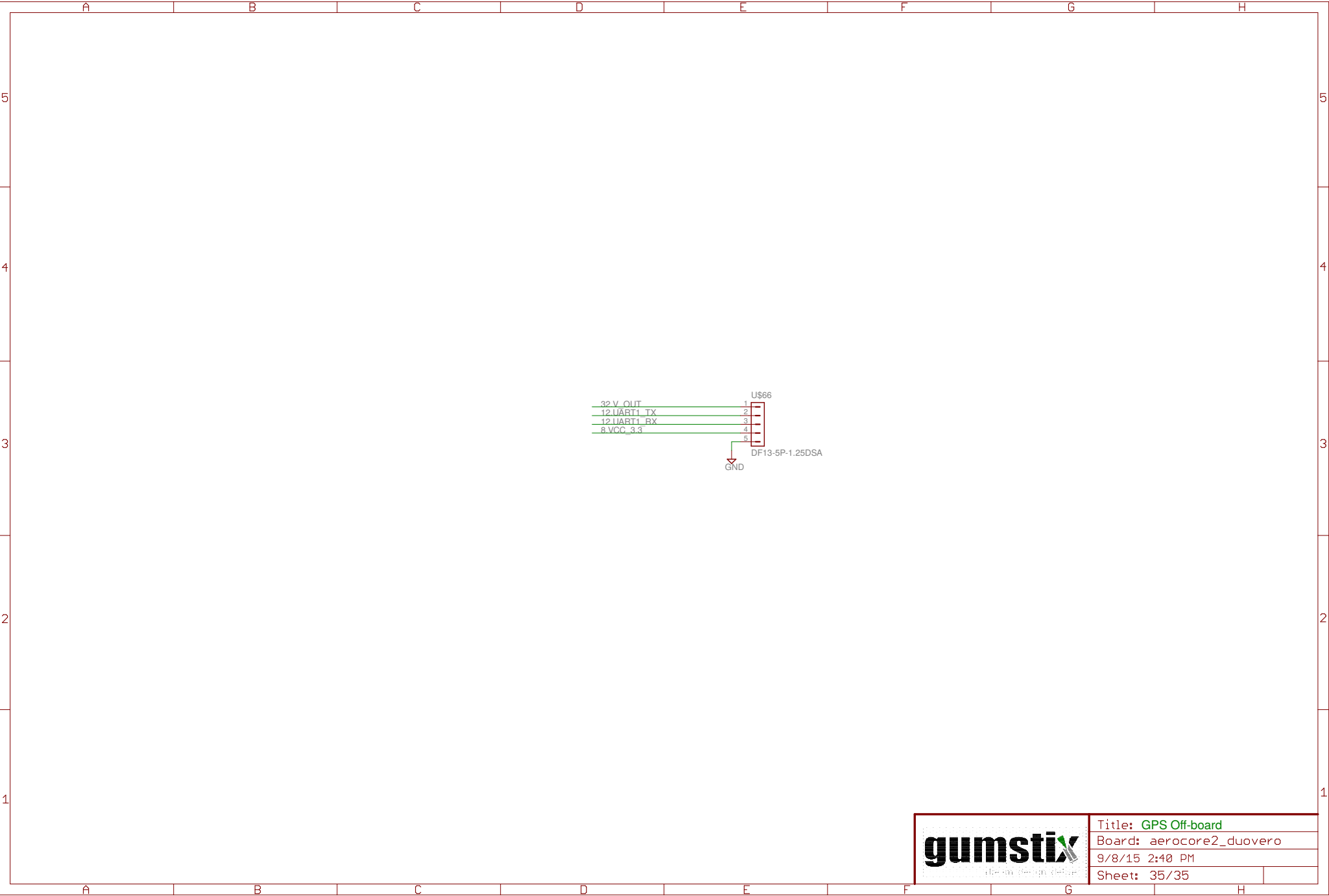


Spektrum receiver uses approximately 23mA (measured) while operating.









Title: GPS Off-board	
Board: aerocore2_duovero	
9/8/15 2:40 PM	
Sheet: 35/35	