

## Problem 9. B-Tree Speed

### Experimental Setup

Conducted our benchmarks using Google Benchmark [1] on a system with the following hardware specifications:

- **Processor:** 11th Gen Intel(R) Core(TM) i7-11370H 8-core CPU @ 3.3 GHz
- **Cache Hierarchy:**
  - L1 Data: 48 KiB per core ( $\times 4$ )
  - L1 Instruction: 32 KiB per core ( $\times 4$ )
  - L2 Unified: 1280 KiB per core ( $\times 4$ )
  - L3 Unified: 12,288 KiB (shared)

All benchmarks were compiled using `-Ox` optimization level with `MSVC` (version 19.42.34436) and executed in a single-threaded environment to minimize external interference. Memory usage was measured using the Windows API `GetProcessMemory()`.

The B-Tree implementation utilized in this experiment is sourced from the repository by frozenca on GitHub [2].

### Finding Optimal Parameter $b$ Of B-Tree

First I carried out benchmarks to measure performance,  $20 \times 10^6$  random unique keys being inserted into the B-Tree while varying  $b$  parameter. Measured aggregate data, It is generally more stable than measure per operation. The operation per second (ops/s) is computed  $\text{CPU-Time} / 20 \times 10^6$ .

$b$	ops/ms	CPU Time (ms)	Memory Usage (KB)
2	336.93	59359.38	2544788
4	229.97	86968.75	1063908
6	187.82	106484.38	740108
8	162.25	123265.63	596900
16	147.98	135156.25	400248
32	138.27	144640.63	309792
64	130.08	153750.00	268680
128	122.46	163312.50	256996
256	114.90	174062.50	267676
512	107.48	186078.13	280852
1024	99.60	200796.88	285412
2048	90.49	221015.63	246636

Table 2: Benchmark results for B-Tree insertion with  $20 \times 10^6$  inserts and varying  $b$  from 2 to 2048.

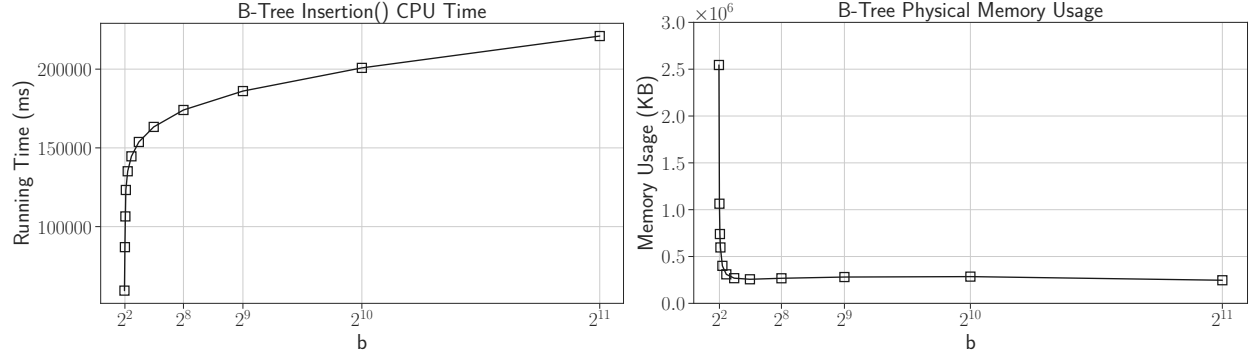


Figure 3: CPU Time and Physical Memory Usage for B-Tree insertion with varying  $b$  values.

Notice that as the parameter of  $b$  the B-Tree increases, the physical memory usage decreases. My theory is that increasing  $b$  makes the tree shallower and more compact. Since a shallower tree reduces the number of pointers and improves spatial locality, nodes are more likely to fit within a cache line, leading to better memory efficiency?

Anyhow... we want the  $b$  such that the it minimizes CPU time and memory used. They way I did it is to simply normalize CPU time and memory used and get closest  $b$  with to the smallest difference.

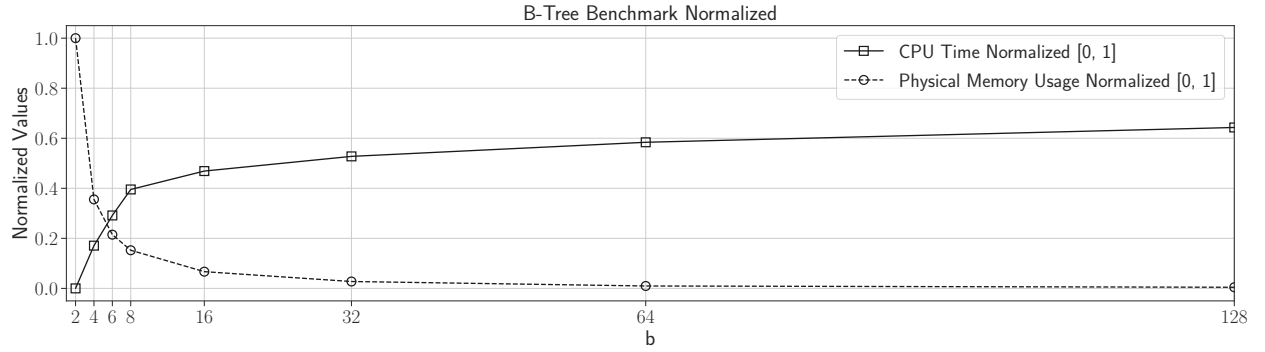


Figure 4: Normalized CPU Time and Physical Memory Usage comparison for B-Tree insertion with varying  $b$  values.

Setting  $b = 6$  minimizes both performance overhead and memory usage the most. However, for applications that prioritize memory efficiency over execution time, a larger value, such as  $b = 16$ , may be more optimal, as the performance trade-off becomes less significant. This version clarifies that  $b = 6$  minimizes both performance overhead and memory usage while improving the flow of the second sentence. Let me know if you need further adjustments!

## Comparing B-Tree and Builtin Ordered-Map Performance

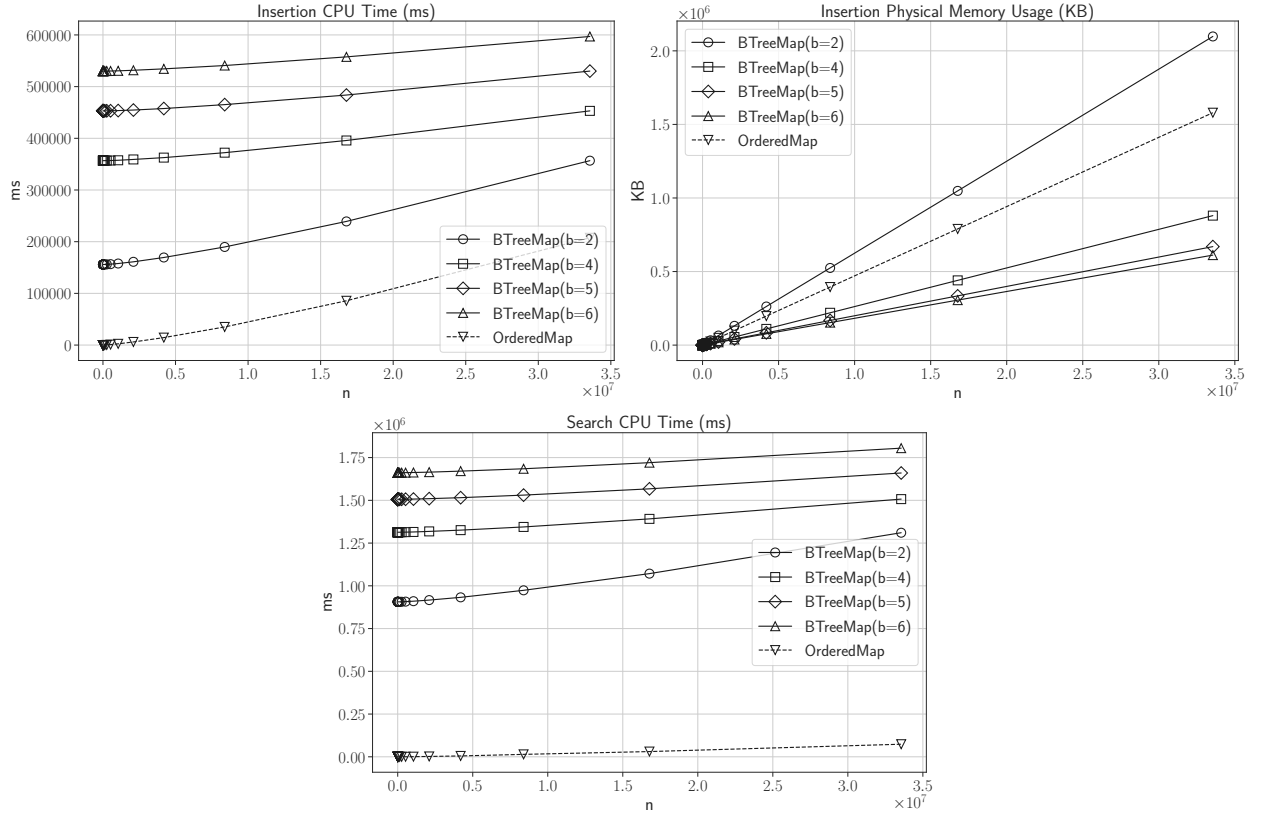


Figure 5: CPU Time and Physical Memory Usage of B-Tree and C++ Builtin Ordered Map. Inserted  $2^{24}$  unique and random keys.

Figure 5 illustrates that the running time for the C++ built-in Ordered Map outperforms the B-Tree in both insertion and search operations. However, it is important to note that for B-Trees with  $b > 2$ , memory efficiency improves, making them more memory-efficient than the built-in Ordered Map.

Ordered Map Insertion		
n	CPU Time (ms)	Memory Usage (KB)
8	0.00E+00	4
16	0.00E+00	0
32	0.00E+00	0
64	0.00E+00	0
128	0.00E+00	0
256	0.00E+00	4
512	0.00E+00	12
1024	0.00E+00	24
2048	0.00E+00	64
4096	0.00E+00	176
8192	0.00E+00	368
16384	3.13E+01	684
32768	1.56E+01	1584
65536	4.69E+01	3080
131072	1.41E+02	5936
262144	3.28E+02	11884
524288	8.91E+02	24628
1048576	2.42E+03	49260
2097152	6.02E+03	98572
4194304	1.44E+04	197348
8388608	3.51E+04	394748
16777216	8.57E+04	789744
33554432	2.08E+05	1579572

B-Tree b=6 Insertion		
n	CPU Time (ms)	Memory Usage (KB)
8	5.30E+05	4
16	5.30E+05	0
32	5.30E+05	0
64	5.30E+05	0
128	5.30E+05	4
256	5.30E+05	4
512	5.30E+05	8
1024	5.30E+05	16
2048	5.30E+05	32
4096	5.30E+05	68
8192	5.30E+05	124
16384	5.30E+05	308
32768	5.30E+05	576
65536	5.30E+05	1148
131072	5.30E+05	2216
262144	5.30E+05	4460
524288	5.30E+05	9356
1048576	5.31E+05	19092
2097152	5.32E+05	38316
4194304	5.34E+05	76508
8388608	5.41E+05	152888
16777216	5.58E+05	305684
33554432	5.97E+05	611656

Ordered Map Search		
n	CPU Time (ms)	Memory Usage (KB)
8	3.07E-05	4
16	7.67E-05	0
32	1.71E-04	0
64	4.53E-04	0
128	9.63E-04	0
256	2.22E-03	4
512	4.46E-03	12
1024	2.93E-02	24
2048	9.42E-02	64
4096	2.68E-01	176
8192	6.28E-01	368
16384	1.38E+00	684
32768	3.29E+00	1584
65536	8.54E+00	3080
131072	2.34E+01	5936
262144	1.02E+02	11884
524288	2.66E+02	24628
1048576	8.13E+02	49260
2097152	2.06E+03	98572
4194304	5.06E+03	197348
8388608	1.43E+04	394748
16777216	3.06E+04	789744
33554432	7.36E+04	1579572

B-Tree b=6 Search		
n	CPU Time (ms)	Memory Usage (KB)
8	1.66E+06	4
16	1.66E+06	0
32	1.66E+06	0
64	1.66E+06	0
128	1.66E+06	4
256	1.66E+06	4
512	1.66E+06	8
1024	1.66E+06	16
2048	1.66E+06	32
4096	1.66E+06	68
8192	1.66E+06	124
16384	1.66E+06	308
32768	1.66E+06	576
65536	1.66E+06	1148
131072	1.66E+06	2216
262144	1.66E+06	4460
524288	1.66E+06	9356
1048576	1.66E+06	19092
2097152	1.66E+06	38316
4194304	1.67E+06	76508
8388608	1.68E+06	152888
16777216	1.72E+06	305684
33554432	1.81E+06	611656

## References

- [1] Google Benchmark. *A microbenchmark support library*. Available at: <https://github.com/google/benchmark>

- [2] B-Tree Implementation on GitHub. *GitHub Repository*. Available at: <https://github.com/frozenca/BTree>