

Type	Signals
wire	HEX0[7:0]
wire	HEX1[7:0]
wire	HEX2[7:0]
wire	HEX3[7:0]
wire	HEX4[7:0]
wire	HEX5[7:0]
reg	K[1:0]
reg	Rn
reg	clk
wire	led[9:0]
reg	sw[9:0]

Filter:

```

Signals
Time
CurrentState[2:0] =
    clk =
        LEDR_L[2:0] =
        LEDR_R[2:0] =

```

