RR NÉLKÜL	A process		B process		C process		D process		Reschedule	
Clock tick	p_uspri	p_cpu	p_uspri	p_cpu	p_uspri	p_cpu	p_uspri	p_cpu	running before	running after
Starting point	60	0	60	0	60	0	60	0		
1	60	1	60	0	60	0	60	0		A
...										
99	60	99	60	0	60	0	60	0	A	A
100	60+50/4 73	100/2 50	60	0	60	0	60	0	A	B
101	73	50	60	1	60	0	60	0	B	B
...										
199	73	50	50	99	60	0	60	0	B	B
200	60+25/4 67	50/2 25	60+50/4 73	100/2 50	60	0	60	0	B	C
201	67	25	73	50	60	1	60	0	C	C
...										
299	67	25	73	50	60	99	60	0	C	C
300	60+13/4 63	25/2 13	60+25/4 67	50/2 25	60+50/4 73	100/2 50	60	0	C	D
301	63	13	67	25	73	50	60	1	D	D
...										
399	63	13	77	25	83	50	60	99	D	D
400	60+7/4 62	13/2 7	60+13/4 63	25/2 13	60+25/4 67	50/2 25	60+50/4+2*5 83	100/2 50	D	

RR	A process		B process		C process		D process		Reschedule	
Clock tick	p_uspri	p_cpu	p_uspri	p_cpu	p_uspri	p_cpu	p_uspri	p_cpu	running before	running after
Starting point	60	0	60	0	60	0	60	0		
1	60	1	60	0	60	0	60	0		A
...										
9	60	9	60	0	60	0	60	0	A	A
10	60	10	60	0	60	0	60	0	A	B
11	60	10	60	1	60	0	60	0	B	B
...	...	...	...	...	...	...	...	...	...	...
19	60	10	60	9	60	0	60	0	B	B
20	60	10	60	10	60	0	60	0	B	C
21	60	10	60	10	60	1	60	0	C	C
...	...	...	...	...	...	...	...	...	...	...
29	60	10	60	10	60	9	60	0	C	C
30	60	10	60	10	60	10	60	0	C	D
31	60	10	60	10	60	10	60	1	D	D
...	...	...	...	...	...	...	...	...	...	...
40	60	10	60	10	60	10	60	10	D	A
...	...	...	...	...	...	...	...	...	...	...
50	60	20	60	10	60	10	60	10	A	B
...	...	...	...	...	...	...	...	...	...	...
60	60	20	60	20	60	10	60	10	B	C
...	...	...	...	...	...	...	...	...	...	...
70	60	20	60	20	60	20	60	10	C	D
...	...	...	...	...	...	...	...	...	...	...
80	60	20	60	20	60	20	60	20	D	A
...	...	...	...	...	...	...	...	...	...	...
90	60	30	60	20	60	20	60	20	A	B
...	...	...	...	...	...	...	...	...	...	...
100	63	15	63	15	62	10	72	10	B	C
...	...	...	...	...	...	...	...	...	...	...
199	63	15	63	15	62	109	72	10		
200	61	7	61	7	73	55	71	5	C	A
201	61	8	61	7	73	55	71	5		