

Education

Vellore Institute Of Technology ,Vellore

Btech in Electronics And Communication Engineering : CGPA 8.50 [2021-2025]

Government Model Sr. Sec. School , Sector – 19 C , Chandigarh

Higher Secondary Certificate : 81.2 % [2019-2020]

DAV Public Sr. Sec. School , Phase-X , Mohali

Matriculation : 77.2 % [2017-2018]

Experience

CSIR-CSIO

Sector-30 , Chandigarh

Internship Trainee

07-08-2023 to 07-10-2023

- Developed expertise in MicroPython programming and implemented code within the Thonny IDE for measuring impedances.
- Improved measurement capabilities for impedance spectroscopy, leading to more accurate and insightful analyses.
- Created visual aids, such as circuit diagrams and photographs, to enhance the clarity and completeness of the report.

Certifications

- Learn JAVA | CodeChef | 2023
- Java for Problem Solving -1 | CodeChef | 2023
- JavaScript – Basic to Advance | Udemy | November 2023

Skills

- HTML , CSS , JavaScript
- Java
- Cadence , MATLAB , Multisim , ModelSim , LTspice , Orcad , Pspice

Projects

- **Spotify Clone :**
The project showcased my skills of Frontend development . This was a clone website of Spotify in which music can be played , paused and can be played around . In this project I implemented all the knowledge I gained by learning HTML , CSS, JS .
- **Temperature And Humidity Measuring Device :**
Hailing from electronics background , as a part of IoT project this device was made using breadboard and all the required sensors which were connected to WiFi Node MCU Module .
- **TinDog :**
This is a responsive website showcasing my Frontend Web-Development Skills . This website was made for dog owners to find matches for there dogs .
- **Smart Plant Monitoring System :**
Developed a Smart Plant Monitoring System using a Node MCU with an ESP12E IC and sensors (soil moisture , DHT11, PIR Motion). Monitored plant conditions and enhanced security with a motion alert system , sending real-time data to Blynk.io for analysis .
- **SR Flip-Flop using Sequential Circuit :**
Research Project using simulations in Cadence's CMOS technology .