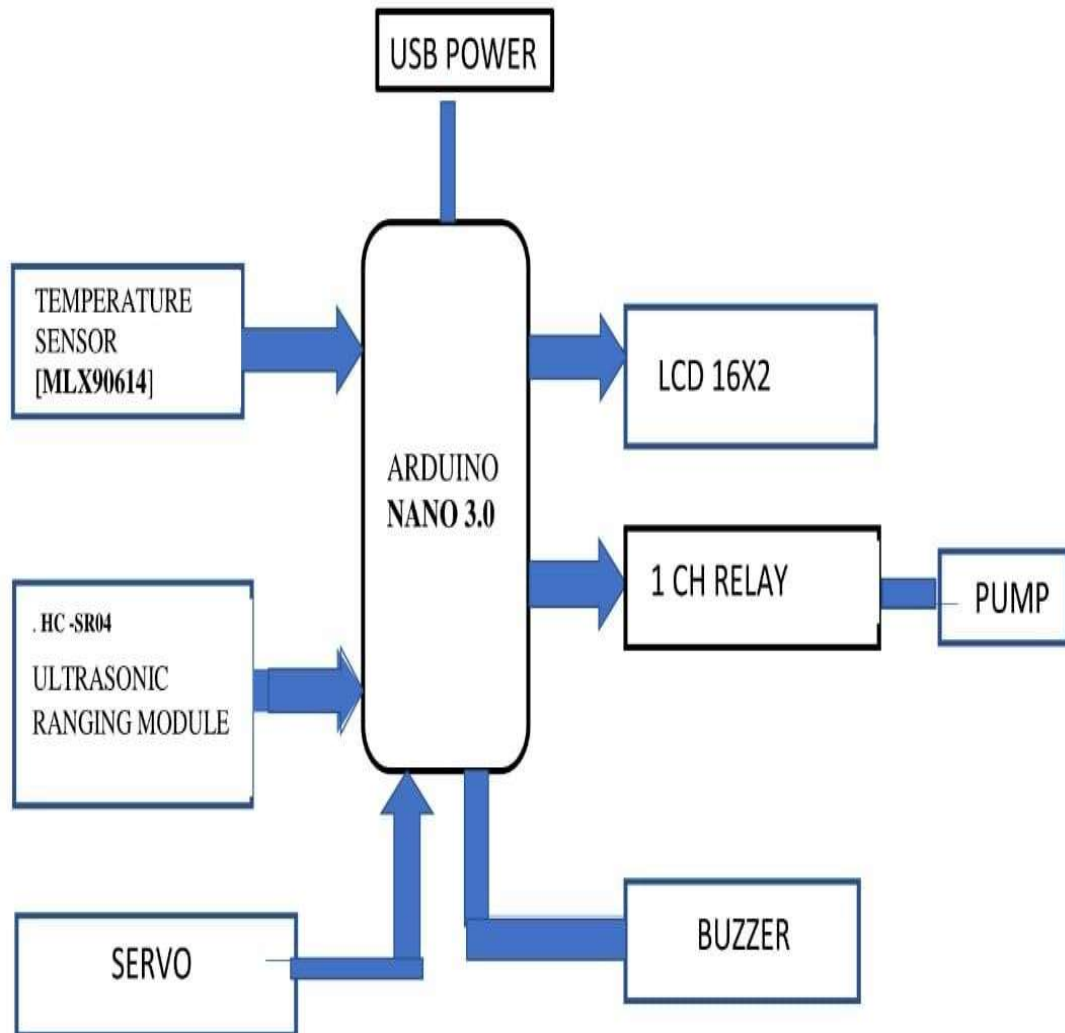
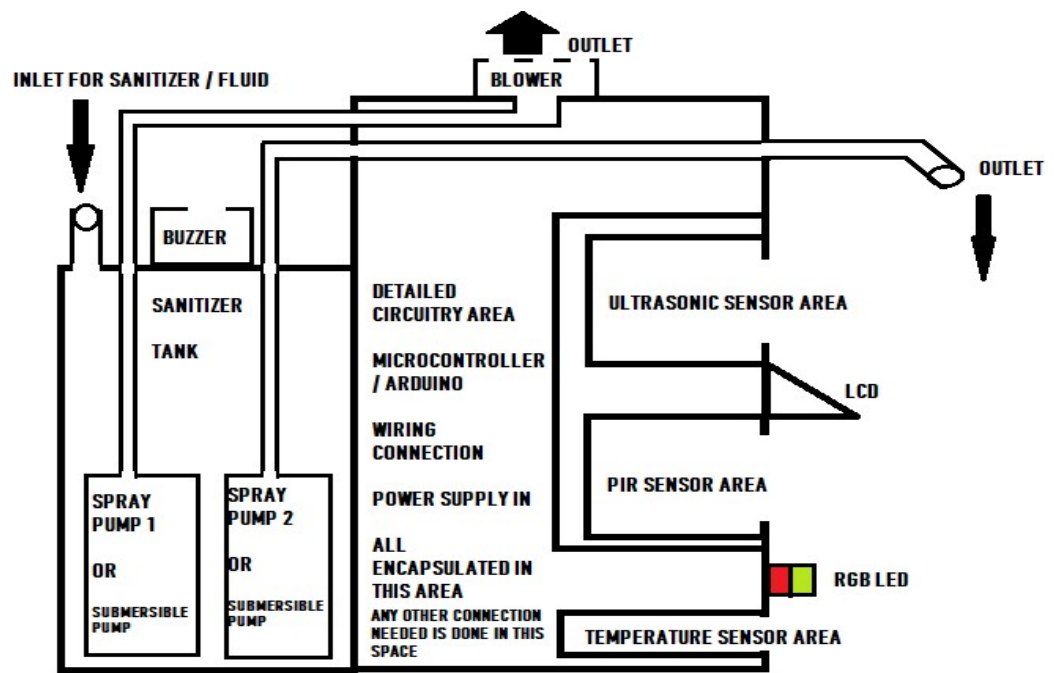
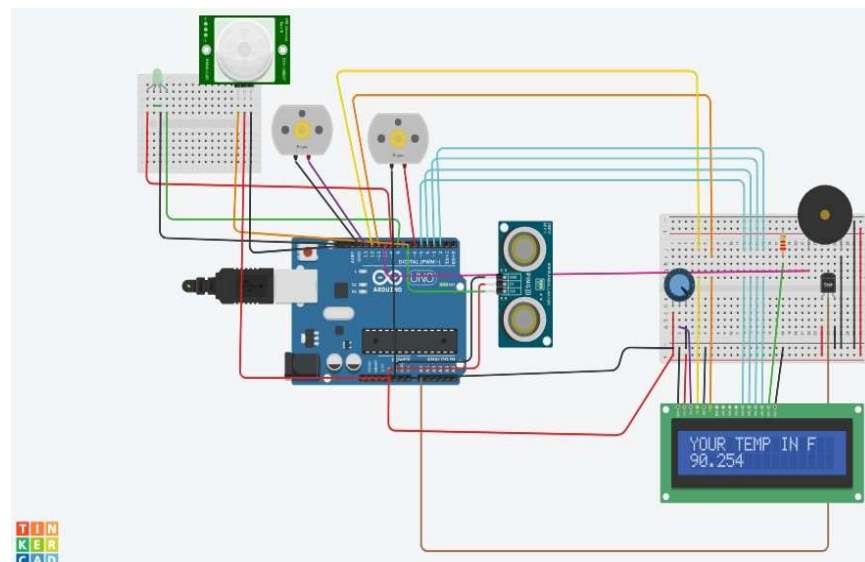


BLOCK DIAGRAM





Device model prototype.



Schematic of the working model.