الم	(1)	بجمالي فتدنا (1) Advanced (1) Exam	?
*:Your name An example of core peripherals in an ARM based microcontroller -1 X	عنوان	بريد الكتروني *	
An example of core peripherals in an ARM based microcontroller -1 X RCC ① DMA ① MPU ① None of the previous ① None of the previous ② The peripheral that is used as the reset manager is called -2 ✓ AHB ① MPU ② None of the previous ② AHB ② MPU ② None of the previous ④ V None of the previous ④ 1/1 When HSE is configured as system clock, CSS enabled, and an HSE -3 ✓ failure occurs, the microcontroller will shut down True ② False ④ 1/1 when switching from HSE to HSI clock to be the clock source , but the -4 ✓ HSI is disabled , the stm32 will ✓ choose the external clock to be the clock source ② enable the internal clock and make it the clock source ② undefined behavior ○ none of the previous ○	.com	haninanwar555@gmail	
An example of core peripherals in an ARM based microcontroller -1 × RCC ① DMA ① MPU ① None of the previous ① None of the previous ② 1/1 The peripheral that is used as the reset manager is called -2 ✓ ICD ① AHB ② MPU ② None of the previous ⑥ 1/1 When HSE is configured as system clock, CSS enabled, and an HSE -3 ✓ failure occurs, the microcontroller will shut down True ② False ⑥ 1/1 when switching from HSE to HSI clock to be the clock source , but the -4 ✓ HSI is disabled , the stm32 will ✓ choose the external clock to be the clock source ② enable the Internal clock and make it the clock source ② undefined behavior ○ none of the previous ③	ame	*:Your n	
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DMA ○ MPU ○ None of the previous ○ ***********************************	×	An example of core peripherals in an ARM based microcontroller -1	0/1
MPU ○ None of the previous ○ None of the previous ○ None of the previous ○ The peripheral that is used as the reset manager is called -2 ✓ ICD ○ AHB ○ MPU ○ None of the previous ● None of the previous ● None of the previous ● True ○ False ● True ○ Choose the external clock to be the clock source , but the -4 ✓ HSI is disabled , the stm32 will ANDER OND STATE OF THE STAT	0	RCC	×
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Temperature variance	✓		1/1
1/1 when switching from HSE to HSI clock to be the clock source , but the -4 HSI is disabled , the stm32 will ✓ choose the external clock to be the clock source enable the internal clock and make it the clock source undefined behavior none of the previous 1/1 Crystal clock system provides high noise immunity against -5 ✓ Temperature variance ✓	0	True	
HSI is disabled , the stm32 will choose the external clock to be the clock source enable the internal clock and make it the clock source undefined behavior none of the previous Temperature variance	•	False	~
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enable the internal clock and make it the clock source undefined behavior none of the previous 1/1 Crystal clock system provides high noise immunity against -5	_		
undefined behavior none of the previous 1/1 Crystal clock system provides high noise immunity against -5 Temperature variance			~
none of the previous 1/1 Crystal clock system provides high noise immunity against -5 Temperature variance			
✓ Temperature variance ✓	_		
	~	Crystal clock system provides high noise immunity against -5	1/1
		Tamperature variance	./
		remperature variance EMI	· ·

Vibration

	e above	
0/1 The RISC processor insures higher throughput than 0	CISC -6	×
	True	0
×	False	
	الصحيحة	الإجابة
	True	•
1/1 ARM cortex M family supports both ARM set and Thum	b set -7	~
	True	0
✓	False	•
0/1 The following could be used for measuring digital signals and fre	clock -8 quency	×
✓ Digital oscill	oscope	~
Analog oscill	oscope	
✓ Logic a	ınalyzer	\checkmark
X Frequency sp	ectrum	~
	الصحيحة	الإجابة
Digital oscill	oscope	\checkmark
Analog oscill		
Logic a	inalyzer	~
1/1 The watchdog timer is use	ed for -9	~
Ensuring execution of the software doesn't stay trapped in an endless look blocked state	op or	•
		O
blocked state	ements	OO
blocked state Scheduling the different software tasks according to their timing require	ements re tasks	0
Scheduling the different software tasks according to their timing requir Generating timely interrupts to trigger execution of software	ements re tasks ller pins al, 2 -10 unt 10,	0
Scheduling the different software tasks according to their timing requires Generating timely interrupts to trigger execution of software Generating PWM signals through dedicated microcontrol of an external signal samples were taken, sample 1 was at count 60, sample 2 was at conumber of overflow interrupts was one, and the timer frequency was	ements re tasks ller pins al, 2 -10 unt 10,	0 0
Scheduling the different software tasks according to their timing requires Generating timely interrupts to trigger execution of software Generating PWM signals through dedicated microcontrol of an external signal samples were taken, sample 1 was at count 60, sample 2 was at conumber of overflow interrupts was one, and the timer frequency was	ements re tasks fler pins flal, 2 -10 unt 10, flam 1MHz, fleriod is	0 0 0
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Scheduling the different software tasks according to their timing requires Generating timely interrupts to trigger execution of software Generating PWM signals through dedicated microcontrol of an external signal samples were taken, sample 1 was at count 60, sample 2 was at conumber of overflow interrupts was one, and the timer frequency was	ements re tasks ller pins al, 2 -10 unt 10, i 1MHz, eriod is 562 us 306 us 206 us	0 0 0 0
Scheduling the different software tasks according to their timing requires Generating timely interrupts to trigger execution of software Generating PWM signals through dedicated microcontrol of If an 8 bit timer is used to measure the period of an external signal samples were taken, sample 1 was at count 60, sample 2 was at connumber of overflow interrupts was one, and the timer frequency was one in the measured point of the sample	ements re tasks ller pins al, 2 -10 unt 10, i 1MHz, eriod is 562 us 306 us 206 us	× ×

	duty cycle of a PWM signal that will vary from 10~95% with frequency :1KHz is	
	0.05 ms	0
	0.95 ms	0
	0.1 ms	0
×	1 ms	()
	المحتبحة	الإجابة
	0.95 ms	•
1/1	A switch that has four positions, to transfer the switch positions to a -12 microcontroller, while keeping minimum I/O usage, It is better to use	~
~	Analog voltage divider	•
	Four digital channels	0
	None of the previous	0
1/1	The complexity of a linear search algorithm is -13	~
~	O(n)	
	O(log n)	
	O(n^2)	0
0/1	Normally IIC pins should be -14	×
	Pulled up	\circ
	Pulled down	0
×	None of the previous	•
	المنتبحة	
	Pulled up	•
0/1	A PWM signal is not characterized by -15	×
	Frequncy	\circ
	Duty Cycle	\circ
	Rise time and fall time	0
×	None of the previous	•
	المحديحة	
	Rise time and fall time	•
1/1 Ti	he interrupt controller in ARM-based microcontrollers always support -16 nesting, whatever the micrcontroller vendor is	~
~	True	•
	False	\bigcirc

0/1 The critical section allowed on a timer based solution to measure the -11 X

	ARM cortex M family supports both SWD and JTAG protocols for -17 debugging, but SWD is preferred for its fewer interfacing pins	*
~	True	•
	False	0
1/1	In ARM processors code can only be fetched from code region to -18 prevent code injection attacks	~
	True	0
✓	False	•
1/1	In ARM cortex processors, the core communicates with the core -19 peripherals via the system bus	~
	True	
✓	False	•
0/1 C o	ore peripherals registers can only be accessed by assembly because -20 they are inside the processor	×
×	True	•
	False	0
	False المحديدة False	الإجابة
1/1	المحيحة	الإجابة
1/1	المنظية False Fetching data and instructions can be done simultaneously from the -21	الإجابة
1/1	آسحيدة False Fetching data and instructions can be done simultaneously from the -21 flash	الإجابة
1/1	المنتبة False Fetching data and instructions can be done simultaneously from the -21 flash True	()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()<
✓	الصحيحة False Fetching data and instructions can be done simultaneously from the -21 flash True False	(Yelli)
✓	False Fetching data and instructions can be done simultaneously from the -21 flash True False Processor can communicate with flash and SRAM simultaneously -22	()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()()
1/1	Fetching data and instructions can be done simultaneously from the -21 flash True False Processor can communicate with flash and SRAM simultaneously -22 True	(Yells () () () () () () () () () (
1/1	المسيحة False Fetching data and instructions can be done simultaneously from the -21 flash True False Processor can communicate with flash and SRAM simultaneously -22 True False us matrix in stm32f4xx microcontrollers is a single master multi slave -23	(a)(b)(c)(d)(d)(e)(e)(e)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)(f)<l< td=""></l<>

	The software should use specific assembly instructions					
×	X The software should undergo reset					
	None of the previous					
	الصحيحة	الإجابة				
	None of the previous	✓				
1/1	:To shift from privileged thread to unprivileged -25	~				
~	The software should write to the privilege bit in the CONTROL register	\checkmark				
~	The software should use specific assembly instructions	\checkmark				
	The software should undergo reset					
	None of the previous					
1/1	stm32 microcontrollers support multiple clock sources but only one -26 clock is turned on at a time	~				
	True	0				
✓	False	•				
0/1	using extern keyword informs the compiler of the variable type but -27 doesn't allocate memory for it	×				
	True	\circ				
×	False	•				
	المحيحة	الإجابة				
	True	•				
1/1 lf a	16-bit microcontroller is used with a 12-bit ADC to measure a signal -28 with 5 Volts maximum, The resolution is	~				
	1.2 uv	0				
~	1.2 mv	•				
	19.5 mv	0				
0/1	PWM signal that has 2 messages in it can be read by -29	×				
×	two interrupts	•				
	Edge counter	0				
	ICU	0				
	Polling None of the provings	_				
	None of the previous					
	المنتوجة ICU					
	100	0				

1/1	Clock skew is one of the disadvantages of serial communication -30 protools True	
~	False	
	لم يتم إنشاء هذا المحتوى ولا اعتماده من بيل Google. <u>- شروط الخدمة - سياسة الخصوصية</u> نصالاً ج Google	