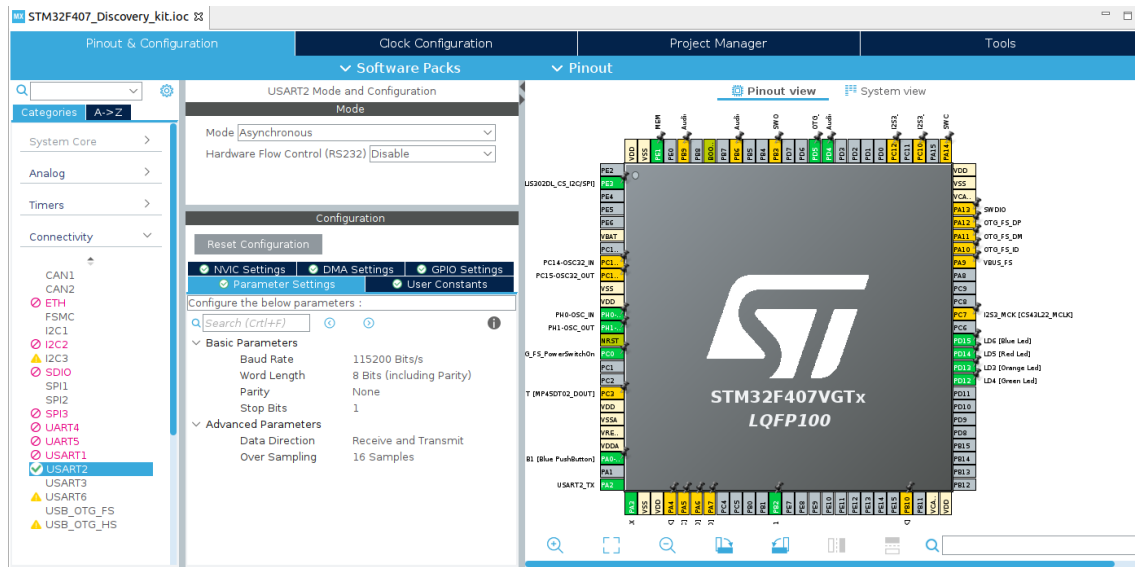
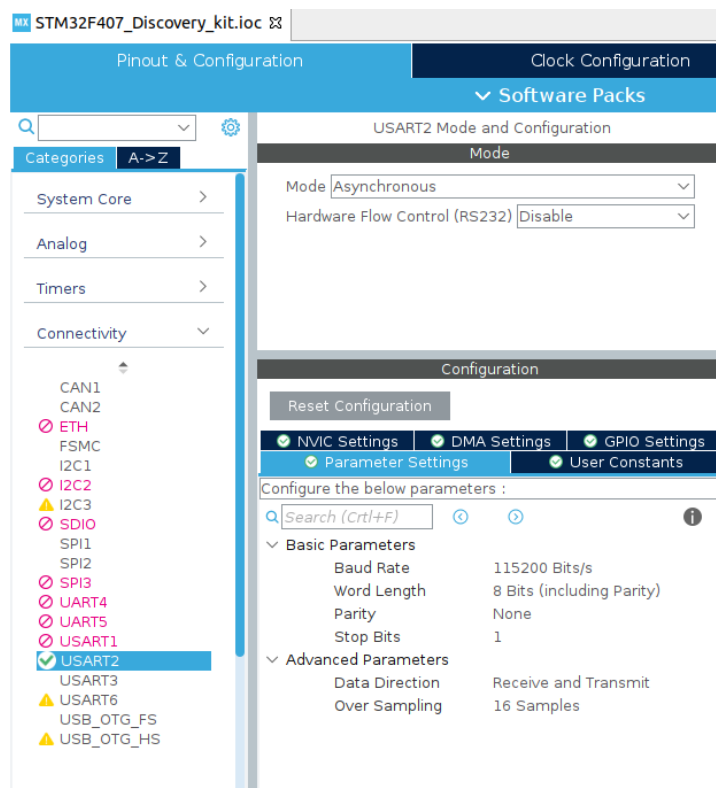


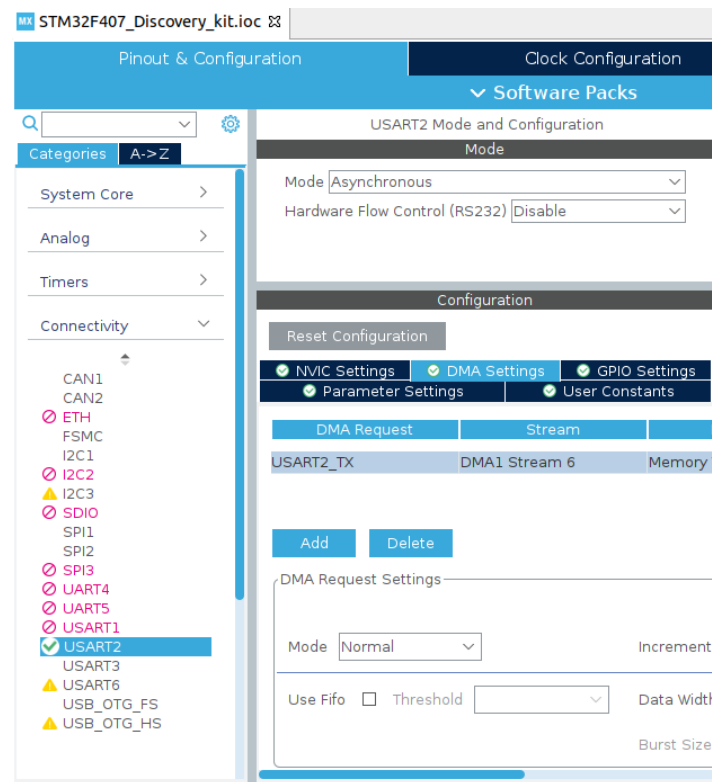
IOC FILE CONFIGURATION

- MOST OF THE CONFIGURATION IS SIMILAR TO THE CONFIGURATION WE MADE ON THE STM32F469 (https://github.com/ARM-Mbed/STM32F469_Discover_kit/blob/develop/IOC%20FILE%20CONFIGURATION.pdf)
- WE CAN SEE NOW HOW RENAME THE GPIO PINS ON THE 'STM32F407VGTxLQFP100', SELECTING THOSE PINS AS GPIO OUTPUT

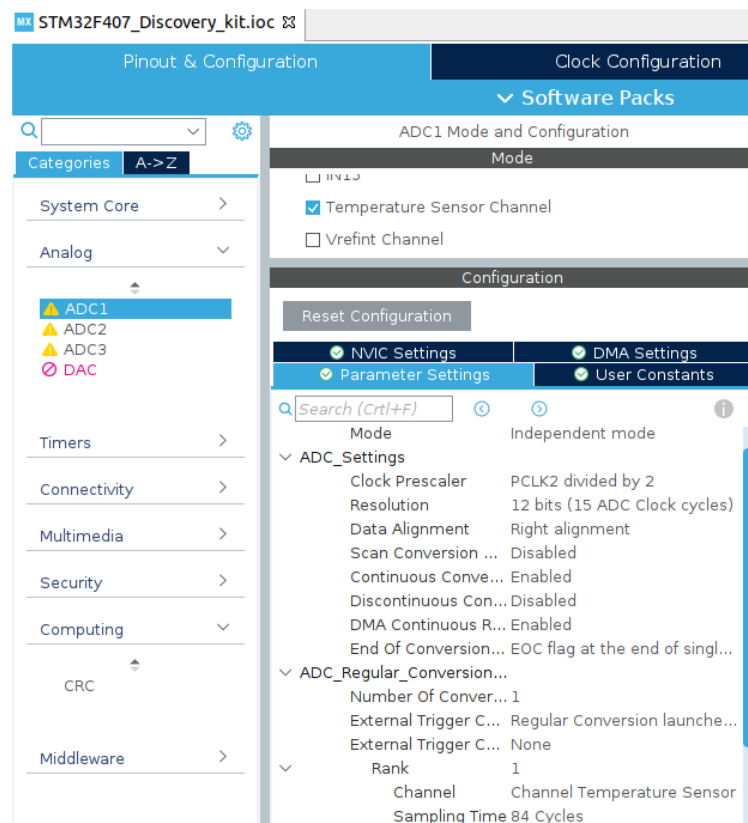


- USART CONFIGURATION ENABLE THE DMA INTERRUPT

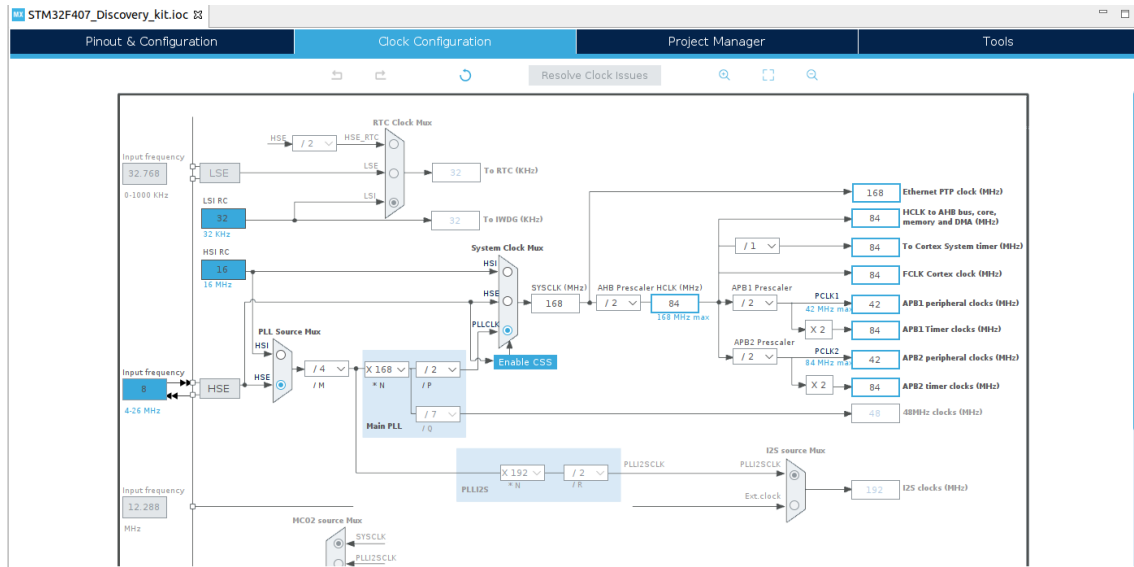




- ADC CONFIGURATION



- AND FINALLY WE CAN SEE HOW LOOKS LIKE THE CLOCK CONFIG AND THE PROJECT MANAGER, AFTER THIS SAVE OR GENERATE THE CODE OF THE PROJECT



The Project Manager and Code Generator interface shows the following sections:

- Project:** Driver Selector with a search bar and a list of drivers (GPIO, DMA, RCC, ADC, USART) with checkboxes for LL and HAL.
- Code Generator:** Generated Function Calls table with columns for Generate Code, Rank, Function Name, Peripheral Instance, Do Not Generate Function Call, and Visibility (Static).
- Advanced Settings:** Register CallBack section with a search bar and a list of registers (ADC, CAN, CEC, CRYP, DAC, DCMI, DFSDM, DMA2D, DSI, ETH, HASH, HCD, I2C, FMPI2C, I2S, IRDA, LPTIM, LTDC, MMC, NAND, NOR, PCCARD, PCF8594, QSPI, RNG, RTC, SAI, SD, SMARTCARD) with checkboxes for DISABLE and ENABLE.

Generate Code	Rank	Function Name	Peripheral Instance	Do Not Generate Function Call	Visibility (Static)
<input checked="" type="checkbox"/>	1	MX_GPIO_Init	GPIO	<input type="checkbox"/>	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/>	2	MX_DMA_Init	DMA	<input type="checkbox"/>	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/>	3	SystemClock_Config	RCC	<input type="checkbox"/>	<input type="checkbox"/>
<input checked="" type="checkbox"/>	4	MX_ADC1_Init	ADC1	<input type="checkbox"/>	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/>	5	MX_USART2_UART_Init	USART2	<input type="checkbox"/>	<input checked="" type="checkbox"/>