# Arm® Base System Architecture Compliance

Revision: r0p9

**User Guide** 



### **Arm® Base System Architecture Compliance**

#### **User Guide**

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#### **Release Information**

#### **Document History**

Issue	Date	Confidentiality	Change
0005-01	12 May 2021	Non-Confidential	Alpha release
0009-02	26 July 2021	Non-Confidential	Beta release

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## **Preface**

This preface introduces the Arm® Base System Architecture Compliance User Guide.

It contains the following:

• About this book on page 6.

#### About this book

This book is the user guide for Arm® BSA architecture compliance.

#### Using this book

This book is organized into the following chapters:

#### Chapter 1 UEFI shell application

This chapter provides an overview on the executing tests from the UEFI shell application.

#### Chapter 2 Linux application

This chapter provides an overview on executing tests from the Linux application.

#### Appendix A Revisions

This appendix describes the technical changes between released issues of this book.

#### Glossary

The Arm® Glossary is a list of terms used in Arm documentation, together with definitions for those terms. The Arm Glossary does not contain terms that are industry standard unless the Arm meaning differs from the generally accepted meaning.

See the *Arm Glossary* for more information.

#### Typographic conventions

italic

Introduces special terminology, denotes cross-references, and citations.

#### bold

Highlights interface elements, such as menu names. Denotes signal names. Also used for terms in descriptive lists, where appropriate.

#### monospace

Denotes text that you can enter at the keyboard, such as commands, file and program names, and source code.

#### <u>mono</u>space

Denotes a permitted abbreviation for a command or option. You can enter the underlined text instead of the full command or option name.

#### monospace italic

Denotes arguments to monospace text where the argument is to be replaced by a specific value.

#### monospace bold

Denotes language keywords when used outside example code.

<and>

Encloses replaceable terms for assembler syntax where they appear in code or code fragments. For example:

```
MRC p15, 0, <Rd>, <CRn>, <CRm>, <Opcode_2>
```

#### SMALL CAPITALS

Used in body text for a few terms that have specific technical meanings, that are defined in the *Arm*® *Glossary*. For example, IMPLEMENTATION DEFINED, IMPLEMENTATION SPECIFIC, UNKNOWN, and UNPREDICTABLE.

#### Additional reading

This book contains information that is specific to this product. See the following documents for other relevant information.

#### **Arm publications**

- Arm® Architecture Reference Manual ARMv8, for Armv8-A architecture profile (ARM DDI 0487G.a (ID011921))
- Arm® Generic Interrupt Controller Architecture Specification for GIC architecture version 3.0 and version 4.0 (ARM IHI 0069D ID072617)
- GICv3 and GICv4 Software Overview (DAI 0492)
- Arm® Base System Architecture 1.0 Platform Design Document (DEN0094A)

#### Other publications

None.

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- The number 102504 0009 02 en.
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#### Other information

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## Chapter 1 **UEFI shell application**

This chapter provides an overview on the executing tests from the UEFI shell application.

It contains the following sections:

- 1.1 Abbreviations on page 1-9.
- 1.2 Overview of tests on page 1-10.
- 1.3 UEFI shell application arguments on page 1-11.
- 1.4 Test IDs on page 1-13.
- 1.5 UEFI shell implementation of PAL APIs on page 1-14.

### 1.1 Abbreviations

This section lists the abbreviations used in this document.

Table 1-1 Abbreviations and expansions

Abbreviation	Expansion	
ACPI	Advanced Configuration and Power Interface	
BSA	Base System Architecture	
DT	Device Tree	
GIC	Generic Interrupt Controller	
PAL	Platform Abstraction Layer	
PCIe	Peripheral Component Interconnect express	
PE	Processing Element	
SMC	Secure Monitor Call	
SMMU	System Memory Management Unit	
UEFI	Unified Extensible Firmware Interface	

#### 1.2 Overview of tests

The following table describes the general divisions of Base System Architecture (BSA) tests between Unified Extensible Firmware Interface (UEFI) shell application and Linux application.

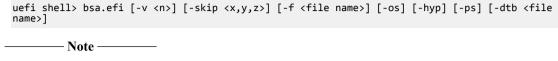
Table 1-2 Test environment and modules

Test environment	Modules
UEFI shell	PE, GIC, Timers, Watchdog, Wakeup and Power, PCIe, Memory map, Exerciser, Peripheral, and SMMU
Linux command line	PCIe, Memory map, and Peripheral

#### 1.3 UEFI shell application arguments

This section provides information on the UEFI shell application arguments.

Run the UEFI shell application with the following set of arguments.

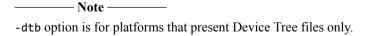


The shell session becomes unusable after all the BSA tests are run and the test results are printed on the UEFI console.

The following table provides descriptions to the arguments.

Table 1-3 Descriptions of UEFI application arguments

Argument	Description		
-v	Print level		
	1 INFO and above.		
	2 DEBUG and above.		
	3 TEST and above.		
	4 WARN and ERROR.		
	5 ERROR.		
-skip	Overrides the suite to skip the execution of a particular test. It allows a maximum of nine values (comma-separated).		
	For example, 302 skips test case with ID = 302.		
	200 skips all tests in module with $ID = 200$ .		
	For more information on module IDs, see the 1.4 Test IDs on page 1-13.		
-f	File name to which the output log is written.		
-os	By default, all the operating system, hypervisor, platform security view tests are run.		
-hyp	To run specific tests, add the following options:		
-ps	-os: Run operating system view tests.		
	• -hyp: Run hypervisor view tests.		
	-ps: Run platform security view tests.		
-dtb	Dumps the board Device Tree (DT) blob into the specified file.		



#### Example

```
shell > bsa.efi -v 2 -skip 200,302 -f acs.txt -os -dtb platform.dtb
```

The set of parameters shown in the code block:

- Prints messages with verbosity of 2 and above.
- Tests for compliance against operating system view tests.
- Skips execution of all tests belonging to Generic Interrupt Controller (GIC) module and test number 302.

- Stores the log messages to the file acs.txt. Stores the dtb into the platform.dtb file.

#### 1.4 Test IDs

This section provides information on module names and test IDs.

The test ID of each test is generated as an addition to module ID and unit test ID. For a given module, unit test ID begins from 1.

The following table describes the module name and module IDs.

Table 1-4 Module name and Module ID

Module name	Module ID
PE	0
Memory Map	100
GIC	200
SMMU	300
Timer	400
Wakeup and Power	500
Peripheral	600
Watchdog	700
PCIe	800
Exerciser	900

Note	
Each module has tests classified as operating system, hypervisor, and platform security as $d = 88 \times 1.0$ specification.	efined by the

### 1.5 UEFI shell implementation of PAL APIs

This section provides information on infrastructure APIs and module-specific APIs.

Booting to a UEFI shell is a prerequisite for running a BSA test.

#### **Infrastructure APIs**

The following table describes the Platform Abstraction Layer (PAL) APIs and UEFI interfaces.

Table 1-5 PAL APIs and UEFI interfaces

PAL API	UEFI interface
pal_print	AsciiPrint
mem_alloc	gBS->AllocatePool
mem_free	gBS->FreePool
mem_alloc_shared	gBS->AllocatePool
mem_free_shared	gBS->FreePool
mem_get_shared_addr	None
mmio_read	None
mmio_write	None

#### **Module-specific APIs**

The following table represents the PAL API to Advanced Configuration and Power Interface (ACPI) table mapping, if the system firmware presents platform configuration through ACPI tables.

Table 1-6 PAL APIs, UEFI interfaces, and ACPI tables consumed

PAL APIs	UEFI interfaces	ACPI tables consumed
pe_create_info_table	<ul><li>gST-&gt;ConfigurationTable</li><li>CompareGuid</li><li>IndustryStandard/Acpi61.h</li></ul>	MADT Table
call_smc	None	-
pe_execute_payload	None	-
pe_install_esr	gEfiCpuArchProtocolGuid     Cpu->RegisterInterruptHandler	-
gic_create_info_table	<ul><li>gST-&gt;ConfigurationTable</li><li>CompareGuid</li><li>IndustryStandard/Acpi61.h</li></ul>	MADT table
gic_install_isr	<ul> <li>gHardwareInterruptProtocolGuid</li> <li>RegisterInterruptSource</li> <li>EnableInterruptSource</li> </ul>	-
timer_create_info_table	<ul><li>gST-&gt;ConfigurationTable</li><li>CompareGuid</li><li>IndustryStandard/Acpi61.h</li></ul>	GTDT table
wd_create_info_table	<ul><li>gST-&gt;ConfigurationTable</li><li>CompareGuid</li><li>IndustryStandard/Acpi61.h</li></ul>	GTDT table
pcie_create_info_table	<ul><li>gST-&gt;ConfigurationTable</li><li>CompareGuid</li><li>IndustryStandard/Acpi61.h</li></ul>	MCFG table
pcie_get_mcfg_ecam	<ul> <li>gST-&gt;ConfigurationTable</li> <li>CompareGuid, IndustryStandard/Acpi61.h</li> <li>IndustryStandard/ MemoryMappedConfigurationSpaceAccessTable.h</li> </ul>	MCFG table
iovirt_create_info_table	<ul><li>gST-&gt;ConfigurationTable</li><li>CompareGuid</li><li>IndustryStandard/Acpi61.h</li></ul>	IORT table
peripheral_create_info_table	<ul><li>gEfiPciIoProtocolGuid</li><li>Pci-&gt;GetLocation</li><li>Pci-&gt;Pci.Read</li></ul>	-
memory_create_info_table	gBS->GetMemoryMap	-

The following table represents the PAL API to DT node mapping, if the system firmware presents platform configuration through DT nodes.

Table 1-7 PAL APIs, UEFI interfaces, and DT nodes consumed

PAL APIs	UEFI interfaces	DT nodes consumed
pe_create_info_table	• gST->ConfigurationTable • CompareGuid	cpu, pmu, interrupt-controller node
gic_create_info_table	<ul><li>gST-&gt;ConfigurationTable</li><li>CompareGuid</li></ul>	interrupt-controller, v2m and its nodes
timer_create_info_table	• gST->ConfigurationTable • CompareGuid	systimer and memory mapped timer nodes
wd_create_info_table	• gST->ConfigurationTable • CompareGuid	watchdog nodes
pcie_create_info_table	• gST->ConfigurationTable • CompareGuid	pcie node
iovirt_create_info_table	<ul><li>gST-&gt;ConfigurationTable</li><li>CompareGuid</li></ul>	smmu node
peripheral_create_info_table	<ul><li> gST-&gt;ConfigurationTable</li><li> CompareGuid</li></ul>	usb, and uart node
memory_create_info_table	gBS->GetMemoryMap	-

## Chapter 2 **Linux application**

This chapter provides an overview on executing tests from the Linux application.

It contains the following section:

• 2.1 Linux application arguments on page 2-18.

### 2.1 Linux application arguments

This section provides information on the Linux application arguments.

Run the Linux application with the following set of arguments.

```
shell> bsa [--v < n>] [--skip < x,y,z>]
```

Table 2-1 Description of Linux application arguments

Argument	Description		
v	Print level	Print level	
	1	INFO and above	
	2	DEBUG and above	
	3	TEST and above	
	4	WARN and ERROR	
	5	ERROR	
skip	Overrides the suite to skip the execution of a particular test.		
	For example, 53 skips test case with ID 53.		

#### **Example**

```
shell> bsa --v 3 --skip 57
```

This set of parameters tests for compliance against BSA with print verbosity set to 3, and skips test number 57.

#### Loading the kernel module

Before the BSA ACS Linux application can be run, load the BSA ACS kernel module using the insmod command.

shell> insmod bsa\_acs.ko

## Appendix A **Revisions**

This appendix describes the technical changes between released issues of this book.

It contains the following section:

• A.1 Revisions on page Appx-A-20.

#### A.1 Revisions

This section consists of all the technical changes between different versions of this document.

#### Table A-1 Issue 0005-01

Change	Location
First release	-

Table A-2 Issue 0005-01 to Issue 0009-02

Change	Location
Added the abbreviation for SMMU in the list.	See 1.1 Abbreviations on page 1-9.
Added [-dtb <file name="">], a UEFI shell argument with its descriptions.</file>	See 1.3 UEFI shell application arguments on page 1-11.