PRODUCT SPECIFICATION

IEEE 802.11 b/g/n 2.4GHz Internet-of-Things WiFi Module

(Realtek Ameba Series)

WFM-250

(RTL8195AM) Single Module

Data Sheet V1.1 (2017/10/25)

Table of Contents

Document History

Revision	Date	Description
V1.0	2015/09/24	Initial version
V1.1	2017/10/25	Add Label and packaging design

1. Product Overview

Realtek RTL8195AM is a highly integrated single-chip low power 802.11n wireless LAN (WLAN) network controller. It combines an ARM-Cortex M3 MCU, WLAN MAC, a 1T1R capable WLAN baseband, and RF in a single chip. It provides useful high speed connectivity interfaces, such as USB 2.0 host, USB 2.0 device, SDMMC HS, SDIO device, and MII/RMII interfaces. It also provides a bunch of configurable GPIOs which are configured as digital peripherals for different applications and control usage.

RTL8195AM integrates intemall memories for complete WIFI prowcol functions. The embedded memory configuration also provides simple application developments.

2. Module Parameter

2.1 Basic Parameter

Feature	Detailed Description
Antenna Type	IPEX compatible antenna or Chip antenna
Main chip	RTL8195AM
Frequency range	2.412GHz-2.484GHz
CPU clock	166MHz
Memory capacity	ROM 1MB SRAM 496KB FLASH 2MB
Flash size	8MB
PCB stack	4 layers
Operating Voltage	DC 3.3V
Form factor	Half size Mini-Card 18X21X1.0mm
Other Interface	UART、JTAG、I2C、I2S、SPI、SDIO

2.2 Current Consumption

Symbol	parameter	Minimum	Typical	Maximum	Units
VA33, VD33IO, SW_HV3	3.3V Supply Voltage	3.0	3.3	3.6	V
VDD_IO	Digital IO Supply Voltage	1.62	1.8~3.3	3.6	V
VA12_AFE, VA12_SYN, VA12_RF	1.2V Core Supply Voltage	1.08	1.2	1.32	V
IDD33	3.3V Rating Current (with internal regulator and integrated CMOS PA)			450	mA
IDD_IO	IO Rating Current(including VDD_IO)			200	mA
IDD_IO_33	3.3V IO Rating Current			50	mA

3.DVT Report

Mode:11b-HT	Tranceiver:A			Bandwidth:20MHz				Data Rate:11Mbps					
Channel	1	2	3	4	5	6	7	8	9	10	11	12	13	Crt.
Pass/Fail	V	V	V	V	V	V	V	V	V	V	V	V	V	
Gain Stage(Dec)	48	48	48	48	48	48	48	48	48	48	48	48	48	
Output Power(dBm)	18.7	17.3	18.4	18.4	18.4	18.2	18.2	18	17.9	17.7	17.4	17.5	17.1	> 17
EVM(%)	5.99	6.79	5.95	6	6.81	5.98	6.75	6.18	6.23	6.68	6.37	6.81	6.1	<8
Freq.Offset(ppm)	-0.336	-0.4	0.61	0.44	0.44	0.51	0.58	0.47	0.43	0.32	0.1	0.44	0.09	±25
Mask	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	
Carr.Leakage(dB)	-53.49	-46.68	-45.26	-42.01	-38.16	-42.75	-46.93	-46.27	-37.43	-54.78	-38.79	-52.94	-37.46	<2

Mode:11g-HT	Tranceiver:A				Bandwidth:20MHz					Data Rate:54Mbps				
Channel	1	2	3	4	5	6	7	8	9	10	11	12	13	Crt.
Pass/Fail	V	V	V	V	V	V	V	V	V	V	V	V	V	
Gain Stage(Dec)	46	46	46	47	47	47	48	48	48	49	49	50	50	
Output Power(dBm)	15.2	15.5	15.4	15.4	15.3	15.1	15.4	15.3	15.1	15.2	15.2	15.4	15.2	>15
EVM(dB)	-31	-31	-31	-32	-31	-31	-31	-32	-32	-32	-31	-31	-31	<-28
Freq.Offset(ppm)	-0	-0.2	0.02	-0.4	-0.2	-0.5	-0.5	-0.3	-0.2	-0.5	-0.2	-0.4	-0.3	±25
Mask	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	
Carr.Leakage(dB)	-38.31	-40.29	-42.24	-37.09	-38.53	-40.03	-42.37	-41.86	-40.03	-40.53	-38.31	-39.43	-39.47	<-15

Mode:11n-HT	Tranceiver:A				Bandwidth:20MHz					Data Rate:MCS7				
Channel	1	2	3	4	5	6	7	8	9	10	11	12	13	Crt.
Pass/Fail	V	V	V	V	V	V	V	V	V	V	/	V	V	
Gain Stage(Dec)	42	42	42	43	43	43	44	44	45	45	45	46	46	
Output Power(dBm)	13.2	13.1	13.2	13.5	13.3	13.1	13.4	13.2	13.2	13.2	13.1	13.3	13.1	> 13
EVM(dB)	-34	-32	-32	-34	-35	-34	-35	-34	-36	-36	-33	-34	-33	<-30
Freq.Offset(ppm)	0.04	-0.004	-0.1	-0.3	-0.1	-0.4	-0.3	-0.2	-0.4	-0.4	-0.3	-0.4	-0.5	±25
Mask	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	PASS	
Carr.Leakage(dB)	-37.82	-38.23	-38.65	-38.05	-37.95	-37.42	-38.61	-39.22	-39.60	-40.18	-40.74	-40.56	-38.02	<-15

Mode:11n-HT	Tranceiver:A			Bandwidth:40MHz				Data Rate:MCS7
Channel	3	4	5	6	7	8	9	Crt.
Pass/Fail	V	V	V	V	V	V	V	
Gain Stage(Dec)	42	42	43	43	44	44	45	
Output Power(dBm)	13.26	13.11	13.24	13.10	13.11	13.26	13.48	> 13
EVM(dB)	-32.48	-35.45	-34.35	-33.24	-35.94	-35.64	-32.54	<-30
Freq.Offset(ppm)	-0.21	-0.26	-0.43	-0.27	-0.41	-0.34	-0.42	±25
Mask	PASS	PASS	PASS	PASS	PASS	PASS	PASS	
Carr.Leakage(dB)	-52.21	-55.92	-54.07	-66.15	-54.82	-54.18	-67.41	<-15

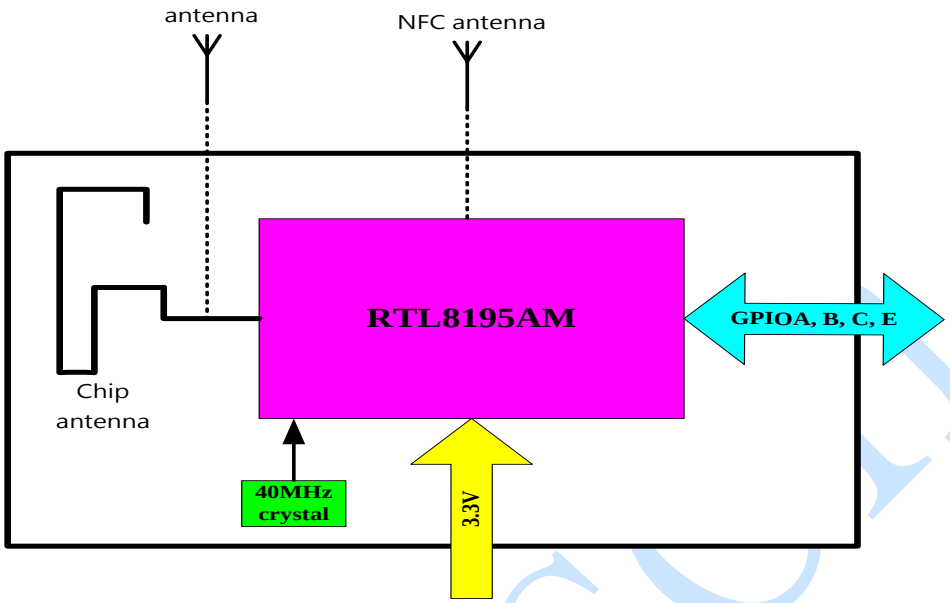
Mode:11b-HT	Receiver:A				Bandwidth:20MHz									
Channel	1	2	3	4	5	6	7	8	9	10	11	12	13	Crt.
11M	-87	-87	-87	-87	-87	-87	-87	-87	-87	-87	-87	-87	-87	≤-76
5.5M	-90	-90	-90	-90	-90	-90	-90	-90	-90	-90	-90	-90	-90	≤-79
2M	-93	-93	-93	-93	-93	-93	-93	-92	-92	-92	-92	-92	-92	≤-80
1M	-94	-94	-94	-94	-94	-94	-94	-94	-94	-94	-94	-94	-94	≤-83

Mode:11g-HT	Receiver:A				Bandwidth:20MHz									
Channel	1	2	3	4	5	6	7	8	9	10	11	12	13	Crt.
54M	-74	-74	-74	-74	-74	-74	-74	-73	-73	-73	-73	-73	-73	≤-65
48M	-76	-76	-76	-76	-76	-76	-75	-75	-75	-75	-75	-75	-75	≤-66
36M	-80	-80	-80	-80	-80	-80	-80	-79	-79	-79	-79	-79	-79	≤-70
24M	-83	-83	-83	-83	-83	-83	-83	-83	-83	-83	-83	-83	-83	≤-74
18M	-86	-86	-86	-86	-86	-86	-85	-85	-85	-85	-85	-85	-85	≤-77
12M	-88	-88	-88	-88	-88	-88	-88	-88	-88	-88	-88	-88	-88	≤-79
9M	-90	-90	-90	-90	-90	-90	-90	-88	-88	-88	-88	-88	-88	≤-81
6M	-91	-91	-91	-91	-91	-91	-90	-90	-90	-90	-90	-90	-90	≤-82

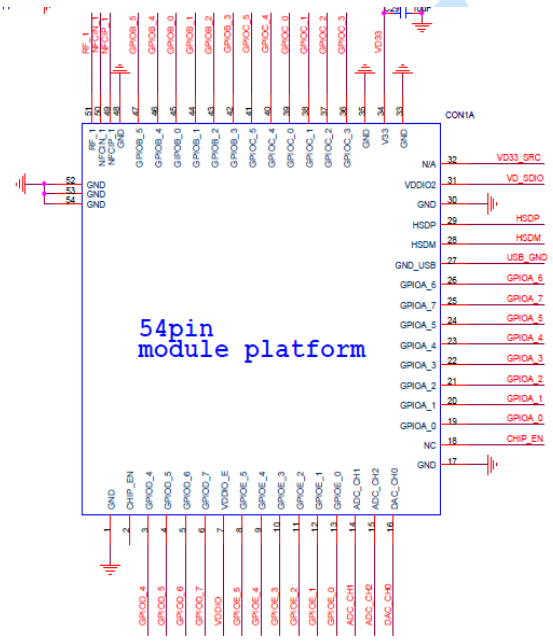
Mode:11n-HT	Receiver:A				Bandwidth:20MHz									
Channel	1	2	3	4	5	6	7	8	9	10	11	12	13	Crt.
MCS7	-71	-71	-71	-71	-71	-71	-71	-70	-70	-70	-70	-70	-70	≤-64
MCS6	-73	-73	-73	-73	-73	-73	-73	-73	-73	-73	-73	-73	-73	≤-65
MCS5	-74	-74	-74	-74	-74	-74	-73	-73	-73	-73	-73	-73	-73	≤-66
MCS4	-78	-78	-78	-78	-78	-78	-77	-77	-77	-77	-77	-77	-77	≤-70
MCS3	-82	-82	-82	-82	-82	-82	-81	-81	-81	-81	-81	-81	-81	≤-74
MCS2	-85	-85	-85	-85	-85	-85	-84	-84	-84	-84	-84	-84	-84	≤-77
MCS1	-87	-87	-87	-87	-87	-87	-86	-86	-86	-86	-86	-86	-86	≤-79
MCS0	-91	-91	-91	-91	-91	-91	-90	-90	-90	-90	-90	-90	-90	≤-82

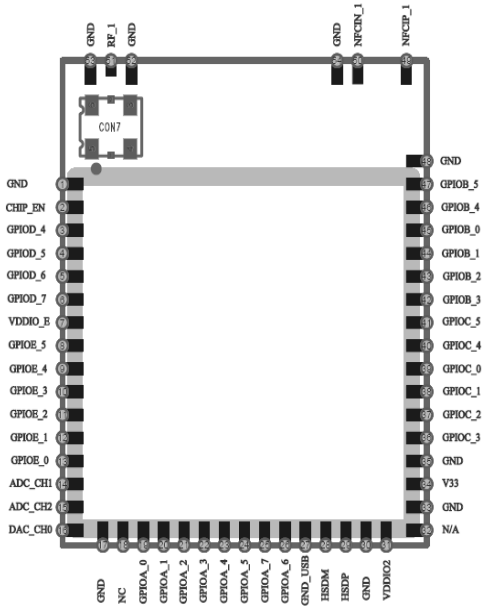
Mode:11n-HT	Receiver:A			Bandwidth:40MHz				
Channel	3	4	5	6	7	8	9	Crt.
MCS7	-67	-67	-67	-68	-68	-68	-68	≤-64
MCS6	-69	-69	-69	-69	-69	-69	-69	≤-65
MCS5	-71	-71	-71	-71	-71	-71	-71	≤-66
MCS4	-75	-75	-75	-75	-75	-75	-75	≤-69
MCS3	-79	-79	-79	-79	-79	-79	-79	≤-72
MCS2	-81	-81	-81	-82	-82	-82	-82	≤-76
MCS1	-85	-85	-85	-85	-85	-85	-85	≤-78
MCS0	-88.5	-88.5	-88.5	-88.5	-88.5	-88.5	-88.5	≤-80

4. Block Diagram



5. Pin Assignment on module





Abbreviations in used:

I: Input AI: Analog Input O: Output AO: Analog Output

IO: Bi-Directional Input/Output AI/O: Analog Bi-Directional Input/Output

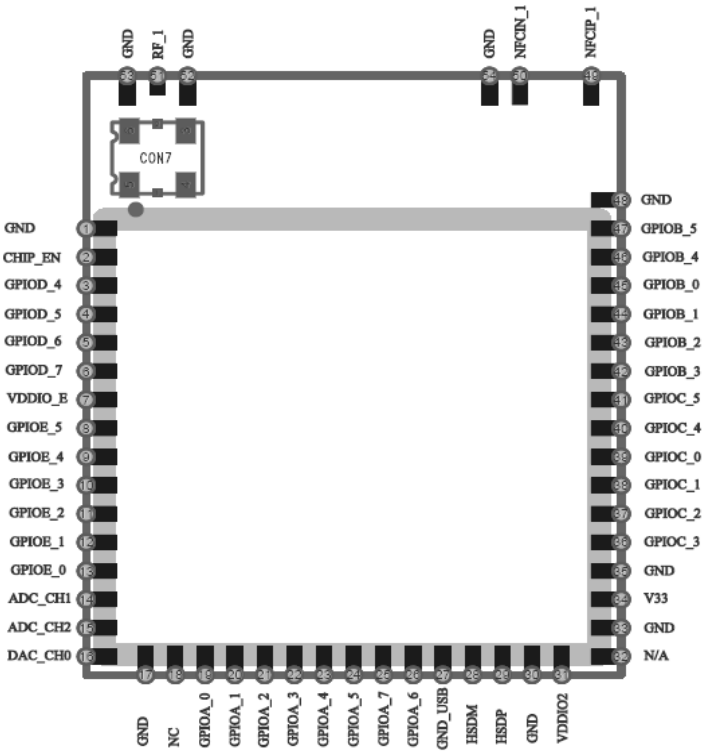
P: Digital Power G: Digital Ground

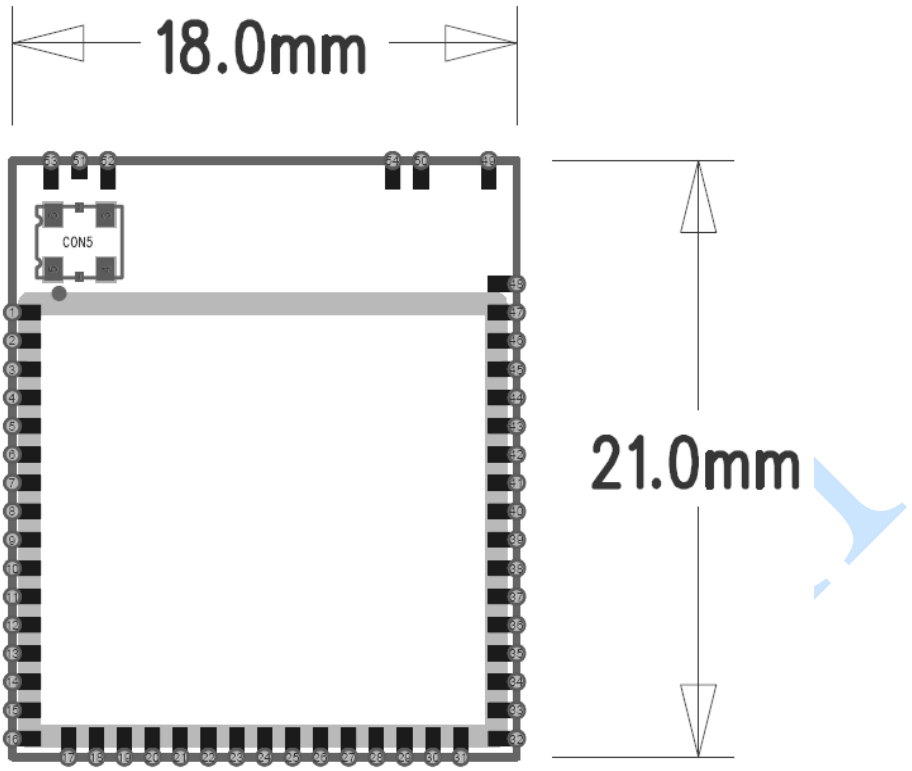
PIN	NAME	TYPE	PIN	NAME	TYPE
1	GND	GND	28	HSDM	IO
2	CHIP_EN	NC	29	HSDP	IO
3	GPIOD_4	IO	30	GND	GND
4	GPIOD_5	IO	31	VDDIO2	P
5	GPIOD_6	IO	32	N/A	P
6	GPIOD_7	IO	33	GND	GND
7	VDDIO_E	P	34	V33	P
8	GPIOE_5	IO	35	GND	GND
9	GPIOE_4	IO	36	GPIOC_3	IO
10	GPIOE_3	IO	37	GPIOC_2	IO
11	GPIOE_2	IO	38	GPIOC_1	IO
12	GPIOE_1	IO	39	GPIOC_0	IO
13	GPIOE_0	IO	40	GPIOC_4	IO
14	ADC_CH1	I	41	GPIOC_5	IO
15	ADC_CH2	I	42	GPIOB_3	IO
16	DAC_CH0	O	43	GPIOB_2	IO
17	GND	GND	44	GPIOB_1	IO
18	NC	I	45	GPIOB_0	IO
19	GPIOA_0	IO	46	GPIOB_4	IO
20	GPIOA_1	IO	47	GPIOB_5	IO
21	GPIOA_2	IO	48	GND	GND
22	GPIOA_3	IO	49	NFCIP_1	I
23	GPIOA_4	IO	50	NFCIN_1	I
24	GPIOA_5	IO	51	RF_1	IO
25	GPIOA_7	IO	52	GND	GND
26	GPIOA_6	IO	53	GND	GND
27	GND_USB	GND	54	GND	GND

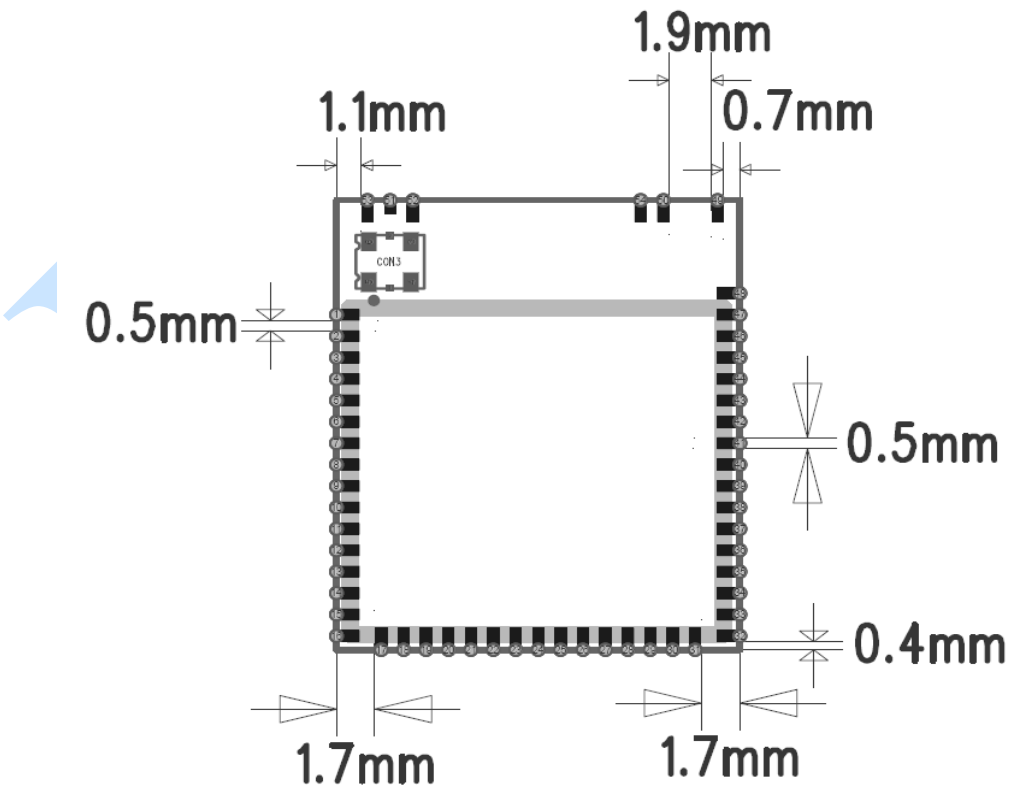
※Pin configurable function group summary table

PIN name	JTAG	SDD	SDH	MII	UART Group	I2C Group	SPI Group	I2S Group	PCM Group	WL_LED	PWM	ETE	WKDT	GPIO INT
GPIOA_0		SD_D2	SD_D2	MII_RX_CK	UART2_IN		SPI1_MISO							GPIO_INT
GPIOA_1		SD_D3	SD_D3	MII_RXD0	UART2_CTS		SPI1_MOSI							GPIO_INT
GPIOA_2		SD_CMD	SD_CMD	MII_RXD1	UART2_RTS		SPI1_CLK							
GPIOA_3		SD_CLK	SD_CLK	MII_RXD2	UART0_RTS									
GPIOA_4		SD_D0	SD_D0	MII_RXD3	UART2_OUT		SPI1_CS							
GPIOA_5		SD_D1	SD_D1	MII_RXDV	UART0_CTS								WKDT0	
GPIOA_6		SD_INT	SD_CD	MII_RXERR	UART0_IN									
GPIOA_7			SD_WP	MII_COL	UART0_OUT									
GPIOB_0					UART_LOG_OUT							ETE0		
GPIOB_1					UART_LOG_IN					WL_LED0		ETE1		
GPIOB_2						I2C3_SCL						ETE2		
GPIOB_3						I2C3_SDA						ETE3		GPIO_INT
GPIOB_4										WL_LED0	PWM0			GPIO_INT
GPIOB_5										WL_LED0	PWM1			
GPIOC_0				MII_TXD2	UART0_IN		SPI0_CS0	I2S1_WS	PCM1_SYNC		PWM0	ETE0		
GPIOC_1				MII_TXD1	UART0_CTS		SPI0_CLK	I2S1_CLK	PCM1_CLK		PWM1	ETE1		GPIO_INT
GPIOC_2				MII_TXD0	UART0_RTS		SPI0_MOSI	I2S1_SD_TX	PCM1_OUT		PWM2	ETE2		
GPIOC_3				MII_TX_CK	UART0_OUT		SPI0_MISO	I2S1_MCK	PCM1_IN		PWM3	ETE3		GPIO_INT
GPIOC_4				MII_TXD3		I2C1_SDA	SPI0_CS1	I2S1_SD_RX						GPIO_INT
GPIOC_5				MII_TXEN		I2C1_SCL	SPI0_CS2							GPIO_INT
GPIOD_4				MII_MDC	UART2_IN	I2C0_SDA	SPI1_CS		PCM1_SYNC		PWM0	ETE0		GPIO_INT
GPIOD_5				MII_MDIO	UART2_CTS	I2C0_SCL	SPI1_CLK		PCM1_CLK		PWM1	ETE1	WKDT2	GPIO_INT
GPIOD_6					UART2_RTS	I2C1_SCL	SPI1_MOSI	I2S0_SD_RX	PCM1_OUT		PWM2	ETE2		GPIO_INT
GPIOD_7					UART2_OUT	I2C1_SDA	SPI1_MISO		PCM1_IN		PWM3	ETE3		GPIO_INT
GPIOE_0	JTAG_TRST				UART0_OUT	I2C2_SCL	SPI0_CS0	I2S0_WS	PCM0_SYNC		PWM0			
GPIOE_1	JTAG_TDI				UART0_RTS	I2C2_SDA	SPI0_CLK	I2S0_CLK	PCM0_CLK		PWM1			GPIO_INT
GPIOE_2	JTAG_TDO				UART0_CTS	I2C3_SCL	SPI0_MOSI	I2S0_SD_TX	PCM0_OUT		PWM2			GPIO_INT
GPIOE_3	JTAG_TMS				UART0_IN	I2C3_SDA	SPI0_MISO	I2S0_MCK	PCM0_IN		PWM3		WKDT3	GPIO_INT
GPIOE_4	JTAG_CLK					I2C3_SCL	SPI0_CS1							
GPIOE_5						I2C3_SDA	SPI0_CS2							GPIO_INT

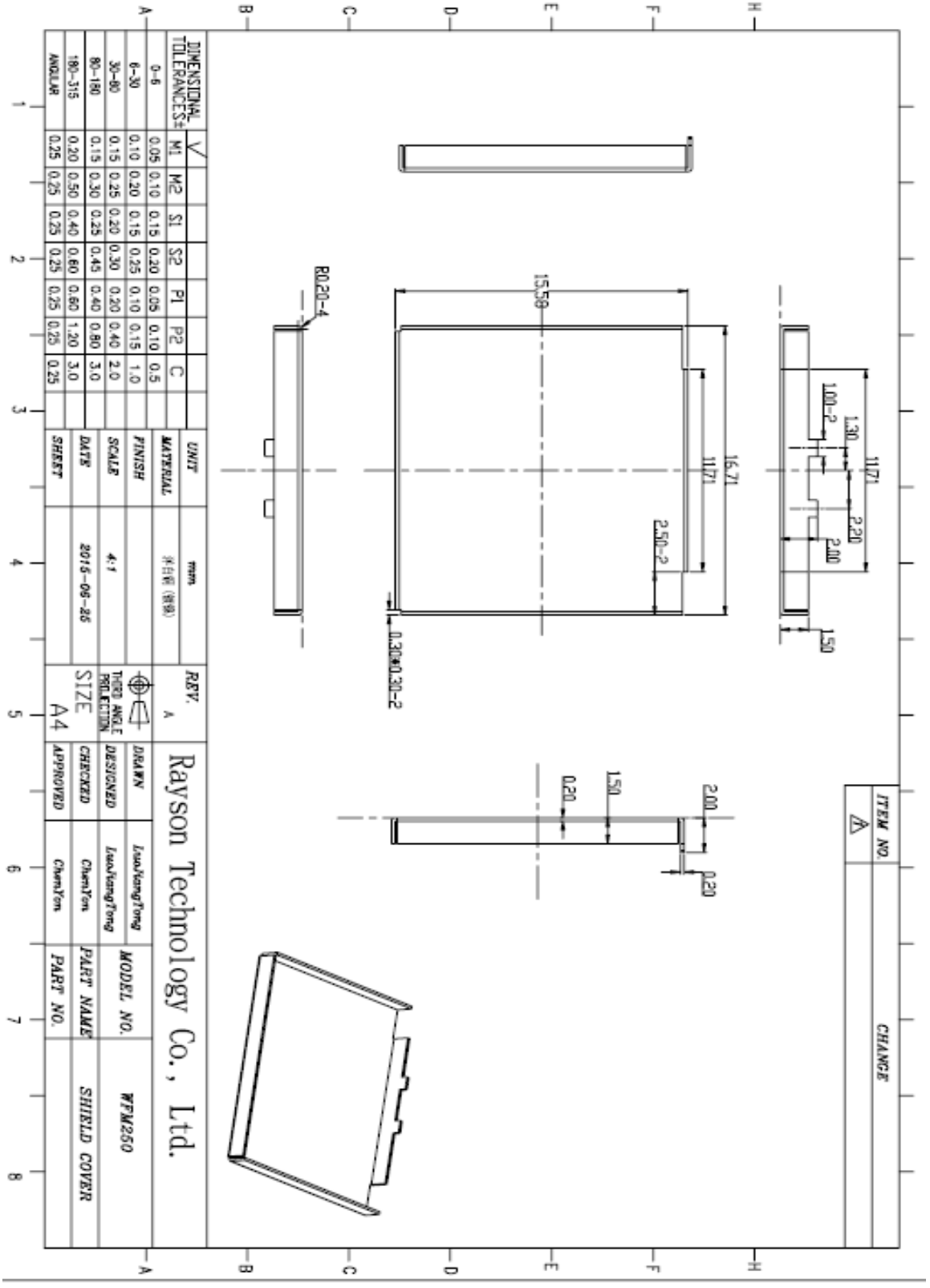
6. Interface Specifications




18.0mm
CON5
21.0mm

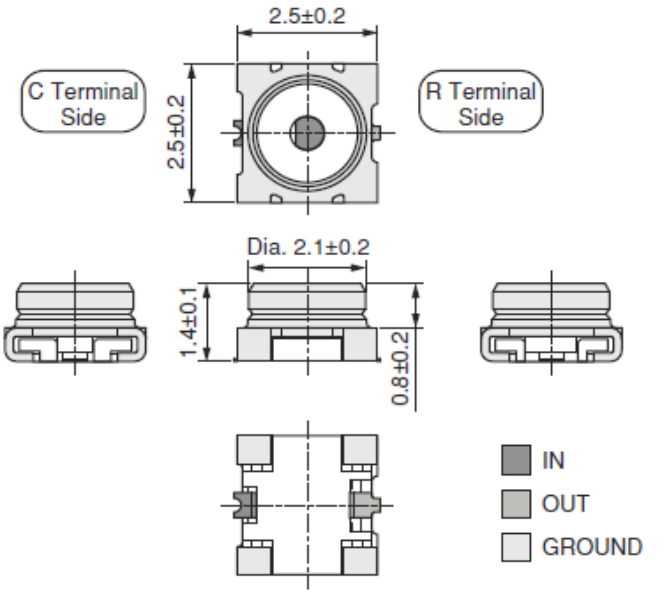

1.9mm
1.1mm
0.7mm
CON3
0.5mm
0.5mm
0.4mm
1.7mm
1.7mm

7. shield cover outline drawing



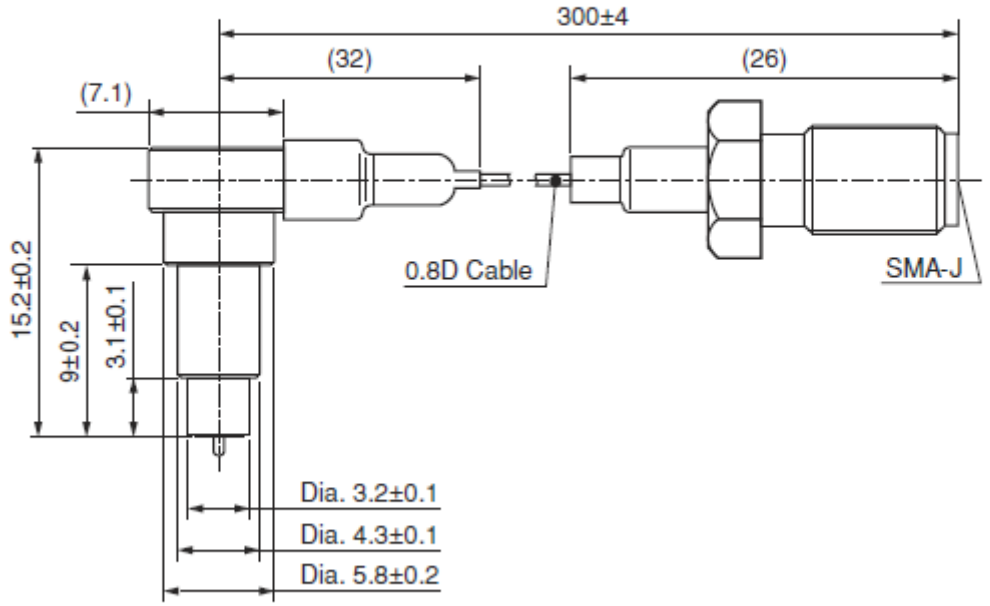
8.RF Connector

MM8130-2600



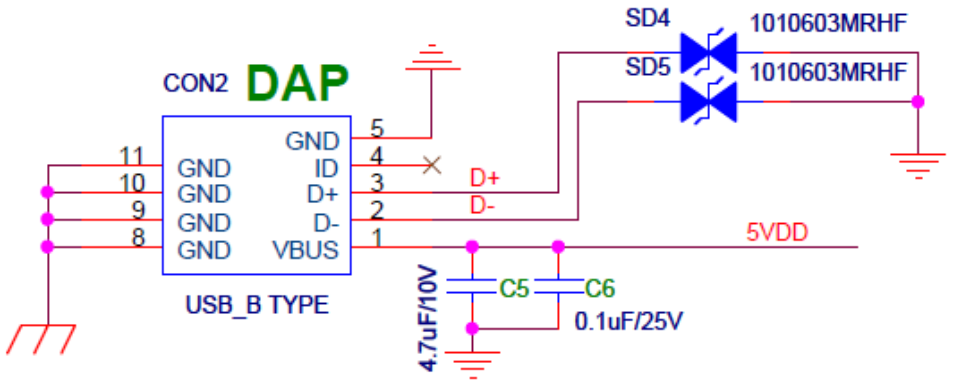
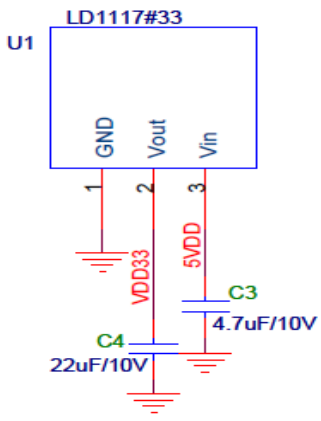
(in mm)

Measurement Probe for MM8130-2600/MM8430-2610 (P/N: MXHS83QE3000)

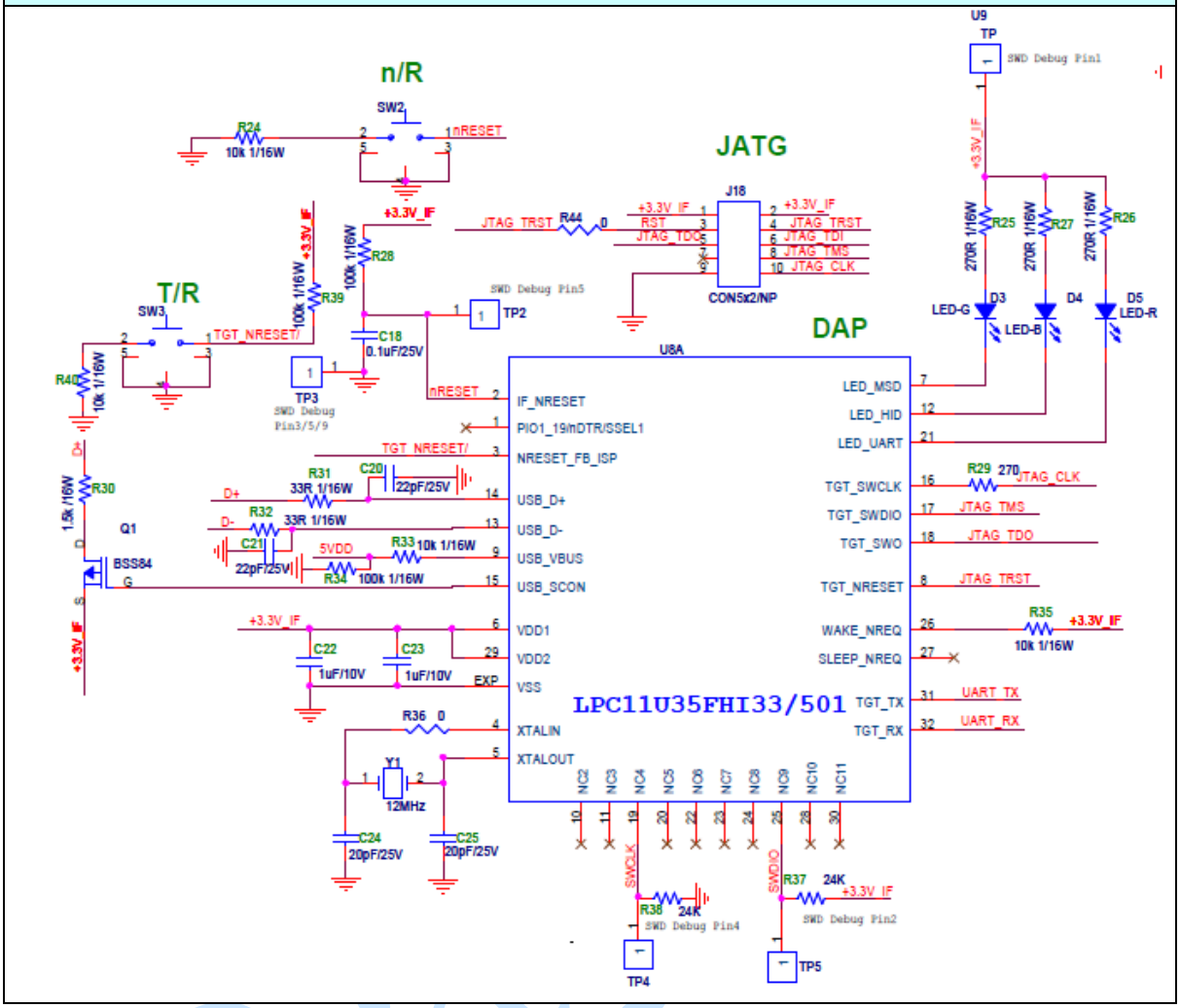


(in mm)

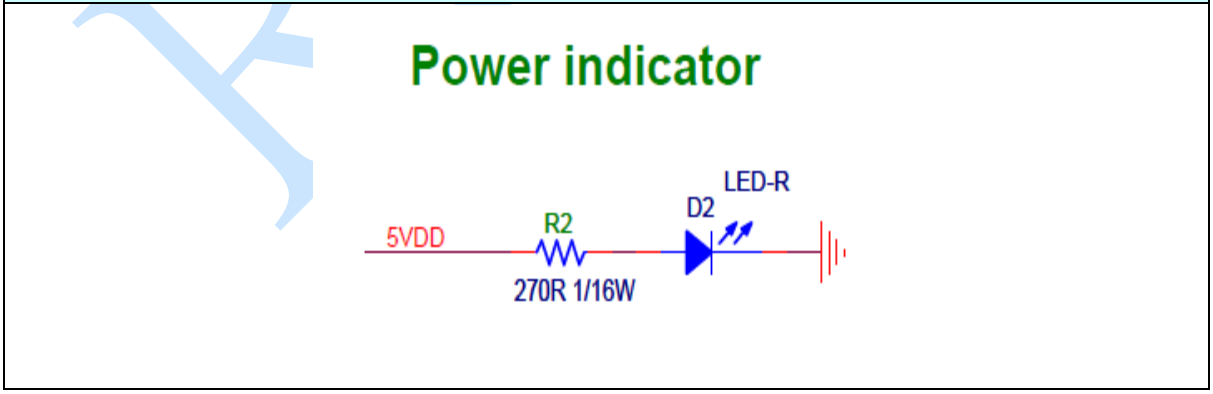
9. Reference Circuit

Power Supply



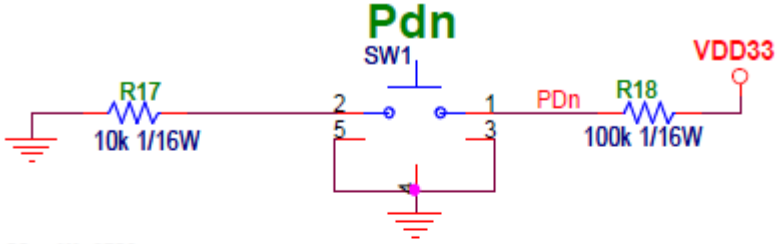
Conversion circuit



LED Circuit



Button Circuit



PDn SW CTRL

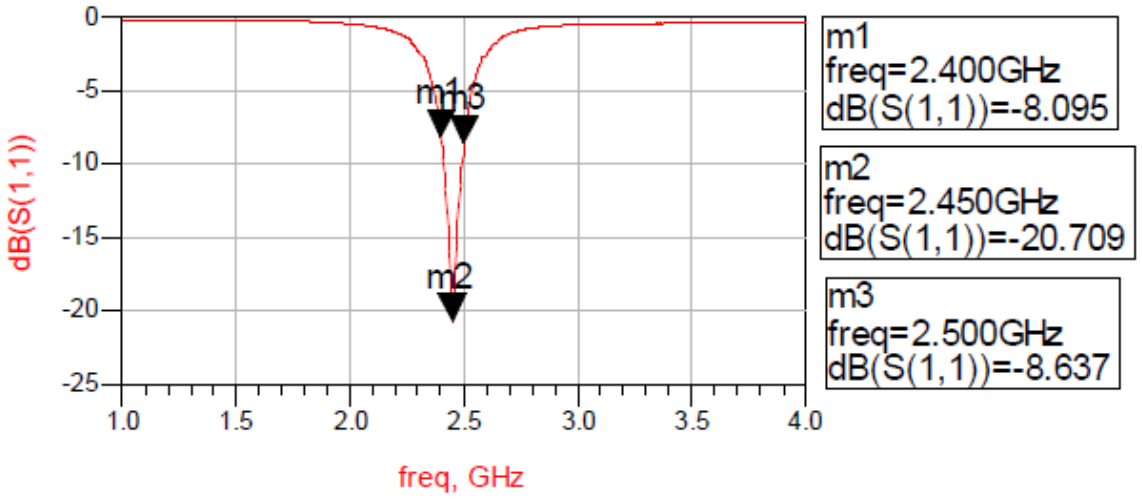
SW CTRL Func.	
Press	PL
Release	PH

R21,R22 Del. CHIP_EN WIRE TO Pdn.

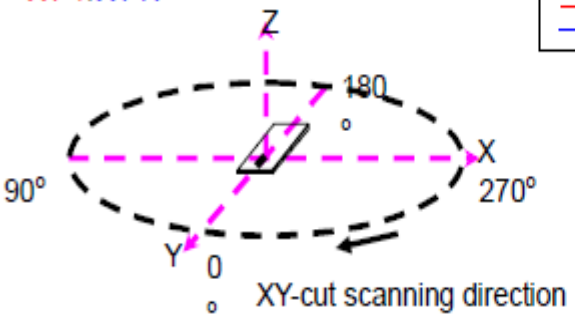
CHIP_EN PDn

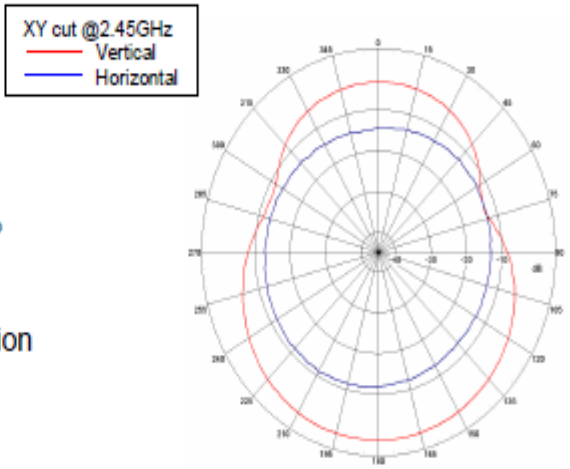
PDn/W_Disable#

10. Chip Antenna

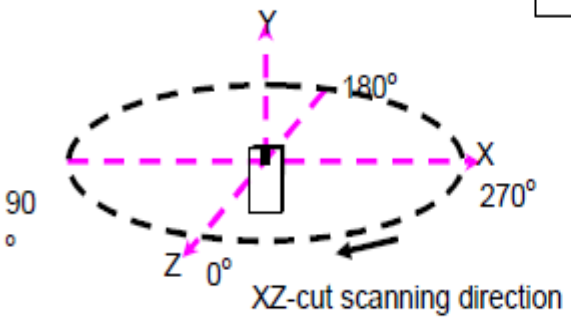


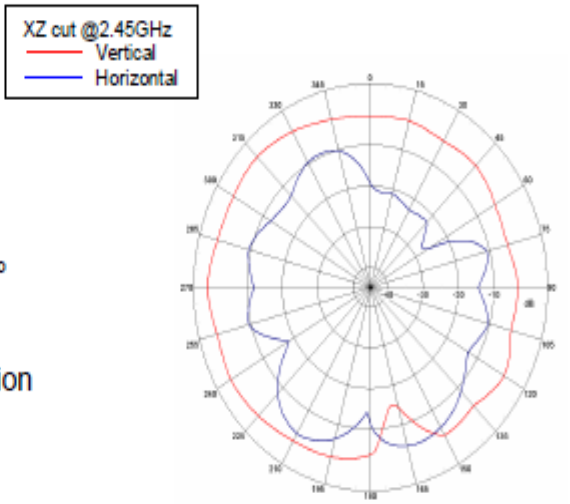
XY-V/XY-H



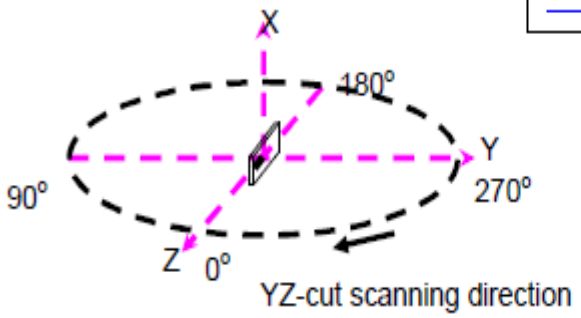


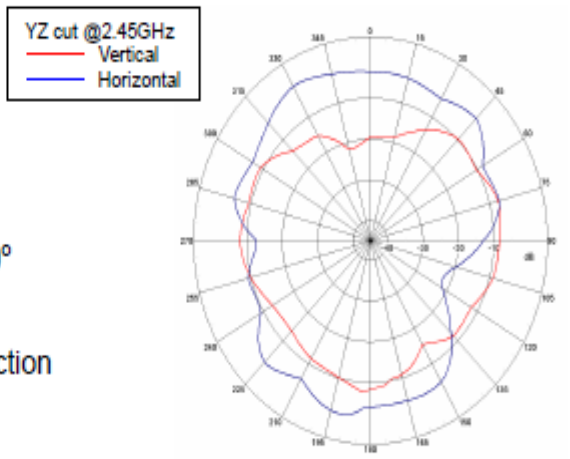
XZ-V/XZ-H





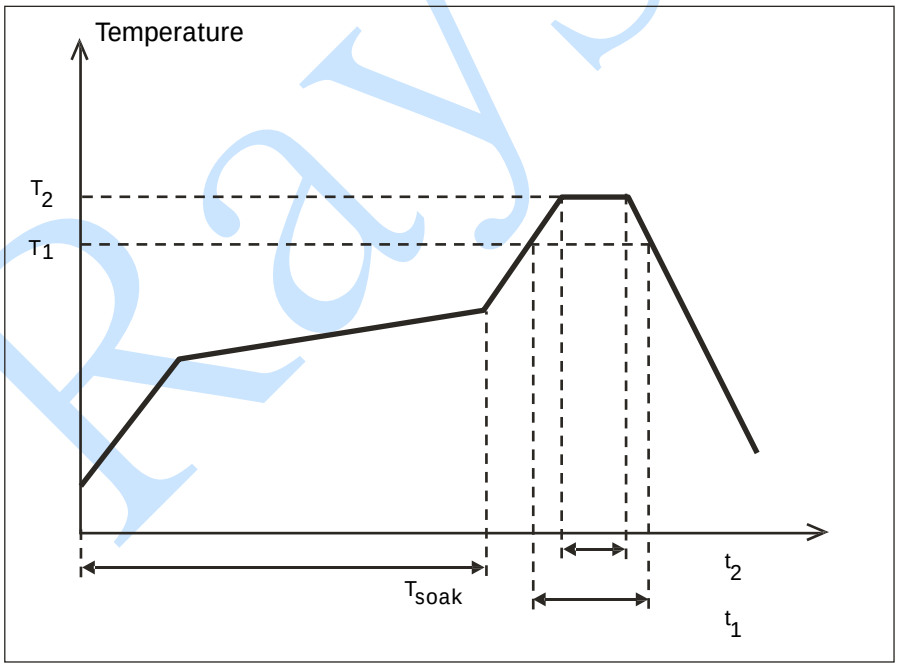
YZ-V/YZ-H





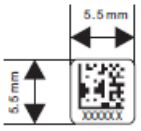
11. Furnace temperature curve

Reflow profile requirements		
Parameter Specification	**Reference**	**Specification**
Average temperature gradient in preheating		1~2.5°C/s to 175°C equilibrium.
Soak time	T_{soak}	120~180 seconds
Time above 217°C (T_1)	t_1	45~90 seconds
Peak temperature in reflow	T_2	250°C (–0/+5°C)
Time at peak temperature	t_2	6 seconds
Temperature gradient in cooling		6°C/second max.



12. Label and packaging design

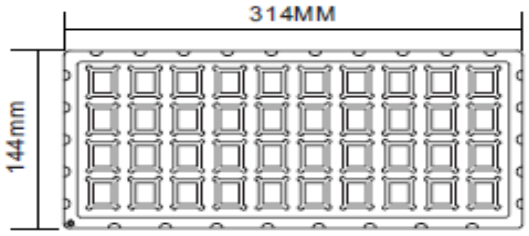
1:1



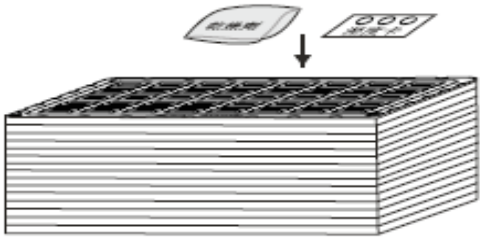
3:1

地址码6位(业务提供)

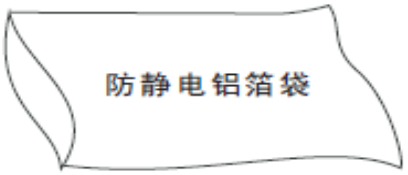
WMF-250 包装示意图



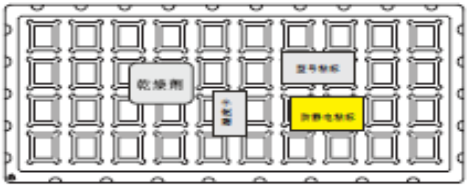
WMF-250 吸塑托盘, 可装40PCS
托盘尺寸: 314*144*7.5mm (L*W*H)



1托盘装40pcs,11托盘入内盒, 上面1PCS是空的。

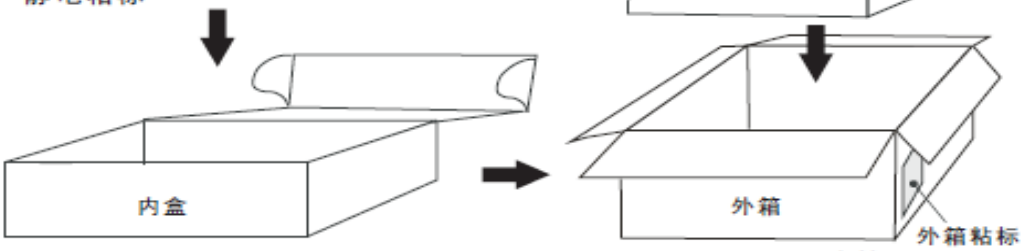


将装入模组的托盘放入防静电铝箔袋中抽真空。



防静电铝箔袋外贴型号粘标及防静电粘标

一盒装400PCS, 盒外正面加贴型号粘标, 4内盒入外箱



1600PCS/外箱
外箱粘标贴于外箱侧唛

End of Datasheet

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