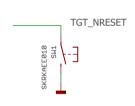
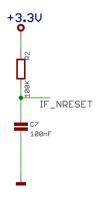
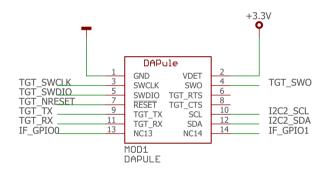
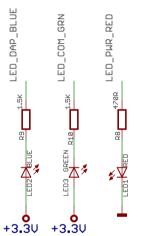
DAPLink Interface STM32F103CBT6 +3.3V C10 C11 100nF 100nF +3,30 +3.3V +3.3V VDD_2 IF_GPIO0 FCSTCE8M00G55-R0 IF_GPIO1 PC13 VSS_2 +3.3V IF_SWDIO JTMS/SWDIO PC14 PC15 PA12 OSCIN PA11 +3.3V IF_NRESET STM32F103CBT6 TGT_SWO LED_PWR_RED OSCOUT PA10 NRST PA9 29 28 27 VSSA VDDA PB15 PA0 PB14 TGT_SWCLK AIN12_1 PA1 PB13 TGT_SWDIO PA2 PB12 +3.3V I2C2_SCL I2C2_SDA .GT_SWCLK mbed-HDK DAPule-STM32F103-v1.0.0 12/02/2018 11:52 1/2











+3.3V

C3 C4 100nF 10uF

mbed-HDK

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