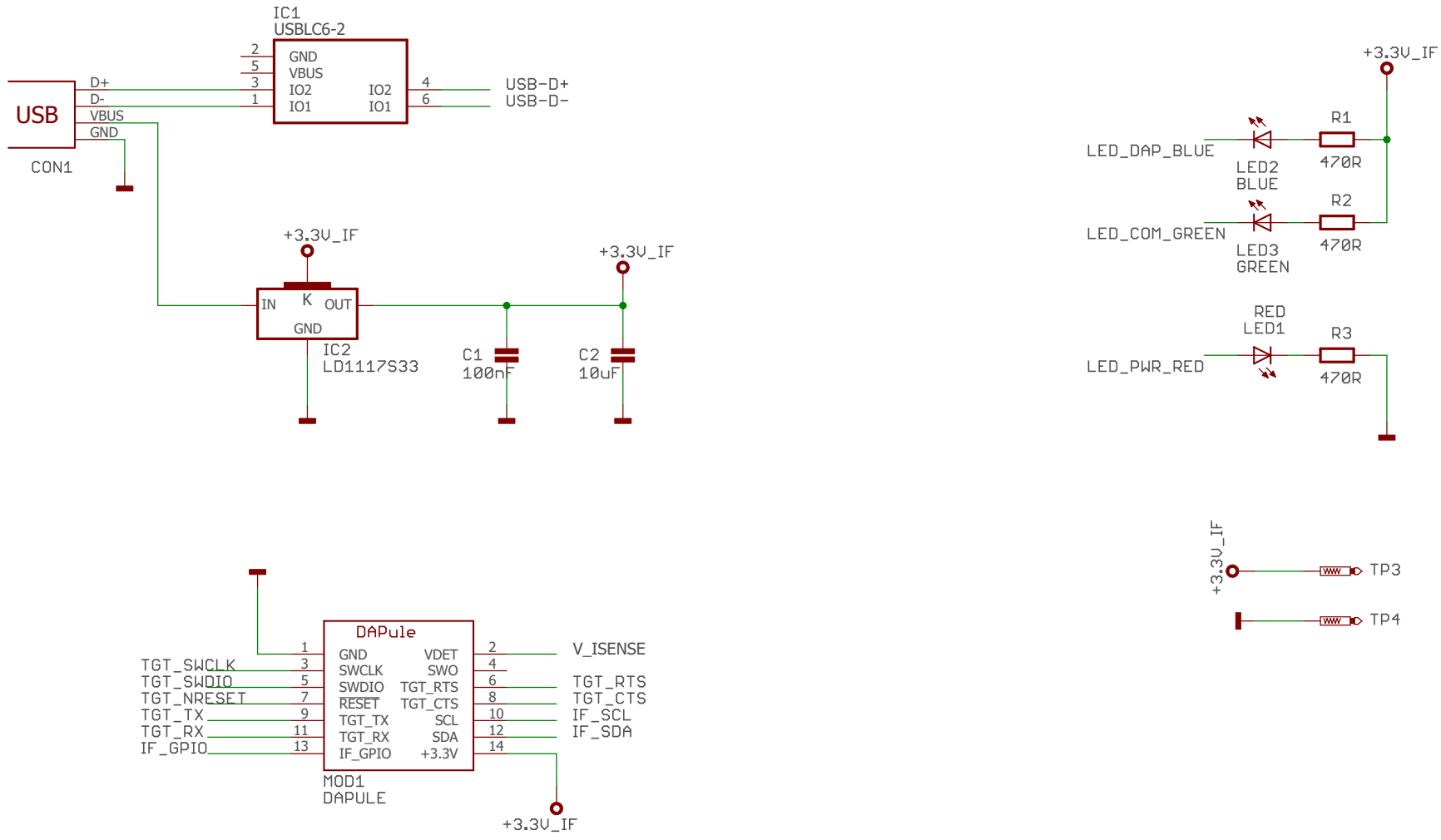

mbed-HDK

DAPule-LPC11U35-v1.1.0

not saved!

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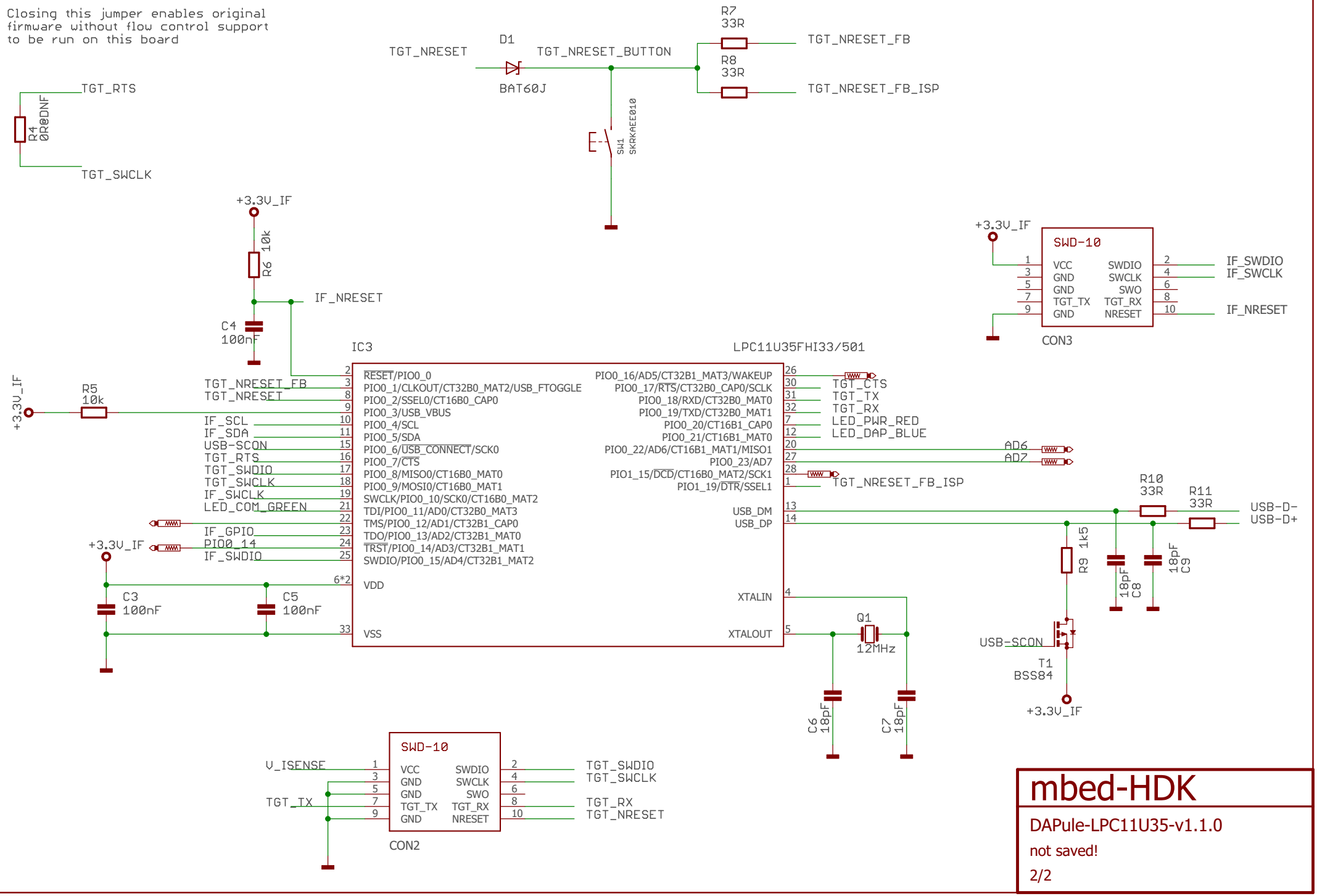
Closing this jumper enables original firmware without flow control support to be run on this board
TGT_RTS
R4 0R@DNF
TGT_SWCLK
TGT_NRESET
D1
BAT60J
TGT_NRESET_BUTTON
R7 33R
R8 33R
TGT_NRESET_FB
TGT_NRESET_FB_ISP
SW1 SKRKAEE010
+3.3V_IF
R6 10k
IF_NRESET
C4 100nF
IC3
LPC11U35FHI33/501
R5 10k
TGT_NRESET_FB
TGT_NRESET
IF_SCL
IF_SDA
USB-SCON
TGT_RTS
TGT_SWDIO
TGT_SWCLK
IF_SWCLK
LED_COM_GREEN
IF_GPIO
PIO0_14
IF_SWDIO
C3 100nF
C5 100nF
RESET/PIO0_0
PIO0_1/CLKOUT/CT32B0_MAT2/USB_FTOGGLE
PIO0_2/SSEL0/CT16B0_CAP0
PIO0_3/USB_VBUS
PIO0_4/SCL
PIO0_5/SDA
PIO0_6/USB_CONNECT/SCK0
PIO0_7/CTS
PIO0_8/MISO0/CT16B0_MAT0
PIO0_9/MOSI0/CT16B0_MAT1
SWCLK/PIO0_10/SCK0/CT16B0_MAT2
TDI/PIO0_11/AD0/CT32B0_MAT3
TMS/PIO0_12/AD1/CT32B1_CAP0
TDO/PIO0_13/AD2/CT32B1_MAT0
TRST/PIO0_14/AD3/CT32B1_MAT1
SWDIO/PIO0_15/AD4/CT32B1_MAT2
VDD
VSS
PIO0_16/AD5/CT32B1_MAT3/WAKEUP
PIO0_17/RTS/CT32B0_CAP0/SCLK
PIO0_18/RXD/CT32B0_MAT0
PIO0_19/TXD/CT32B0_MAT1
PIO0_20/CT16B1_CAP0
PIO0_21/CT16B1_MAT0
PIO0_22/AD6/CT16B1_MAT1/MISO1
PIO0_23/AD7
PIO1_15/DCD/CT16B0_MAT2/SCK1
PIO1_19/DTR/SSEL1
USB_DM
USB_DP
XTALIN
XTALOUT
TGT_CTS
TGT_TX
TGT_RX
LED_PWR_RED
LED_DAP_BLUE
AD6
AD7
TGT_NRESET_FB_ISP
R10 33R
R11 33R
USB-D-
USB-D+
R9 1k5
C8 18pF
C9 18pF
USB-SCON
T1 BSS84
Q1 12MHz
C6 18pF
C7 18pF
+3.3V_IF
SWD-10
VCC
GND
GND
TGT_TX
GND
SWDIO
SWCLK
SWO
TGT_RX
NRESET
IF_SWDIO
IF_SWCLK
IF_NRESET
CON3
V_ISENSE
TGT_TX
TGT_SWDIO
TGT_SWCLK
TGT_RX
TGT_NRESET
CON2
mbed-HDK
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not saved!
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