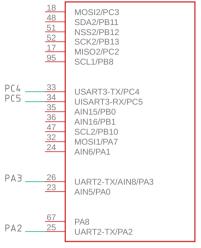
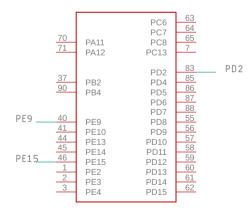
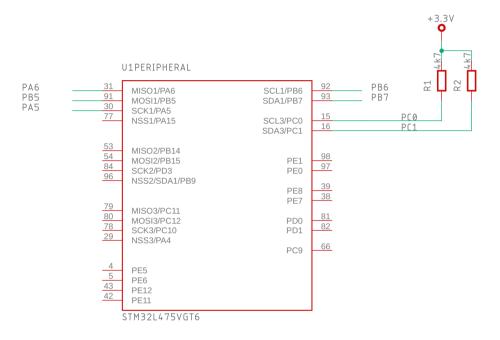
#### MCU GPIO

#### U1ETH



STM32L475VGT6

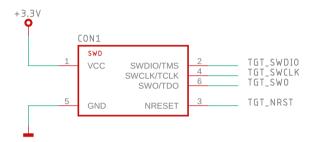


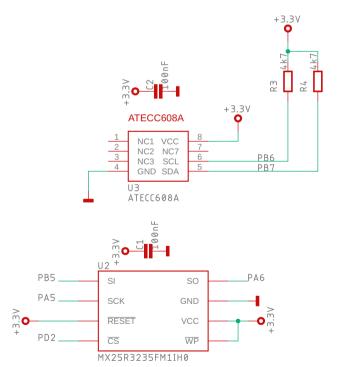


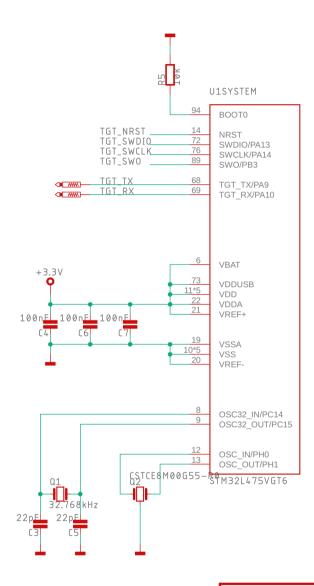
#### mbed-HDK

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# MCU Power, Clock and Peripherals





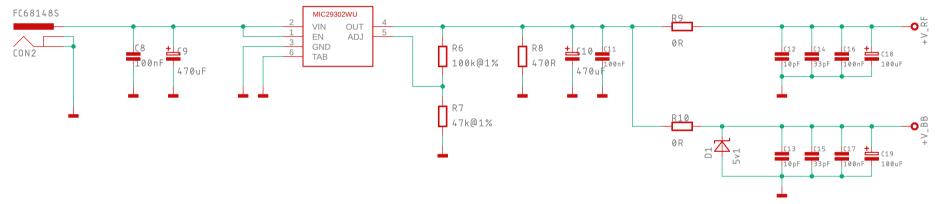


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# Modem Power supply

Place Decoupling caps for V\_RF and V\_BB close to the modem, and make sure both nets are tracked as short and wide as paractical

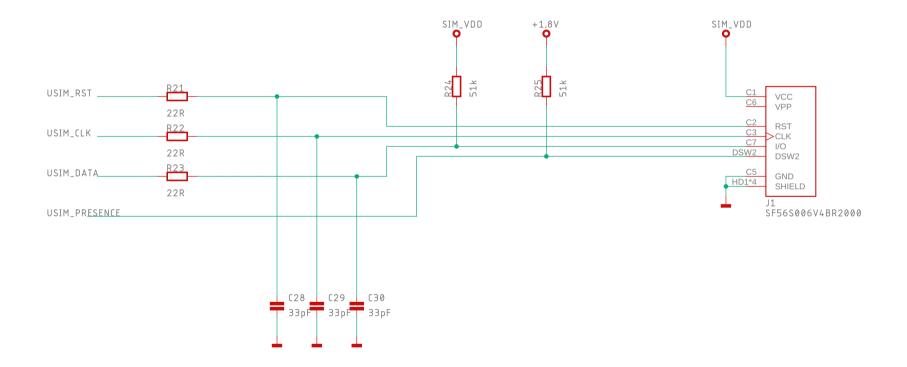


# mbed-HDK

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#### Modem subsystem +1.8V +1.8V +3.3V +1.8V +3.3V 100nF VCCA VCCB MCU\_TX\_1V8 \_\_ MCU\_RX\_1V8 \_\_ DBG\_TXD\_1V8 DBG\_RXD\_1V8 PA2 PA3 PC4 PC5 В1 A2 B2 A3 A4 ВЗ В4 NETLIGHT A5 В5 CON3 Α6 В6 +V\_BB +V\_BB В7 SMA-142-0701-801/806 0R +V\_BB **=** C23 GND STATUS DNF DNF U4 TXS0108EPWR +1.8V +V\_RF SIM\_VDD MOD1 VDD EXT CON4 32\*2 VBAT\_BB VBAT\_RF USB VBUS SMA-142-0701-801/806 0R 43 USIM\_VDD **=** C24 18 19 W\_DISABLE# AP\_READY STATUS 21 DNF DNF **NETLIGHT** 4k7 30 37 ANT MAIN DTR 49 ANT\_GNSS RTS MCU\_TX\_1V8 RXD PE15 MCU\_RX\_1V8 ADC0 TXD ADC1 PWRKEY USB\_BOOT DCD PWRKEY CTS RESET/N NRST 12S\_D0 UART3 RXD/SPI MISO 12S D1 UART3\_TXD/SPI\_MOSI I2S\_BCLK< >GPIO/SPI CLK I2S\_MCLK < I2S\_WCLK DBG\_RXD\_1V8 DBG\_TXD\_1V8 PE9 9 10 DBG\_TXD USB DP USB\_DM >I2C SCL I2C SDA PSM\_IND USIM\_PRESENCE USIM PRESENCE RESERVED 44 USIM\_RST USIM RST 46 64 USIM\_CLK USIM CLK **GPIO** USIM\_DATA USIM DATA mbed-HDK USIM\_GND 3\*28 GND BG96 RD2166-v1.0.0 26/02/2019 19:53

#### SIM Card Socket



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