

SHEET NO. 1

**Answer Book for Main Examination**

Pl. Note: No Provision of Supplementary Answer Book

(to be filled by the candidate)  
[Write only the desired information.  
Any other information provided on the  
sheet will be treated as unfair means]

Year/Semester: 2022-23 / 3 (III)

Branch: I.T.

Subject/Paper: E.D.C.

Subject/Paper Code:

3 E 1 6 5 1

Day & Date of Examination:

Tuesday 12/09/23



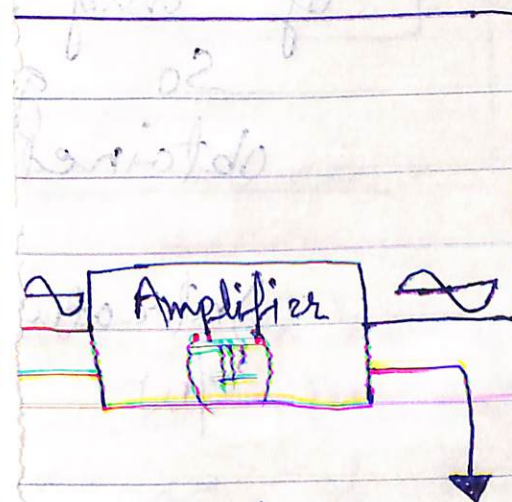
Facsimile Seal of Controller/Director of Exam



**Evaluation Sheet**

Q. No.	Marks obtained					Total
	a	b	c	d	e	
1						
2						
3						
4						
5	6	5				11
6						
7						
8						
Grand Total (in figure)						
Grand Total (in words)						

is frequency  
stage is  
stage R-C coupled  
back network.  
shown below: →



Network  
parallel  
fed to the input



(Last Page)

## [Unit - IV]

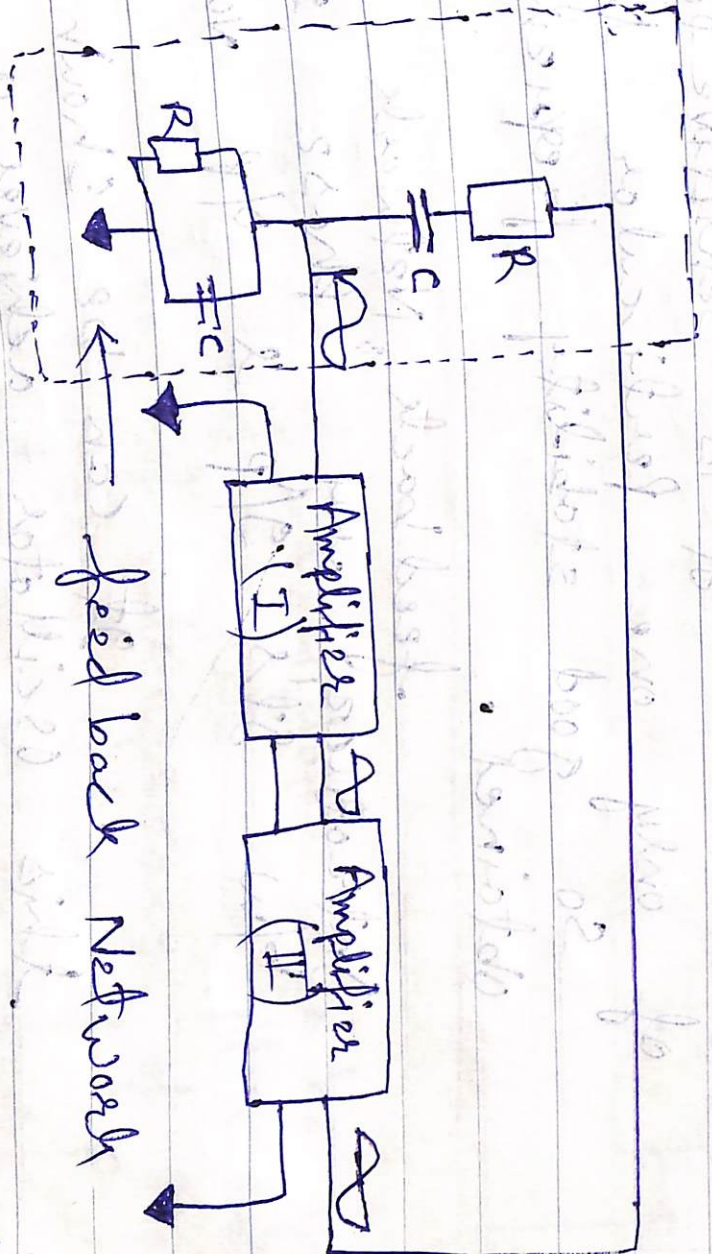
5. (a) Wein bridge Oscillator  $\rightarrow$

This is an Audio frequency

R-C Oscillator. It's Advantage is frequency may be two stage R-C coupled

Amplified and a feedback network.

The block diagram is shown below:



The voltage across the parallel combination of  $R$  and  $C$  is fed to the input



[X-1011]

(Sg 8/10/14)

of Amplifier (I). The Net Phase shift to two Amplifier is zero. Amplifier (II) will Amplify signal over a wide range of frequency & direct coupling would result in poor frequency stability by Adding Wein bridge Oscillator Network. It is sensitive to signal of only one particular frequency. So good stability frequency is obtained.

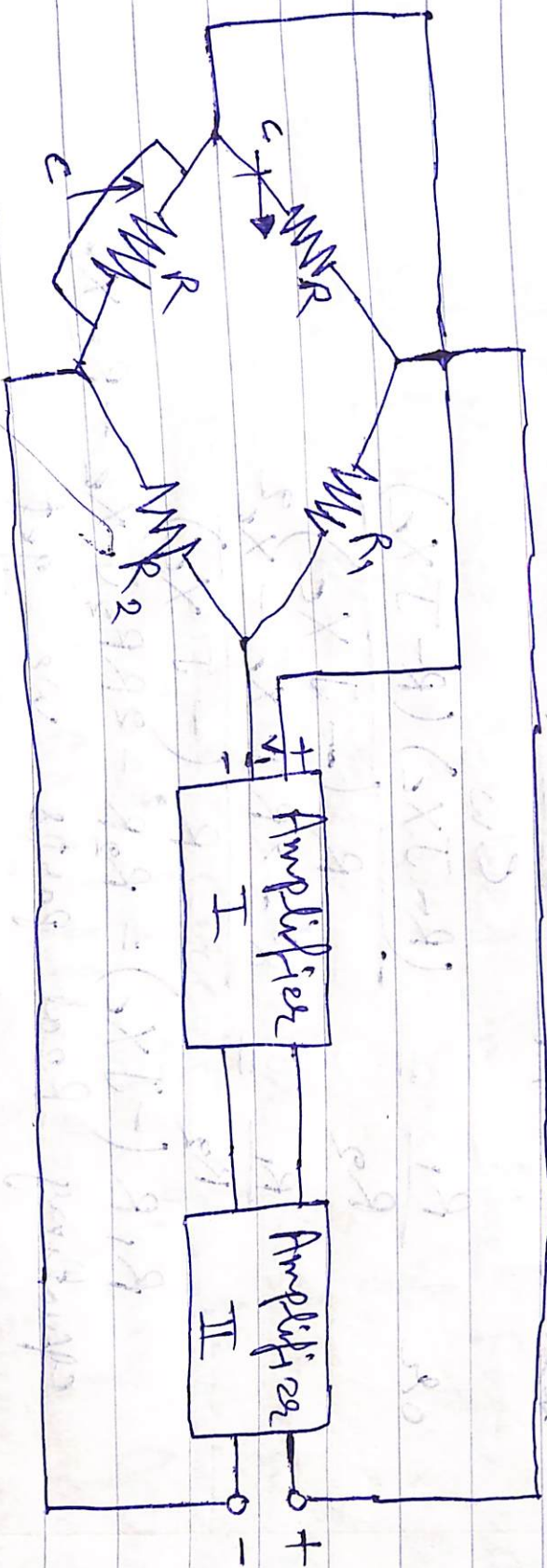
feedback Network should not introduce any phase shift b/w its input & output voltage.

It can be shown that this oscillator achieves a frequency.

$$f = \frac{1}{2\pi RC}$$



obtained by two resistors  $R_1$  &  $R_2$   
in feedback Network. Shown below



frequency of Oscillator can be varied  
by two capacitor. We can also change  
by the range of frequency of Oscillator  
by using different values of  $R$ .  
frequency of oscillation  $\Rightarrow$   
figure in balanced Network



$$\frac{R_1}{R_2} = \frac{R - jX_c}{R(-jX_c) / (R - jX_c)}$$

where  $X_c = \frac{1}{C\omega}$

$$\text{or } \frac{R_1}{R_2} = \frac{(R - jX_c)(R - jX_c)}{R(-jX_c)}$$

$$\frac{R_1}{R_2} = \frac{R^2 - 2RjX_c - X_c^2}{R(-jX_c)}$$

$$R_1 R(-jX_c) = R_2 R^2 - 2R R_2 j X_c - R_2 X_c^2$$

equating Real parts we get

$$R_2 R^2 = R_2 X_c^2 \quad \text{or } X_c = R$$

$$\frac{1}{C\omega} = R \quad \text{or } \omega = \frac{1}{CR}$$

$$F = \frac{1}{2\pi CR}$$

where  $F$  is frequency of oscillation



Ques Sol. (b) Schmitt Trigger using BJT →

This circuit is called Schmitt trigger whenever free base terminal is required. due to the fact that the base of transistor is not involved in the regenerative switching & the base of transistor  $Q_1$  is thus free.

The collector resistance  $R_c$  in the output circuit of transistor  $Q_2$  is not required for the binary operation so this resistance may be

Suitably chosen so as to achieve any desired amplification of output voltage

As a squaring circuit it can convert input of any wave shape or output pulses of rectangular or square wave shape

2. Amplifier - Comparator or level detector in Schmitt trigger

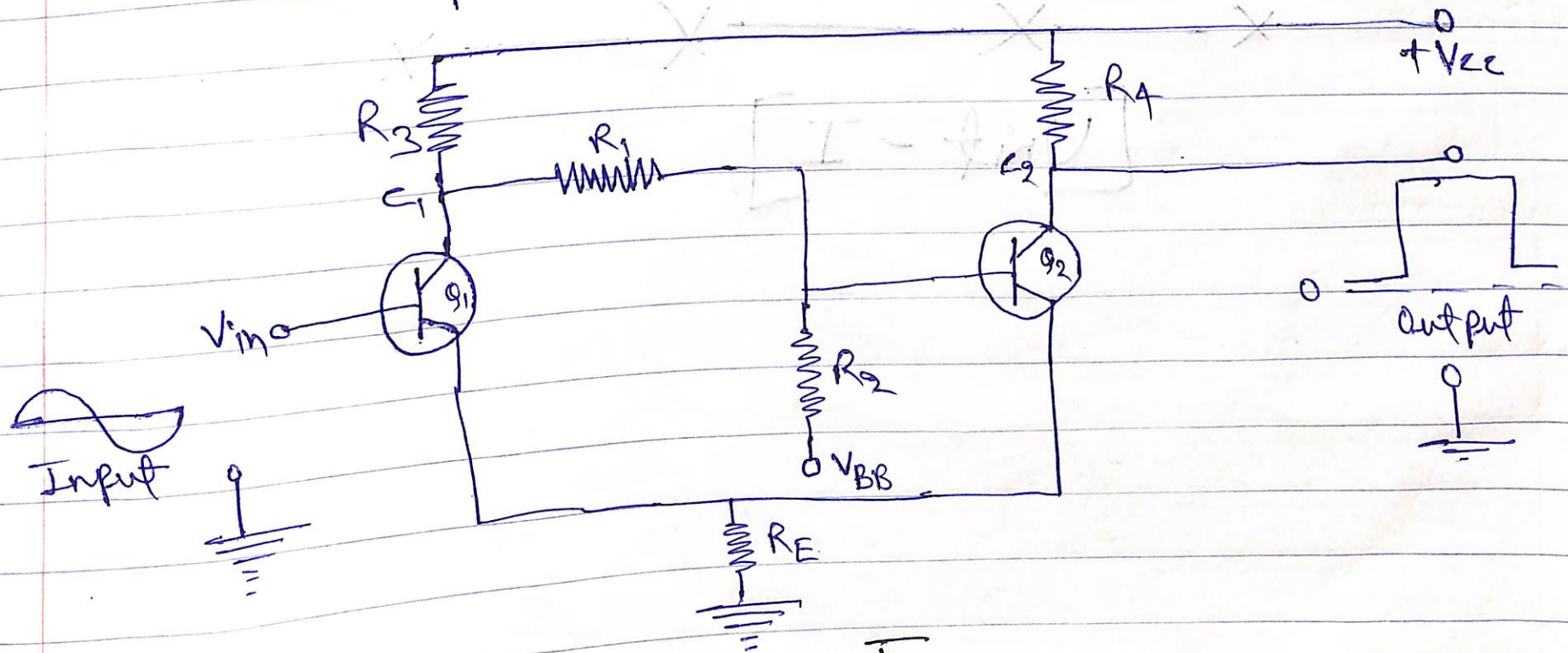


- Circuit a change of state occur
3. AS a flip flop circuit
  4. Reshaping the worn out pulses by giving them sharp leading and trailing edges.

Working  $\rightarrow$  when power supply is switch on for first time, Resistor  $R_1, R_3, R_2$  forming a potential divider across  $V_{cc}$  and  $-V_{BB}$  forward biases slightly the transistor  $Q_1$  and  $Q_2$  start conducting.  ~~$R_2$  from~~  
 $Q_1$  is now Reverse biased due to flow of current in emitter Resistor  $R_E$  from transistor  $Q_2$ .  
Initial static Condition  
transistor  $Q_1$  is in cut-off &  
transistor  $Q_2$  is in Saturation



Input ac signal is Applied to base of  $Q_1$  & If it has strength to overcome a reverse bias placed on  $Q_1$  due to voltage drop across emitter resistor  $R_E$ ,  $Q_1$  is forward biased.  $Q_1$  is start conducting, collector  $C_1$  is potential drop. Negative going coupled to base of  $Q_2$  via  $R_1$ . Reduce forward bias and consequences emitter current.



Figure

Figure



Thus one cycle is completed. This cycle is repeated as input ac signal Repeat its cycle.

positive going pulses are generated at the output of Schmitt trigger for each cycle of the Schmitt trigger.

