

# IONOS SIM HF/VHF-UHF Channel Simulator

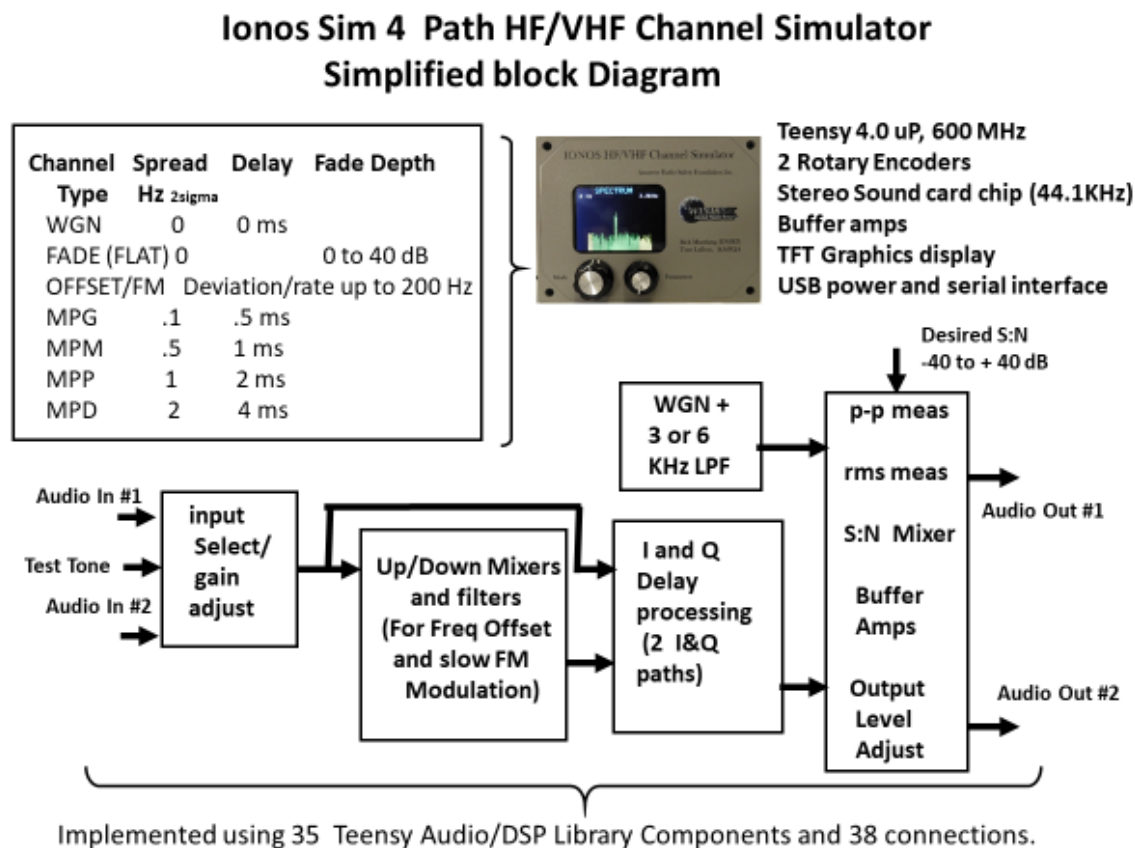
(with experimental BUSY DETECT Busy Channel Detection)

## [Instructions, Basic Documentation, Specifications and Open Source Info]

Ref: Software Release revision 1.01 Rick Muething, KN6KB Oct 17, 2020

**1.0 Introduction:** The Teensy **IONOS SIM** is an audio processing 1-2 path (ray) simulator that models standard HF and VHF/UHF propagation channels. It is intended primarily for modem and protocol design, analysis, optimization and comparison. It allows *non-radio* testing of protocols and modems using statistically standardized channels that would be almost impossible to achieve with normal “over the air” RF testing. The simulator is based on the well-documented Watterson model [ 1] used by many laboratory grade instruments costing many thousands of dollars.

**2.0 Basic Description:** The **IONOS SIM** Simulator is based on the low-cost but powerful Teensy 4.0 processor (<https://www.pjrc.com/store/teensy40.html>) and was developed using the Arduino platform and makes extensive use of the Teensy Audio DSP library <https://www.pjrc.com/teensy/gui/index.html> . The IONOS SIM software is released under the MIT Open Source License (see appendix). Simplified and detailed block diagrams of the simulator are shown below in Fig 1 and Fig 2. The implementation uses 35 Teensy Audio Library “components” with 38 virtual “connections”.



**Fig 1. Teensy IONOS HF/VHF Channel Simulator simplified block diagram.**

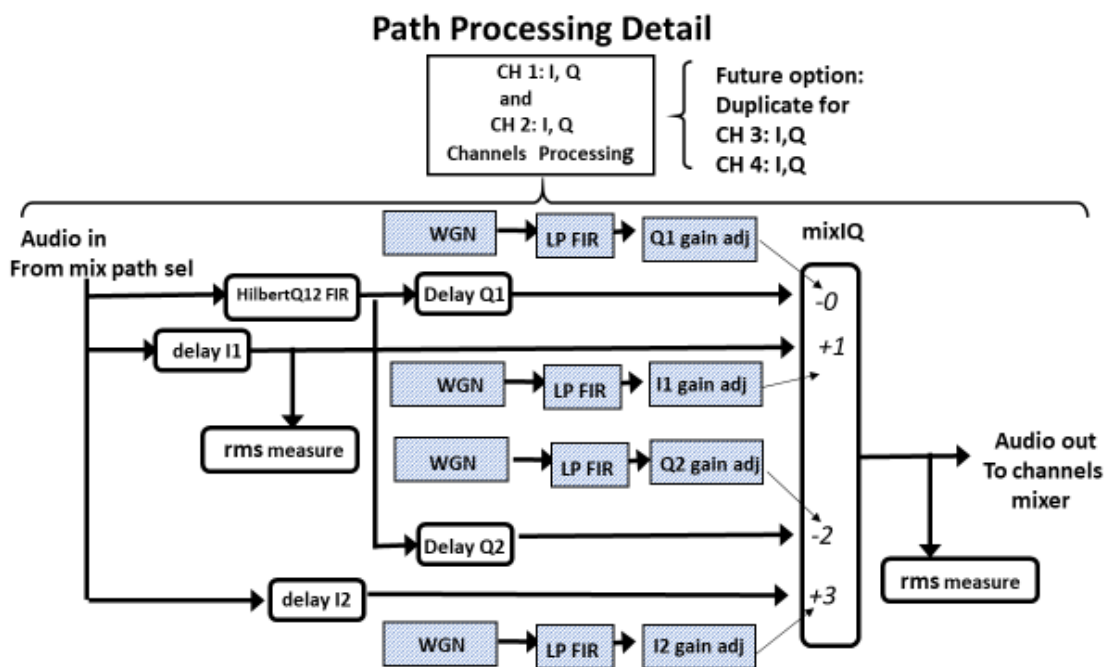
[1] Watterson, C.C., J.R. Juroshek, & W.D. Bensema. 1970 Experimental confirmation of an HF channel model IEEE Transaction of Communication. Technology. Vol COM-18. Pp 792-803 Dec 1970

### Detailed Block Diagram

The diagram illustrates the audio processing system architecture. Key components and their interconnections are as follows:

- Inputs:** Audio In #1 (mono) LINEINL, Test Tone, and Audio In #2 (mono) LINEINR feed into the **input** block.
- Processing Path:**
  - The **input** block feeds into the **UP Mixer 7.7 KHz LO**.
  - The output of the UP Mixer goes to the **FIR HP 7.9-22 KHz** filter.
  - The output of the FIR HP filter goes to the **multiply** block.
  - The **multiply** block also receives input from the **sine** block (which is controlled by offset, VLF drift, and freq dev).
  - The output of the **multiply** block goes to the **LP FIR** filter.
  - The output of the LP FIR filter goes to the **mix path sel** block.
  - The **mix path sel** block outputs to the **Path Processing I1,Q1 I2,Q2** block.
  - The output of the Path Processing block goes to the **mixChannels** block.
  - The **mixChannels** block also receives input from the **LP FIR filter** (via a '0' signal) and the **Commands** block (via a '1' signal).
  - The output of the **mixChannels** block goes to the **amp Left** and **amp Right** blocks.
  - The output of the **amp Right** block goes to the **p-p measure** block (for testing and calibration).
  - The output of the **amp Left** and **amp Right** blocks goes to the **Simulated Channels Audio Out**.
- Control and Measurement:**
  - The **Commands** block (Control Panel or USB Serial) controls the **Set Ch Type,S:N** block, which provides mixer gains to the **mixChannels** block.
  - The **Set Ch Type,S:N** block also receives input from the **rms measure** block.
  - The **rms measure** block receives input from the **LP FIR filter** and the **mixChannels** block.
  - The **p-p measure** block receives input from the **amp Right** block and the **mixChannels** block.
  - The **mix path sel** block outputs to the **~7700 Hz L.O. Down mixer**.
- Legend:**
  - Collection of Audio Components:** Represented by white boxes.
  - Teesny Library Audio Component:** Represented by light blue boxes.
  - Audio:** Represented by a thick black arrow.
  - Control:** Represented by a thin black arrow.

**Fig 2. Teensy IONOS HF/VHF Channel Simulator detailed block diagram.**



1. Normal audio processing is done at 44.1KHz sample rate, 16 bit audio.
2. C++ Functions in crosshatched boxes are updated at 64 times the selected path Doppler spread frequency
3. Delay tap values I1,Q1,I2,Q2 are constant for each modeled Path type.
4. mixIQ gain signs extract only real parts to send to channels mixer. (see following slide)

**Fig 3. I & Q Path Processing Detail of the IONOS Sim**

**3.0 Basic Layout:** The Basic layout of the simulator circuit board is shown in Figs 4 & 5. The simplified user interface consists of just 2 rotary encoders (each with push action switch) and a small color graphics TFT display mounted in an ~ 4" x 6" customized PolyCase enclosure. Additional surface mount components are on the rear side of the PCB. The simulator is powered through its micro USB connector which also provides optional USB serial I/O for computer- controlled operation if desired. Audio input (2 mono channels) and audio output (2 mono channels) connections are made using standard 1/8" (3.5 mm) stereo phone jacks.



**Fig 4. Basic control/display layout (Rev 2 prototype) Schematic, PCB design, layout assembly and test by Tom Lafleur KA6IQA**

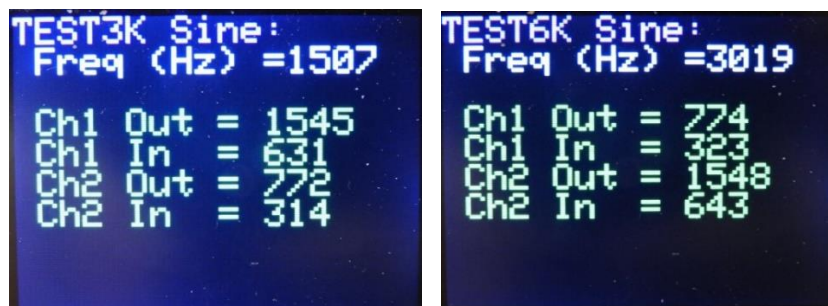


**Fig 5. Production Front Panel and side connections**

**3.1 Automated Self-Test.** There is a built-in mechanism for automated self-test and calibration verification. First connect the output jack to the input jack with a standard Stereo 1/8" (3.5 mm) 3 conductor male patch cord. This jumps the CH1 and CH2 output amplifiers to the CH1 and CH2 input buffers. The self-test software will block the normal in-to-out connection to avoid oscillations and accurately measure the levels of the precision internally generated test signal and the injection of the outputs for that signal back into the inputs. This verifies:

- a) Connections and wiring to the output and input jacks,
- b) proper amplifier resistor values/tolerance,
- c) proper low frequency coupling capacitors,
- d) Teensy Audio processing and DSP FIR filtering,
- e) Frequency response on both 3KHz and 6KHz bandwidths (Flat +/- 1dB). The self-test is also useful in setting up initial modem output levels.

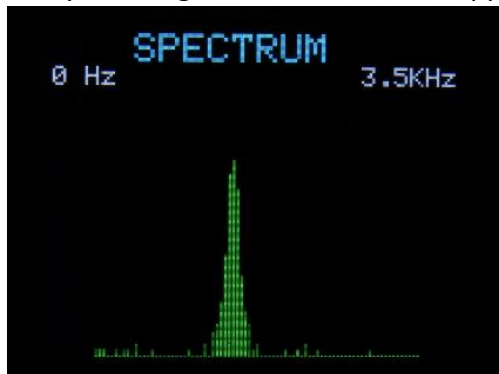
**3.1.1 Running the Self-Test.** With the patch jumper above in place, power on the IONOS Simulator by plugging in the USB port. The software and self-test software are already loaded. After the power on display of the current code revision (e.g. IONOS SIM 1.01 etc.) and serial number the display will enter the normal power on mode (WGN mode with S:N = 40 dB). Rotate the Mode (left hand encoder) knob 17 clicks CW to activate the TEST3K Sine: self-test. This will set up a 1500 Hz test sine wave at a specific amplitude and begin averaging the p-p (peak to peak) voltage readings at the input buffers (Ch1 in, Ch2 in) and output Amps (Ch1 Out, Ch2 Out). Averaging is used to increase resolution and after about 5 seconds the average measurements should stabilize to stable values (all values in p-p mv). If the values displayed are all green text, the measurements are within +/- 1 dB of the specification ...the basic accuracy of the simulator. Any values in red text are outside the +/- 1 dB limit above. Different levels for Ch1 and Ch2 are used to verify there is no cross-coupling. The entire range of frequencies for the 3 KHz bandwidth can be tested by rotating the right hand (parameter) knob. Frequency range for the TEST3K will range from 300 Hz to 3300 Hz. During the test you can toggle to the Spectrum display by pushing down on the Mode (left hand) encoder. Pushing again will toggle back to the TEST3K display. In a similar way rotating the mode knob CW one additional click (18 clicks from Power on WGN position) will enable the TEST6K test. It works exactly the same as the TEST3K but the frequency range spans 300 Hz to 6300 Hz. It exercises different FIR filters used in the 6 KHz bandwidth mode. Again, you may have to wait a few seconds for reading averages to stabilize. The 6 KHz mode swaps the Ch1/Ch2 amplitudes from the 3 KHz test insuring all paths are independent and work at full amplitude.



**Fig 6. Example Screens for TEST3K an TEST6K self-test and calibration verification.**

Green text indicates parameter within +/- 1 dB and values can vary from those shown above.

**3.2 Getting Familiar with the controls and displays of IONOS SIM.** You can gain familiarity with the simulator without hooking it up to any modems or TNCs. An earphone (mono or stereo) plugged into the Ch1,2 output jack will be helpful. Upon power-up after plugging in to a +5 v powered micro USB port the TFT display will Activate and show IONOS SIM version xxxxx for four seconds along with an 8 digit hex serial number before going to the **Basic Mode** display. There it will show the current mode “WGN:” in blue text and the current parameter value for that mode which will be “S:N= 40 dB” in green text. Also shown in the WGN mode is the internal signal level (mv p-p) and the Crest Factor (CR) of the input. WGN (White Gaussian Noise) is the most basic mode and one that will be used often for setup and initial testing runs. Rotate the right hand encoder (parameter) CW or CCW to roll through the S:N options (-40 to +40 dB). The display of mode settings/parameters and text colors are similar for the 14 other modes shown in Fig 8. (The self-test modes TEST3K and TEST6K are shown in magenta to distinguish them from normal operating modes) Most modes and settings can be left at their power on defaults for basic testing. For modes WGN-MPD pushing down on the right encoder until it clicks will toggle the internal 1500 Hz test tone on/off. You should hear the tone in the earphone and see the simulator’s internal p-p level in mv shown on the screen. Adjusting the S:N lower should increase the noise. Most people can still hear the signal down to about -10 to -15 dB below the noise (all dB measurements and calculation are based on the operational bandwidth (3000/6000 Hz). In other modes pushing down on the right encoder will save the current mode parameter to EEPROM as the power on default value. For modes WGN-MPD pushing on the left encoder knob should switch the display to the **Spectrum** mode (3.5 or 6.5 KHz) . There you should see the test tone and the background noise (white noise filtered to bandwidth) displayed. The vertical plotting scale of the spectrum analyzer is logarithmic and covers approx. 60 dB dynamic range. Each spectrum line (2 pixels wide for 0



to 3500 Hz) represents one ~43 Hz frequency bin. Toggle off the spectrum display with another push of the mode knob) to return to the **Basic Mode** display. Now select a multipath mode (say MPD for Multi Path Disturbed). Push the Mode (left) encoder once to switch back to the spectrum display. There you will see the impact the multipath delays created by the simulator has on the 1500 Hz test tone. The amplitude fluctuations will be obvious but the small frequency deviations will not be visible on the spectrum display. MPD is a very poor channel and HF digital communications on this path are often difficult or impossible.

Pushing the mode (left) encoder once more should return you to the **Basic Mode** display. For most parameters (except S:N on WGN-MPD) you can push the parameter (right) knob to set the displayed value as the power on default (stored in EEPROM). You can explore the various other mode and parameter settings. There are no settings that will damage the Simulator but care should be taken when advancing the output volumes (CH1, CH2 OUT) levels which could result in loud levels to some earphones.



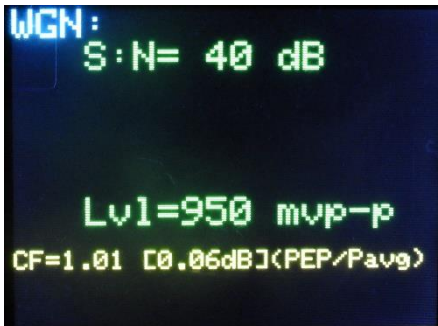
**3.3 Exploring the other modes and parameters for other testing.** While the Multipath channels are primarily used in HF Path simulation the simulator also supports functions useful in VHF/UHF path simulation. This testing is usually done using the standard WGN channel (across a range of S: N range of interest) modified with functions for VHF/UHF simulation. The primary ones of these are **FLAT fade** caused by multipath signal reception on FM and **FM Deviation** caused by transmitter/receiver drift or doppler shift. Figure 9 shows these modes and the parameter range of each. Again the 1500 Hz test tone can be used as a basic signal source and the Spectrum analyzer display to observe these effects just as was done in the basic HF modes. The fixed **OFFSET** mode can also be used to supply a small (+/- 200 Hz range) frequency offset to the audio input and this is helpful in testing/evaluating the ability of the modem to handle frequency tuning offsets



primarily on HF SSB transmission. These additional modes and parameters can be used with (applied to) the normal WGN channel. Note: OFFSET and FM Deviation cannot be used with multipath modes (MPG, MPM, MPP, MPD) due to the required precision necessary for proper multipath generation. If OFFSET or FM Deviation is enabled a yellow warning message “no FM or Offset!” will be displayed on the Multipath modes and the offset and/or FM Deviation will be ignored.

The simulator also supports a 6 KHz bandwidth mode. This can sometimes be useful for modeling wide band audio channels but is not normally used on amateur HF transmissions. The 6 KHz mode can be used in modeling wider band audio channels used on VHF and UHF such as 9600 baud packet or VARA VHF. In 6 KHz bandwidth mode the simulator’s internal FIR filters are all switched to a 6300 Hz range and the Spectrum display is modified to cover the full range 0 to 6500 Hz. In the 6500 Hz spectrum mode each ~43 Hz bin is represented by a one-pixel-wide vertical rectangle spaced by 1 pixel of black.

**3.4 Internal Signal Levels and Crest Factor.** The simulator also provides a continuous display of its peak internal signal level (mv p-p) in display modes WGN through MPD. This is designed to aid in setting Simulator output levels (CH1, CH2 OUT) and Simulator Input gain (CH1, CH2 IN) to appropriate operating levels. The full useable internal signal range of the simulator is 0 to 2000 mv p-p but normally signal levels should target an internal range of 200 to 1200 mv p-p (“Lvl=” reading) to accommodate fading and peaking in multipath modeling. The p-p and rms levels are measured at the output of the Input Low Pass filter and will display green when between 200-1200 mv p-p. There is also an internal level monitor at the output of the mixIQ mixer and any level of the mixIQ mixer > 1800 mv p-p will also trip the level monitor to red. This *could* be possible in some multipath cases and if observed the input level to the simulator or the CH1/CH2 IN gains should be reduced. The important goal is to not allow the internal peak audio level to get above 1200 mv to eliminate any chance of audio signal clipping internal to the simulator. Also, on all channel types the Crest Factor (CF) of

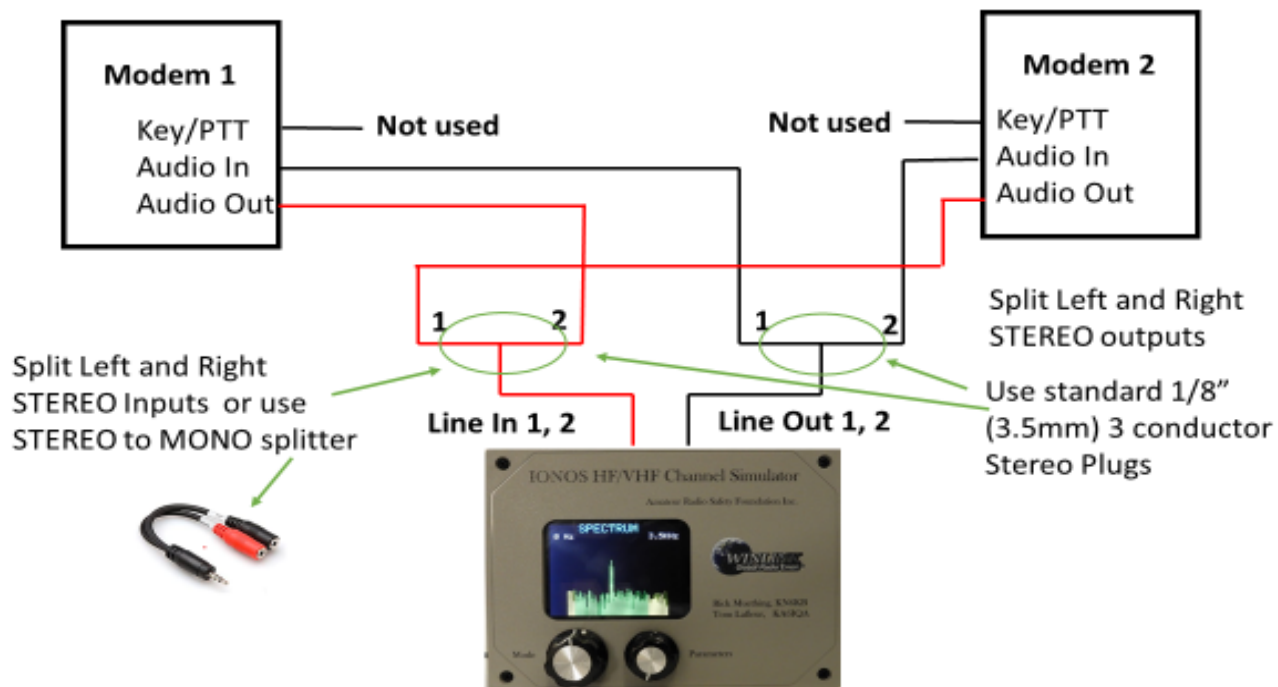


the input waveform is computed and displayed every 200 ms if the Lvl is > 50 mv. To follow RF power metrology conventions the CF indicates the ratio of PEP (Peak Envelope Power) to Average Power. For reference a pure sine wave has an RF CF = 1.00 or 0 dB. The crest factor is useful when computing the energy per bit ( $E_b$ ) in digital communications. Many SSB transmitters may not be able to sustain a CF below 2.0 (3dB) at max rated PEP for long periods (high duty cycles) due to thermal limitations so would require a reduced drive level to a lower PEP value.

**3.5 Adjusting for Protocol Bandwidth.** The simulator supports two bandwidth options 3 KHz and 6 KHz and adjust the S:N ratio based on the requested S:N setting and these bandwidths. This can work fine for comparing protocols of similar bandwidth but will result in some S:N calibration offset if the protocols are not 3KHz or 6KHz bandwidth. E.g. A VARA HF mode of 500 Hz bandwidth is actually operating at a S:N ratio of  $10 * \log(3000/500)$  or ~7.8dB higher than the simulator setting for S:N used on the simulator set for 3000Hz bandwidth. If protocols of different bandwidths are plotted on the same graph their plotted S:N ratio should be adjusted for the actual bandwidth of the protocol. If the bandwidths are reasonably close (within 10-15%) the correction needed would be small (<.6 dB). This kind of bandwidth S:N adjustment is important when comparing very low bandwidth modes (PSK31, JT modes etc.) against other wider modes .

**4.0 Basic Setup and Connections for using the Simulator.** The simulator can be used to model the paths for non ARQ (one direction) or ARQ half-duplex or full duplex connections. With its' internal audio CH1 and CH2 mixing a *single IONOS SIM* can handle *both* directions of a half-duplex ARQ connection providing symmetric channel modeling (same channel model for Modem 1 to Modem 2 and Modem 2 to Modem 1). Figs. 6 and 7 show the connections for both half duplex and full duplex channel modeling.

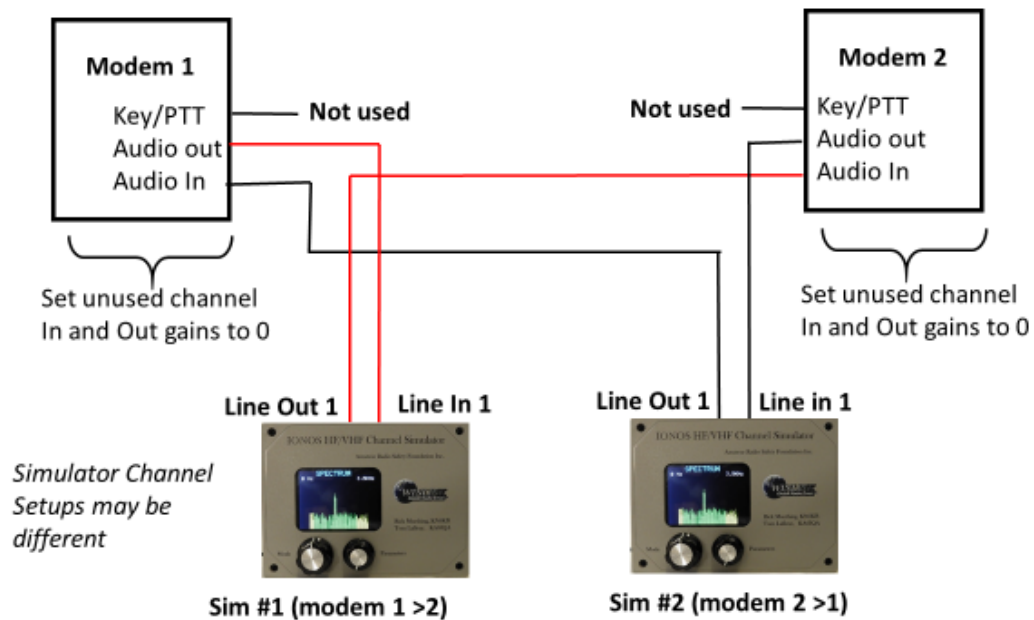
## Connecting the simulator: 1 Simulator Half Duplex, Channels will be symmetric



**Fig 7. Basic Half Duplex, symmetric channel setup with one IONOS SIM.**

(note: Stereo isolation transformers can be used if ground loops affect audio quality)

## Connecting the simulator: 2 Simulators Full Duplex, non symmetric channels:



**Fig8. Full Duplex, non-symmetric channel setup using two IONOS SIMs**

(note: Stereo isolation transformers can be used if ground loops affect audio quality)

**4.1 Audio Levels.** For proper operation the simulator must supply non-saturated, undistorted audio *to* the modems and receive correct level audio *from* the modems. The Simulator has 4 audio gain/output level controls that allow a wide independent adjustment for CH1 and CH2 input and CH1 and CH2 Outputs. There are probably also other gain adjustments (hardware or software) on the *modem* in and out audio. Some modems also support an auto level function. The optimum settings are those output levels that allow the modem to be driven with the required signal without overdriving or clipping *and* with input levels and gain settings that allow the simulator to process audio in its normal range (200 – 1200 mv p-p). The p-p level readouts shown in Modes WGN thru MPD show the actual measured pp levels inside the simulator. Take time to adjust and note the settings [Modems 1 and 2 inputs and outputs, Simulator CH1 and CH2 Inputs and Simulator CH1 and CH2 outputs] so they can be easily setup again. (*Simulator gain settings can be saved to EEPROM by pushing on the right (parameter) encoder when the gain/level setting is displayed*). Input gains and output levels are adjustable over a wide logarithmic range. If an input gain or output level is too large for the dynamic range of the simulator (levels shown in RED) it may cause clipping and distortion of the audio signal. One suggested mechanism for setting the proper level signal into the simulator is:

- 1) Connect *one* modem output to the simulator (In1 or In2). If there is any noise or hum introduced by ground loops using a stereo line isolator (transformer) in the audio to/from the simulator has been shown to help.



2) Set the simulator up for the desired channel mode (WGN through MPP). This will show the input signal p-p readings (“Lvl =”) on the Ch1 In or Ch2 in lines.

3) Adjust the output of the *modem* to fall within a range of 200 – 1100 (mv p-p). The level can be controlled by **EITHER** the modem’s output level adjustment or the appropriate CH1 or CH2 **IN** adjustment on the simulator.

4) The simulator Out gain settings (CH 1 OUT or CH2 OUT) can then be used to make simulator *output* adjustments as needed for proper modem *input* level. There may be additional modem input gain settings on the modem. The maximum output of the simulator is limited to 2000mv p-p

5) Repeat steps 1-4 for the other modem input and output. If using a **single** Simulator to model a symmetric half duplex channel make the modem outputs (to the simulator) approximately the same level or adjust the CH1 and CH2 input gains to achieve similar p-p level readings. Make test runs using the WGN mode with 20 or 30 dB S:N to verify proper setup and operation before making any extensive measurement runs. For multipath simulations Insure normal output swings to not exceed simulator **Lvl** limits before running extensive tests. Document all the levels used to make repeated setups easier and consistent and if desired save the Simulator Input Gain and Output Level settings to EEPROM. *Note: The modem’s input and output levels may not be the same as normal operation with a transceiver since the audio input requirements and output levels from the transceiver may be different.*

**5.0 Manual User Interface (Right & Left Encoders).** The primary user interface consists of 2 rotary encoders with push capability and a 320x240 pixel color graphics display. The Encoders allow selection of basic Modes (left encoder) and parameters (right encoder). The left push function toggles less used functions on/off (e.g. **Spectrum Plot**). The right encoder push function toggles a built in 1.5 KHz oscillator on/off for testing or saves power on default parameter values to EEPROM. Fig 9 describes the modes and parameters of the left and right encoders.

<b>Manual User Interface</b>	
<b>Left Encoder</b>	<b>Right Encoder</b>
<b>MODE</b>	<b>PARAMETER / Range /Power On Default*</b>
WGN	S:N /-40 to +40 ,1 dB steps/ <b>+40*</b>
MPG	S:N /-40 to +40 ,1 dB steps/ <b>+40*</b>
MPM	S:N /-40 to +40 ,1 dB steps/ <b>+40*</b>
MPP	S:N /-40 to +40 ,1 dB steps/ <b>+40*</b>
MPD	S:N /-40 to +40 ,1 dB steps/ <b>+40*</b>
MULTIPATHS	# of Multipaths used/ 2, or 4/ <b>2</b>
FADE DEPTH (dB)	Depth of Fade dB/ 0 to 40 dB/ <b>0</b>
FADE FREQ (Hz)	Fade Frequency/0,.1,.2,.5,1,2,5,10,20/ <b>0</b> (Hz)
OFFSET	Freq Offset/-200 to +200 Hz, 10 Hz steps/ <b>0</b> (Hz)
FM DEVIATION(p-p, Hz)	FM Deviation/ 0,1,2,5,10,20,50,100,200/ <b>0</b> (Hz)
FM RATE (Hz)	FM Rate/ 0,.1,.2,.5,1,2,5,10,20/ <b>0</b> (Hz)
CH1 IN (Gain)	Ch1 Input Gain/0,1,2,5,10,20,50,100,200/ <b>10</b>
CH2 IN (Gain)	Ch2 Input Gain/0,1,2,5,10,20,50,100,200/ <b>10</b>
CH1 OUT (Gain)	Ch1 Output Gain/ 0,.01,.02,.05,.1,.2,.5,1,2/ <b>1</b>
CH2 OUT (Gain)	Ch2 Output Gain/ 0,.01,.02,.05,.1,.2,.5,1,2/ <b>1</b>
BANDWIDTH	Audio bandwidth/3 KHz or 6 KHz/ <b>3</b>
SERIAL	Serial baud rate 4800-115200 baud/ <b>9600</b>
TEST3K Sine	Test/Cal Measure Ch1,2, In p-p, Out p-p 3KHz BW
TEST6K Sine	Test/Cal Measure Ch1,2, In p-p, Out p-p 6KHz BW

\* Except for Path S:N, power on default may be saved to EEPROM with push of right encoder

Fig. 9. Manual User Interface Encoders 1 and 2.

**6.0 Computer interface.** Although most users will find the encoder interface above simpler and easier to use an alternative USB serial interface (4800-115200 baud) is included that can allow more sophisticated and automated testing using short ASCII commands through the Simulator's serial USB port. This will require some user supplied control or script program that sets up the simulator and modem, runs a session for a specific path type and S:N, and computes and saves performance data (e.g. average net bytes/minute transferred). This can save much time if a large number of simulations are to be run. Each command sent by the computer to the simulator must be sequenced by the correct response [ OK(Cr)] from the simulator. The commands are case insensitive standard ASCII as shown in Fig 11. If a syntax, spelling error, or parameter out of range is received by the simulator a ["?" (Cr)] is returned. The BUSY command is used for a busy detector function (section 7). The commands in Fig 10 correspond to mode and parameter settings of rotary encoders 1 and 2.

## Simulator USB Serial User Interface

### USB Serial Commands and Responses for automated testing

(Mimic manual commands from Mode and Parameter Encoders)

Command: value/range	Acknowledge Response/ Interpretation
WGN:20	OK(Cr) or ?(Cr) / WGN +20dB S:N
MPG:20	OK(Cr) or ?(Cr) / MPG +20dB S:N
MPM:-10	OK(Cr) or ?(Cr) / MPM -10dB S:N
MPP:+20	OK(Cr) or ?(Cr) / MPP +20dB S:N
MPD:20	OK(Cr) or ?(Cr) / MPD +20dB S:N
MULTIPATH: 2,4	OK(Cr) or ?(Cr) / # of multipaths used
FADE DEPTH: 0-40	OK(Cr) or ?(Cr) / FADE DEPTH:0-40dB
FADE FREQ: 0-20 6	OK(Cr) or ?(Cr) / FADE FREQ:(log)s Hz
OFFSET: -200 - +200	OK(Cr) or ?(Cr) / OFFSET: +/-200 Hz
FM DEVIATION:0-200 s	OK(Cr) or ?(Cr) / FM DEVIATION:(log)s Hz
FM RATE: 0-20 6	OK(Cr) or ?(Cr) / FM RATE:(log)s Hz
CH1 IN:0-200 s	OK(Cr) or ?(Cr) / CH1 IN gain,(log)s
CH2 IN:0-200 s	OK(Cr) or ?(Cr) / CH2 IN gain,(log)s
CH1 OUT:0-2 7	OK(Cr) or ?(Cr) / CH1 OUT gain,(log)7
CH2 OUT:0-2 7	OK(Cr) or ?(Cr) / CH2 OUT gain,(log)7
BANDWIDTH:3000	OK(Cr) or ?(Cr) / BANDWIDTH = 3KHz
BUSY:	OK(Cr) or ?(Cr) / ENABLE BUSY DETECT

Note: 1. All commands case insensitive. "+" signs ignored. All commands and replies terminate with (Cr).

2. "OK" reply indicates command was interpreted OK and executed.

3. "?" reply indicates command was not understood, corrupted or out of range.

4. Commands must be sequenced by sender using Acknowledge Response

5. Log scale values: 0,1,2,5,10,20,50,100,200.

6 Log scale values: 0,.1, .2, .5, 1, 2, 5, 10, 20.

7. Log scale values: 0, .01, .02, .05, .1, .2, .5, 1, 2.

**Fig 10. Serial Commands for Simulator Control**

(note: Command "BUSY:") is normally not used with the Simulator. It switches the **IONOS SIM** to a **BUSY DETECT** mode. See section 8.0). The **SERIAL** baud rate must be set manually using the Parameter encoder.

## 7.0 Basic Application of the Simulator for HF and VHF/UHF use

The use of the IONOS Sim is relatively straight forward but some care is required in setting up test to get consistent and representative test results.

**7.1 Test Parameters.** Most Simulation runs compare/plot net throughput (after all retries for ARQ modes) vs a specific path type and range of S:N (e.g. MPG over +20 to -5 dB). For comparison several graphs are often shown on one chart. To obtain accurate and repeatable results requires sending sufficient data over a fairly long session (at least 5 minutes and sometimes over 10.). This is because of the nature and designed randomness of the channel modeling and the adaptation to changing channels used by adaptive modems. White Noise and multipath channel delay values use random or normal (gaussian) distributions so even with long successive runs with the same test data and parameter there will normally be some variance in net throughput values.

**7.2 Test Examples for HF and VHF Testing.** Fig 11 below plots the performance in net bytes/minute of five different HF protocols over a S:N range +40 to -5 dB S:N using a simulated Multi Path Poor (MPP) channel done by Tom Whiteside N5TW using the IONOS SIM simulator. The drop off of throughput vs S:N is normal and gives a good indication of how each protocol can be expected to work at a given received S:N on a typical multipath poor channel.

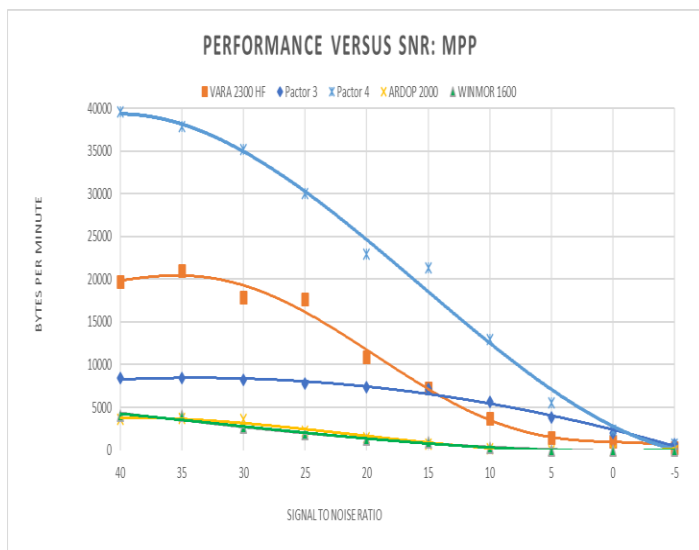


Fig 11: 5 HF Protocols over +40 to -5 dB S:N, MPG

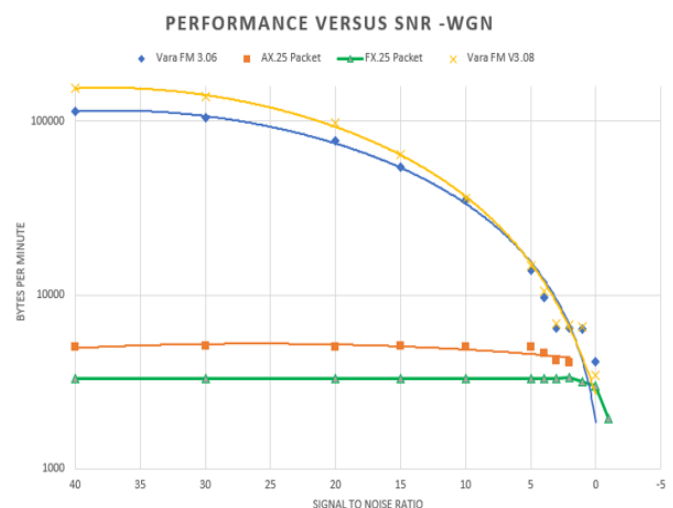


Fig 12: 4 VHF FM Protocols over +40 to -3 dB S:N

Figure 12 shows a simulation for 4 different VHF FM protocols over a WGN channel. Since these protocols can use wider bandwidth “audio” to the FM Modulator the IONOS SIM simulator was set for its 6KHz bandwidth option. More details on Tom’s extensive simulation efforts using the IONOS SIM simulator can be found at [https://winlink.org/content/ionos\\_simulator](https://winlink.org/content/ionos_simulator). To collect repeatable and representative data it is necessary for the simulations runs to be long enough to accommodate modems/protocols that use adaptive modulation and to average out the slow amplitude fluctuations due to multipath. Each “dot” or symbol in the above plots represents a typical simulator run of 5-10 minutes. Measured throughput values may be changed somewhat by the length of test and the client program used so for best accuracy these should be kept constant and noted in the test results.

To aid making many long runs when extensive testing is anticipated the IONOS SIM simulator can be computer controlled using the text commands shown in Fig 10. These example simulation cases and runs highlight why trying to obtain this kind of test accuracy and repeatability is so difficult using normal “over-the-air” radio testing.



# IONOS SIM HF/VHF Channel Simulator Specifications

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**Description:** IONOS SIM is a self-contained audio DSP based ionospheric simulator based on the Watterson Model [1] for ITU/CCIR standardized HF and VHF propagation paths. It uses the Teensy 4.0 CPU (ARM Cortex-M7 processor at 600 MHz) and Teensy Audio DSP Library.

**Primary Use:** The development, characterization, maintenance and comparison of HF and VHF modems and protocols including “sound card” protocols.

**Channels Modeled:** **WGN** (White Gaussian Noise: Spread 0 Hz, Delay: 0 ms); , **MPG** (CCIR Multipath Good) Spread .1 Hz, Delay .5 ms ; **MPM** (CCIR Multipath Moderate) Spread .5 Hz, Delay 1 ms; , **MPP** (CCIR Multipath Poor) Spread 1 Hz, Delay 2 ms; **MPD** (Multipath disturbed) Spread 2 Hz, Delay 4 ms; **Flat Fading** 0-40 dB, Fade Rate: .1-20 Hz; **Fixed Freq offset** +/- 200 Hz, 10 Hz steps; **Slow FM deviation** +/- 1-200 Hz, Rate .1 to 20 Hz; Multipath update rate 64x Doppler spread. Fade, offset, and FM can be combined with mode WGN.

**Audio Paths:** WGN: 1 path. Multipath: 2 paths. (future update to 3 and 4 paths). Input to Output Delay: Min 3.5ms (WGN); Max 8 ms (Multipath Disturbed). I2, Q2 Paths delayed by channel model (.5, 1, 2, or 4 ms)

**Connections Modeled:** 1 Simulator: Half duplex, both sides, symmetric channel models.  
2 Simulators: Full Duplex both sides: symmetric or asymmetric channel models.

**Bandwidths:** 3 KHz: 300 Hz to 3.3 KHz, 6 KHz: 300 Hz to 6.3 KHz. +/- 1dB **Sampling rate:** 44.1 KHz

**Noise modeling:** WGN filtered to 3 KHz or 6 KHz bandwidth. S:N -40 to +40 dB, 1 dB steps.

**Inputs:** Ch1, Ch2 via standard Stereo 1/8” (3.5 mm) jack. AC coupled. Input protection. Impedance > 50KOhms. Nominal input range: 10 – 1800 mv p-p. Manual input level control. Internal simulator level and CF displayed.

**Outputs:** Ch1, Ch2 via standard Stereo 1/8” (3.5 mm) jack. AC Coupled. Short circuit protection. Impedance 150 Ohms. Nominal output range 10 - 1800 mv p-p. Manual output level control.

**Power Required:** Nominal 5 v (internally reg. to 3.3 V) via micro USB connector. < 200 ma.

**Display:** 320H x 240V pixel color TFT for Text and Graphics. Spectrum Display 3 & 6 KHz.

**Modes:** Self-Test and Calibration verification to +/-1 dB Across operating BW; Setup modes and parameters;

**User Parameter and control:** Two Incremental encoders (Mode, Parameter) each with push toggle. Alternate USB serial ASCII commands for automated operation (4800-115200 baud).

**Size:** 6.1” (155 mm) L x 4.7” (120 mm) W x 1.4” (36 mm) H (excluding knobs).

**Alternate Functions:** Integrated Busy Channel Detector (experimental). Uses only serial USB ASCII command interface with host program.

[1] Watterson, C.C., J.R. Juroshek, & W.D. Bensema. 1970 Experimental confirmation of an HF channel model IEEE Transaction of Communication. Technology. Vol COM-18. Pp 792-803 Dec 1970

## APPENDIX:

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