

The schematic diagram illustrates the RTL8762DW module, a small form-factor device with various pins and internal components. The module is labeled "RTL8762DW" in the center.

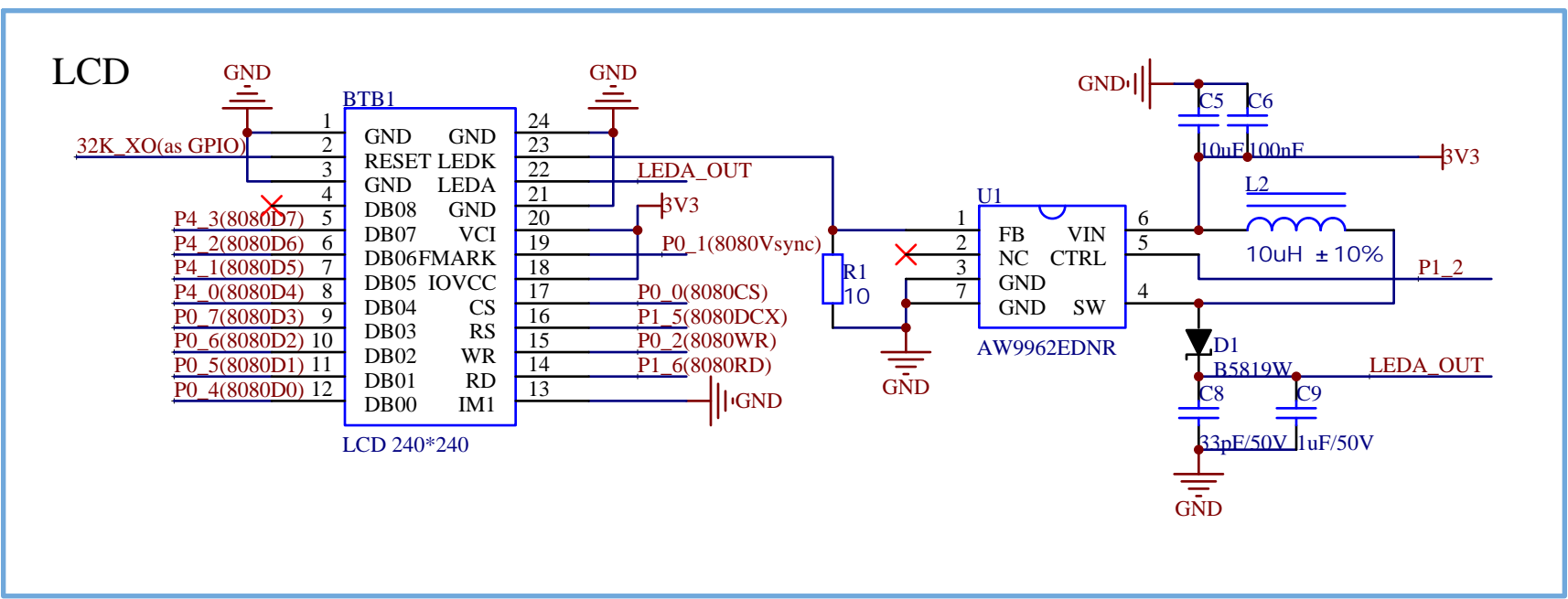
Pin Connections:

- Top Pins (57-43):**
 - 57: EPAD
 - 56: 32K_XO (as GPIO)
 - 55: VBAT
 - 54: P3_0(UART_TX)
 - 53: P3_1(UART_RX)
 - 52: P3_2(SPIC1_SIO0)
 - 51: P3_3(SPIC1_SIO1)
 - 50: P3_4(SPIC1_SIO2)
 - 49: P3_5(SPIC1_SIO3)
 - 48: P3_6(SPIC1_CLK)
 - 47: P1_5(8080DCX)
 - 46: P1_7(SPIC1_CS)
 - 45: P1_6(8080RD)
 - 44: HVD
 - 43: LX
- Left Pins (1-14):**
 - 1: 32K_XO (as GPIO)
 - 2: P2_0
 - 3: P2_1
 - 4: P2_2
 - 5: P2_3
 - 6: P2_4
 - 7: P2_5
 - 8: P2_6(MIC_N)
 - 9: P2_7(MIC_P)
 - 10: MICBIAS
 - 11: VA18
 - 12: VD12_TRX
 - 13: RFIO
 - 14: VD12_PA
- Bottom Pins (15-28):**
 - 15: VD12_SYN
 - 16: XI
 - 17: XO
 - 18: RESET
 - 19: P4_3(8080D7)
 - 20: P4_2(8080D6)
 - 21: P4_1(8080D5)
 - 22: P4_0(8080D4)
 - 23: P0_7
 - 24: P0_6
 - 25: P0_5
 - 26: P1_2
 - 27: P1_3(SPIC0_SIO3)
 - 28: SCLK
- Right Pins (42-29):**
 - 42: VDDCORE
 - 41: VDIGI
 - 40: P1_1(SWDCLK)
 - 39: P1_0(SWDIO)
 - 38: P0_0(8080CS)
 - 37: P0_1(8080Vsync)
 - 36: P0_2(8080WR)
 - 35: P0_3
 - 34: P0_4(8080D0)
 - 33: VDDIO
 - 32: P1_4(WP#SIO2)
 - 31: SO/SIO1
 - 30: CS
 - 29: SI/SIO0

Internal Components and Connections:

- Capacitors:** C1 (4.7uF), C2 (100nF), C3 (4.7uF), C4 (2.2uF), C5 (1uF), C6 (1uF), C7 (100nF), C8 (1uF), C9 (1uF), C10 (1uF), C11 (1uF), C12 (1uF), C13 (1uF), C14 (1uF), C15 (100nF), C16 (10K), C17 (100nF), C18 (22pF), C19 (100nF), C20 (1.2pF), C21 (1.2pF), C22 (NP), C23 (NP), C24 (NP), C32 (NP).
- Inductors:** L1 (4.7uH), L2 (0.3nH), L3 (0.3nH), L4 (0.3nH).
- Resistors:** R16 (10K).
- Other Components:** Y1 (Oscillator), OSC2, OSC1, NP (Not Present), SW-SMD (Switch).

The diagram shows the module's internal circuitry, including the oscillator, decoupling capacitors, and various signal paths. The module is designed to be connected to a 3V3 power supply and a 32KHz crystal oscillator.



PSRAM

NOR FLASH

The diagram shows a NOR FLASH memory (U3) connected to a microcontroller. The microcontroller pins are CS, SO/SIO1, P1_4(SPIC0_SIO2), GND, /CE, SO/SIO[1]SIO[3], SIO[2], SCLK, GND, and SIO[0]. The NOR FLASH pins are CS, /CE, SO/SIO[1]SIO[3], SIO[2], SCLK, GND, and SIO[0]. The circuit includes a 10K resistor (R3) connected to CS and a 100nF capacitor (C16) connected to /CE. Both CS and /CE are connected to a 3V3 supply. The SO/SIO[1]SIO[3] pin is connected to P1_4(SPIC0_SIO2). The SIO[2] pin is connected to SCLK. The SIO[0] pin is connected to S/SIO0. The GND pins are connected to GND.

USB-UART

U7

1 UD+ 2 UD- 3 UD- 4 GND 5 RTS# 6 CTS# 7 VCC 8 TXD 9 RXD 10 TNOW

CH340E

V3

P3_0(UART TX)

P3_1(UART RX)

3V3

GND

GND

C33

100nF

[illegible]

AXIS SENSOR

TOUCHPAD

The diagram illustrates the electrical connections for a touchpad. The central component is the BS814A-1 IC (U8), which is a 10-pin device. Its pins are connected as follows:

- Pin 1 (VDD):** Connected to a 3V3 power supply.
- Pin 2 (KEY1):** Connected to a 10pF capacitor (C34) and the TPAD pin of the RIGHT touchpad.
- Pin 3 (KEY2):** Connected to a 10pF capacitor (C35) and the TP4 pin of the LIFT touchpad.
- Pin 4 (KEY3):** Connected to a 10pF capacitor (C36) and the TP3 pin of the DOWN touchpad.
- Pin 5 (KEY4):** Connected to a 10pF capacitor (C37) and the TP2 pin of the UP touchpad.
- Pin 6 (OUT4):** Connected to the P2_7 signal line.
- Pin 7 (OUT3):** Connected to the P2_6 signal line.
- Pin 8 (OUT2):** Connected to the P2_5 signal line.
- Pin 9 (OUT1):** Connected to the P2_4 signal line.
- Pin 10 (VSS):** Connected to ground (GND).

Additionally, a 100nF capacitor (C38) is connected between the 3V3 supply and ground. The touchpads are labeled RIGHT, LIFT, DOWN, and UP, with their respective TPAD and TP pins.

The diagram shows a 555 timer configured as an astable multivibrator. The timing network consists of a 33K resistor (R6) and a 10K resistor (R7) connected to the timing pin (pin 5). The output of the timer (pin 3) is connected to the base of an NPN transistor (Q3, SS8050). The emitter of the transistor is connected to GND, and the collector is connected to one terminal of a speaker (B1). The other terminal of the speaker is connected to a 3V3 supply. The 3V3 supply is also connected to the VCC pin (pin 1) of the 555 timer. The GND pin (pin 4) of the 555 timer is connected to GND. The output of the speaker is labeled BEEP.

SWD