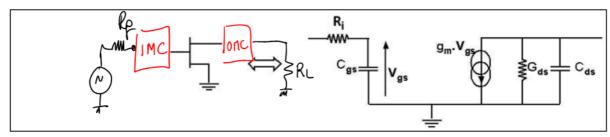




## Basics of Active and Nonlinear HF Electronics – Tutorial 2

## A) Power gain matching of a single-stage narrow-band amplifier

This figure represents the simplified electrical model of an FET.



The model parameters are given at a bias point of  $(V_{GSO} = -2 \text{ V and } V_{DSO} = 20 \text{ V})$ 

$$\text{Ri} = 5~\Omega$$
 ;  $\text{C}_{\text{GS}} = 0.5~\text{pF}$  ;  $\text{g}_{\text{m}} = 100~\text{ms}$  ;  $\text{R}_{\text{DS}} = 200~\Omega$  ;  $\text{C}_{\text{DS}} = 0.15~\text{pF}$ 

The amplifier environment is :  $R_G = 50 \Omega$  ;  $R_L = 50 \Omega$  ;  $f_0 = 10 \text{ GHz}$ 

- Determine the expression of G<sub>MAX</sub>; maximum power gain of the transistor as a function of frequency
- We consider the design of a linear amplifier, which is loaded by a resistor  $(R_1)$ , while the input generator  $\lambda$ has an internal resistor(R<sub>G</sub>.)
  - a) Draw the ideal input and output matching circuits of a narrow-band amplifier which is matched to its maximum power gain G<sub>MAX</sub> at f<sub>0</sub>.
  - b) Determine the expressions of each passive matching elements as a function of f<sub>0</sub>, the model parameters and the R<sub>G</sub> and R<sub>L</sub> resistors.
- 3) Determine the expressions of the input and output quality, factor's/QIN and QOUT, respectively.



$$P_{IN} = \frac{1}{2} \operatorname{Re} \left( V_{\Lambda} I_{\Lambda}^{*} \right) \left| I_{\Lambda} I_{\Lambda}^{2} \right|$$

$$= \frac{1}{2} \operatorname{Re} \left( \left( \operatorname{Ri} - \hat{j} \frac{b_{\Lambda}}{G_{A} \omega} \right) I_{\Lambda} I_{\Lambda}^{*} \right) = \frac{1}{2} \operatorname{Ri} \left| I_{\Lambda} \right|^{2}$$

$$V_{\Lambda}$$

$$T_{N} = \int G_{N} \omega |V_{S}| \rightarrow |T_{N}| = G_{N} \omega |V_{S}|$$

$$\longrightarrow P_{N} = \frac{1}{2} \operatorname{Ri} G_{N} \omega^{2} |V_{S}|^{2}$$

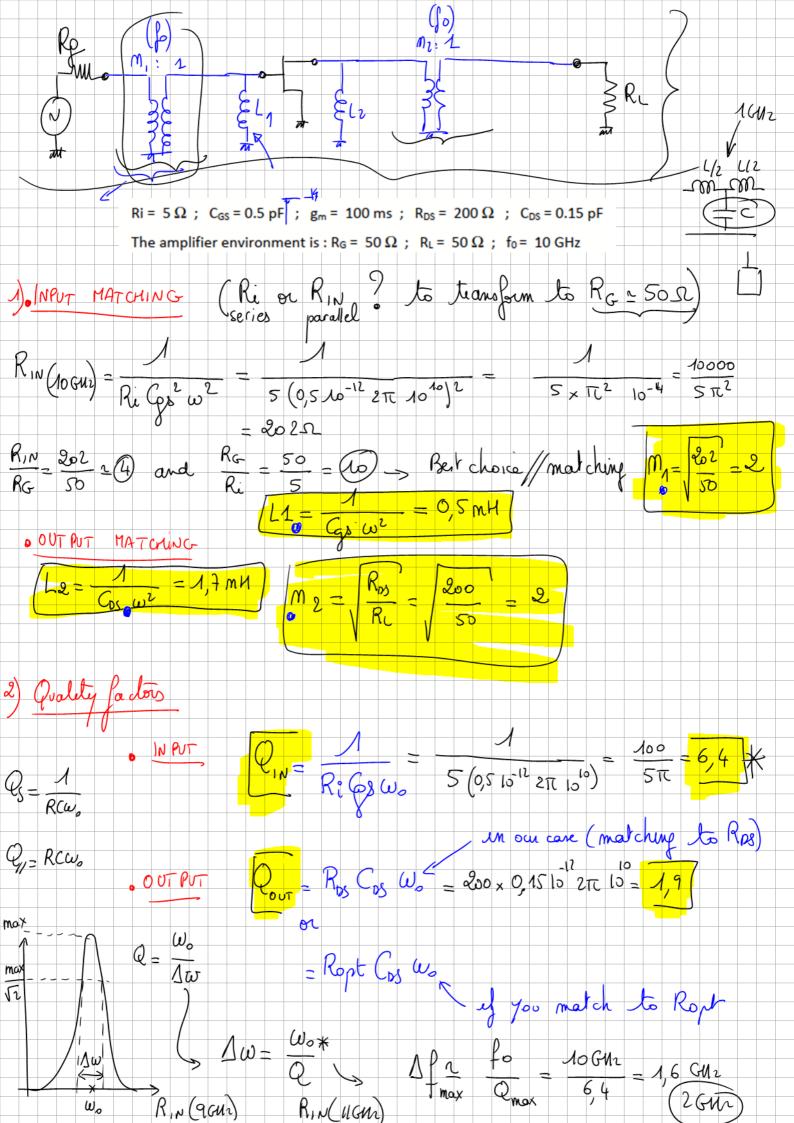
$$G_{MAX} = \frac{1}{4} \frac{\operatorname{Ros} gm^{2}}{\operatorname{Ri} G_{N}^{2} \omega^{2}}$$

Pout = 
$$\frac{1}{2}$$
 Re( $V_{out}$   $I_{out}$ )

=  $\frac{1}{2}$  Re( $R_{os}$   $I_{out}$   $I_{out}$ )

=  $\frac{1}{2}$  Ros  $I_{out}$   $I_{out}$ 

max  $I_{out}$  =  $\frac{1}{2}$  Ros  $I_{out}$   $I_{out}$ 



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# B) Power gain matching of a two stage narrow-band amplifier

#### Main pecifications of the narrow-band amplifier:

Output Power  $P_{OUT} > 1 \text{ W}$ ; Center frequency  $f_0 = 15 \text{ GHz}$ ; Power Gain  $G_P > 15 \text{ dB}$ 

### Selected MMIC foundry (0.15 μm GaAs HEMT):

Power Density PD = 1 W/mm ; Measured Gain G<sub>MAX(@10GHz)</sub> = 16 dB ;

Saturated Drain Current IDSS = 800 mA/mm; Drain Voltage range (VDSmin=1V and VDSmax=11V)

Pinch-off Voltage: VP = -4 V

Optimum load impedance  $R_{OPT} = 12.5 \Omega.mm$ 

Small-signal model:  $C_{GS} = 2.7 \text{ pF/mm}$ ; Ri =1  $\Omega$ .mm;  $g_m = 200 \text{ mS/mm}$ ;

 $R_{\text{DS}}$  = 125  $\Omega.mm$  ;  $C_{\text{DS}}$  = 0.6 pF/mm

<u>Selected device size @ 15 GHz</u> = 4x75 μm GaAs HEMT

- 1) Given the required specifications and the selected transistor technology, determine the initial sizing of the power amplifier (number of stages, number of amplifying cells per stage, power gain and input/output powers of each stage) using the selected device size (4x75  $\mu$ m) as the unitary amplifying cell.
- 2) Given that only the last stage should be matched to its optimum output power while all the preceding stages should be matched to their maximum power gain, draw the input and output matching circuits of this two-stage narrow-band amplifier and calculate the values of each matching elements.
- 3) Calculate the input and output quality factors of each stage to assess what will be the bandwidth behavior.

4) Can you comment on the power limitations and the power added efficiency of this two-stage amplifier?

