Outline

- **♦** Introduction
- ★ Theory of frequency conversion
- **♦** Characteristic performances of mixers
- Active mixers
 - → Single Ended (SEM) → Gate Mixer / Drain Mixer
 - → Gilbert Cell → DBDM Double Balanced Differential Mixer
- Passive Mixers
 - → Diodes → SEM / SBM / DBM
 - → Cold FETs → SEM / SBM
- **♦ IRM: Image Reject Mixers**

Passive "Resistive" Mixers

Multiplication: Resistive Mixers

◆ Principle: Implementing a multiplication by using the Ohm's law

$$\begin{aligned} V &= R.I \quad or \quad I = G.V \\ V_{out}(t) &= R(t).I_{in}\left(t\right) \quad or \quad I_{out}\left(t\right) = G(t).V_{in}\left(t\right) \end{aligned}$$

- ♦ Varying nonlinear conductance G(t) = nonlinear element driven by a large signal @ LO
 - → Obtain a time varying conductance and its required frequency spectrum
- **♦** Expansion of G(t) in Fourier transform:

$$G(t) = G_0 + G_1.\cos(\omega_{LO}.t) + G_2.\cos(2\omega_{LO}.t) + G_3.\cos(3\omega_{LO}.t) + \dots$$

$$Ideal term$$

$$Spurs for a fundamental mixer (However 2^{nd} harmonic can be used for subharmonic mixers)$$

The mixing is obtained by applying a low signal V_{RF} at the input of the time varying conductance to create the required converted current at IF

$$I_{S}(t) = G(t) \cdot V_{RF}(t)$$

$$\uparrow \qquad \uparrow \qquad \uparrow$$

$$\omega_{IF} \quad \omega_{IO} \quad \omega_{RF}$$

Principle of Diode Mixers

Minimum of conversion losses for passive mixers

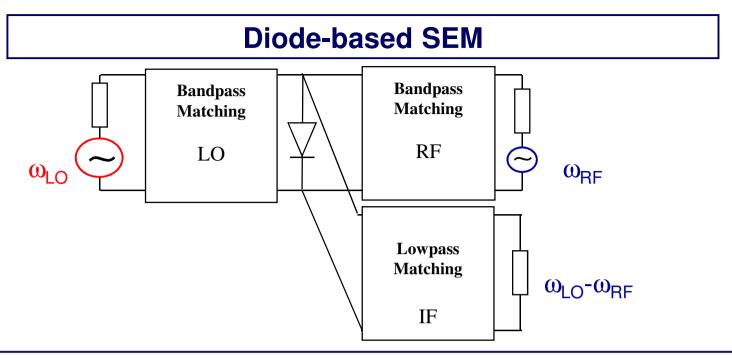
Ideal case of a diode mixer :

$${\to} Lc_{MIN} \cong 4 \ dB$$

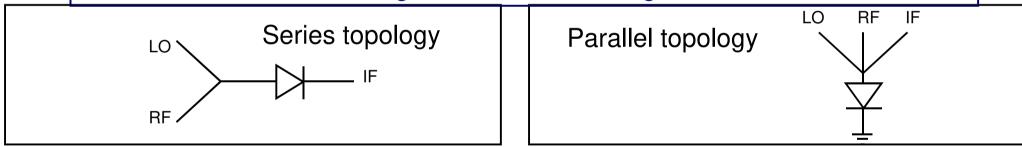
♦ Actual case :

$$Lc_{MIN} \approx 5 \text{ à } 6 \text{ dB}$$

$$Lc = f \left[I-V_{NL}, V_{LO}(t), Z_{L}(\omega_{SPURS}) \right]$$



Critical matching constraints for single diode mixers



Advantages:

Low Lc and P_{LO}

Disadvantages:

- No suppression of spurs
- Bandwidth and isolation are greatly limited by critical filtering issues
- Filter losses increase Lc → Very few diode-based SEM

SBM and DBM diode-based mixers

SBM: Single Balanced Mixer= 2 SEM with couplers or baluns

DBM: Double Balanced Mixer = 2 SBM with couplers or baluns

Advantages



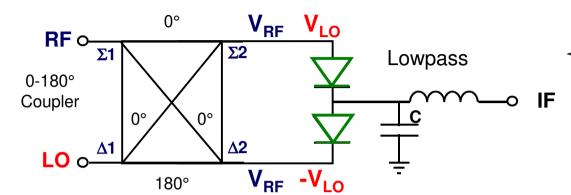
- Suppression of some spurs by out of phase recombination
- Good LO-RF Isolation and sometimes LO-IF isolation without filtering issues

Disadvantages

Requirement of higher levels of LO power

Example of a diode-based SBM (LO at Δ port)

•Architecture with 180° coupler (LO @ Δ)



180° Coupler

Input Matching @LO/RF ≈ Input Matching@LO/RF of coupler

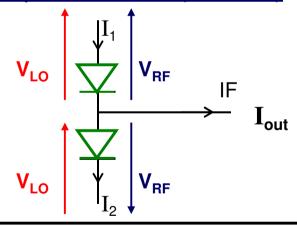
Isolation LO-RF ≈ Isolation LO-RF of coupler

LO@ 5 > mix[FH(LO) * 1 rejected

LO @ $\Sigma \rightarrow mix[EH(LO), *]$ rejected

LO @ $\Delta \rightarrow \text{mix}$ [* , EH(RF)] rejected

Operation mode (LO @ Δ)



$$I(V) = F(V) = a.V + b.V^2 + c.V^3 + ...$$

$$I_1 = F(V_{LO} + V_{RF})$$

$$I_2 = F(V_{LO} - V_{RF})$$

$$V_{\text{LO}}^{\text{p}}$$
 . $V_{\text{RF}}^{\text{q}} \rightarrow \pm \text{p.} \omega_{\text{LO}} \pm \text{q.} \omega_{\text{RF}}$

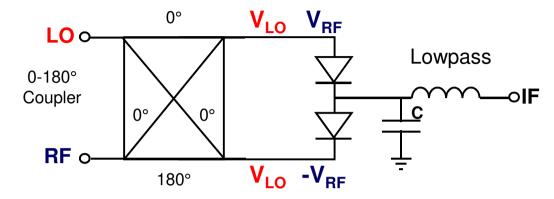
$$I_{OUT} = I_1 - I_2 = 0 \iff \omega = \pm m.\omega_{LO} \pm n.\omega_{RF}$$

If **n is even** because $(V_{RF})^n = (-V_{RF})^n$

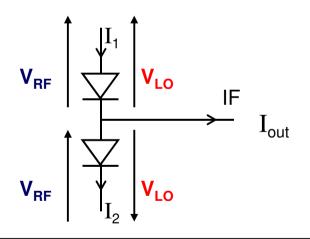
 ω_{LO} , $2(\omega_{LO} - \omega_{RF})$, ... suppressed @ IF port \rightarrow (LO-IF Isolation) ...

Diode-based SBM : LO and RF ports are reversed (LO at Σ port)

•Architecture with 180° coupler (LO @ Σ)



•If LO and RF are reversed



$$I(V) = F(V) = a.V + b.V^2 + c.V^3 + ...$$

$$I_1 = F(V_{RF} + V_{LO})$$

$$I_2 = F(V_{RF} - V_{LO})$$

$$V_{LO}{}^p$$
 , $V_{RF}{}^q \to p \omega_{LO} \pm \ q \omega_{RF}$

$$I_{OUT} = I_1 - I_2 = 0 \iff \omega = \pm \text{ m.} \omega_{LO} \pm \text{ n.} \omega_{RF}$$

If **m** is even because $(V_{LO})^m = (-V_{LO})^m$



$$\omega_{RF}$$
, $2(\omega_{LO}-\omega_{RF})$, ... suppressed @IF port \rightarrow (RF-IF Isolation) ... Less critical

Diode-based SEM

- Properties are linked to the architecture (Baluns, Couplers, ...)
- Conversion losses of SBM are higher than conversion losses of SEM

(Coupler losses, Non ideal phase shift and amplitude balance ...)

• SBM enable to suppress critical spurs and enhance isolations



Diode-based SBM are more efficient than diode-based SEM

Diode-based DBM

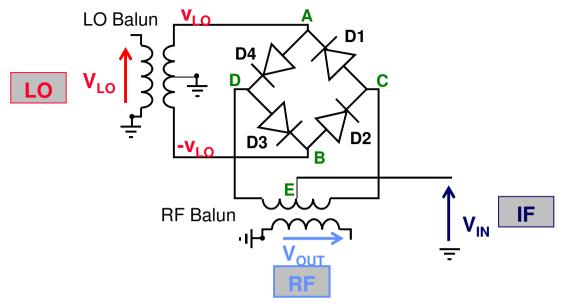
A diode-based DBM integrates 2 pairs of diodes

Diode-based DBM are preferred to SBM

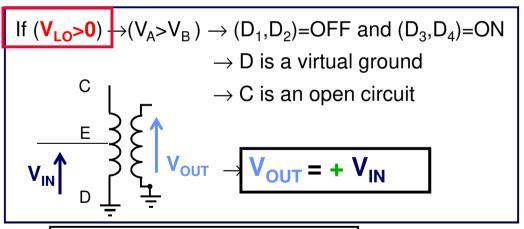
if the isolation requirements are more critical than the conversion loss specifications

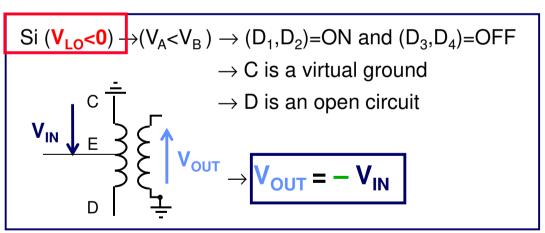
Example of diode-based DBM: Ring oscillator

Architecture



Operation principle (Diode = switch controlled by LO)





$$\rightarrow \boxed{ \mathbf{V}_{\mathsf{OUT}}(t) = \mathbf{sign}[\mathbf{V}_{\mathsf{LO}}(t)] \times \mathbf{V}_{\mathsf{IN}}(t) } \quad \text{avec} \qquad \mathbf{sign}[\mathbf{V}_{\mathsf{LO}}(t)] = \frac{4}{\pi} \left[\cos(\omega_{LO}t) - \frac{1}{3}\cos(3\omega_{LO}t) + \frac{1}{5}\cos(5\omega_{LO}t) - \dots \right]$$

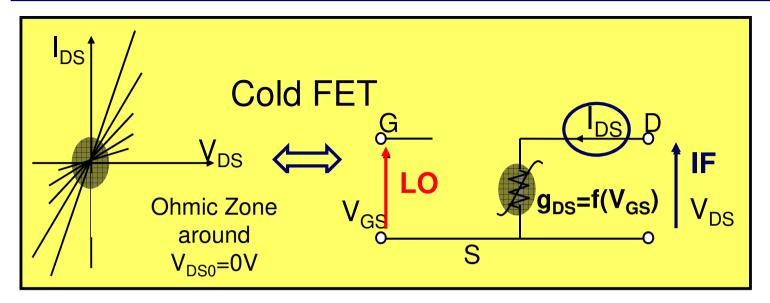


 $\mathbf{V_{OUT}} = \mathbf{0} \iff \omega = m. \omega_{LO} \pm \ n. \omega_{IF} \ / \ (m \ or \ n) \ even \rightarrow \omega_{LO} \, , \ \omega_{IF} \ldots \ suppressed @ \ RF \ port$

Cold-FET mixers : Principle

Main advantages: High linearity and No Consumption

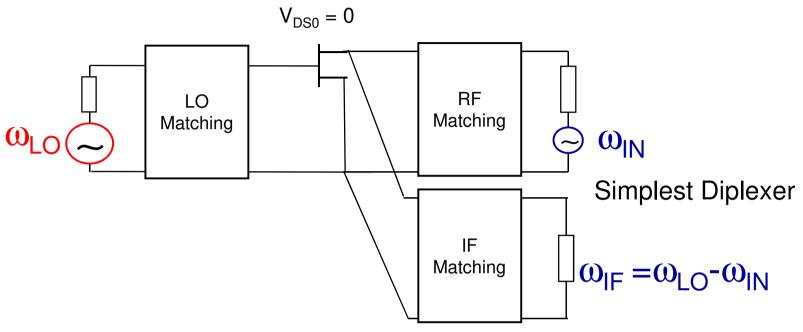
Principle of cold-FET resistive mixers



Not biased Drain

- \rightarrow Transconductance gm is zero @ $V_{DS0}=0$
- \rightarrow Resistive Nonlinearity $R_{DS}\left(V_{GS}\right)$ between drain and source

Cold-FET advantages



- → Simplified design constraints
 - better LO-RF isolation due to the intrinsic G-D isolation of FETs (depends on Cgd and ω)
 - limited interactions between matching circuits (LO @ gate port and $\omega_{RF}>>\omega_{IF}$ @ drain port)
 - higher linear voltage swing of RF signal from 0 to knee voltage \rightarrow higher linearity
 - Only mixer that can be efficiently used in SEM configuiration
- → Other solutions: SBM and DBM cold-FET mixers

Comparison (Diode / Cold FET)

• Conversion losses

 \rightarrow L_C(Diode Mixers) < L_C(Cold-FET mixers) because R_{MIN}(Diode) < R_{MIN} (Cold-FET)

• <u>Linearity/Saturation</u>

- Linearity of diode-based mixer is limited to very low level of RF signal (exponential current)
- Linearity of cold-FET mixer is limited to moderate level of RF signal (ohmic zone)
 - → Higher linearity of cold-FET and higher output power

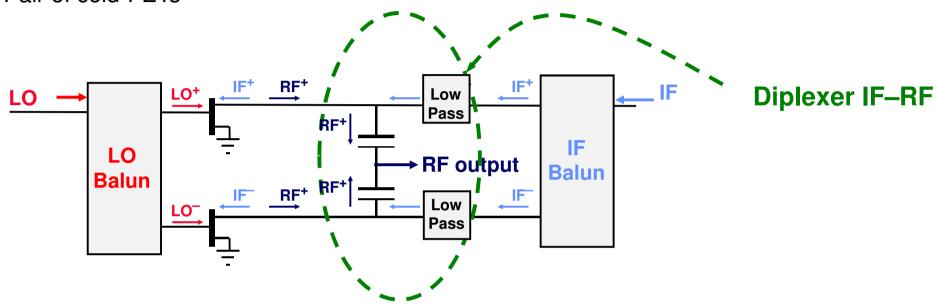
Isolations

- Diodes do not have intrinsic isolation → can only used in complex balanced architectures
- Cold-FETs have the intrinsic G-D isolation → can be used as SEM or balanced topologies

Cold-FET SBM

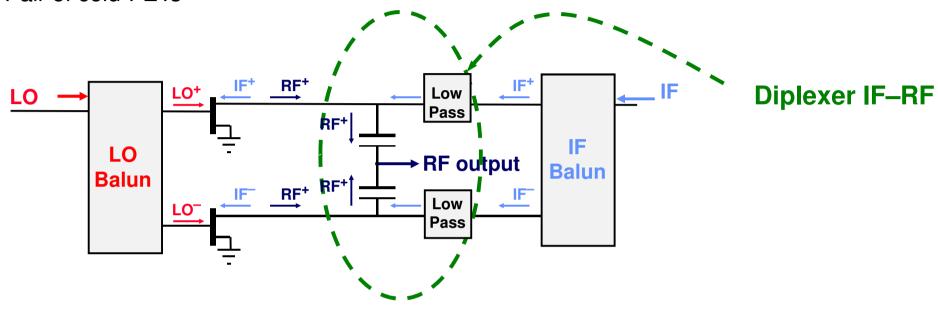
Cold-FET SBM

- Example of SBM with LO and IF baluns (emission)
- Pair of cold-FETs

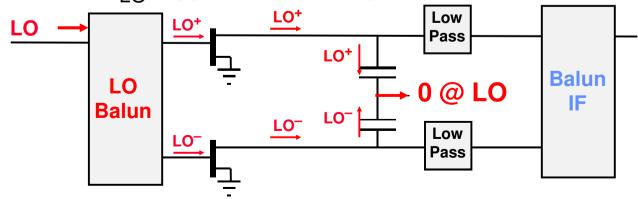


Cold-FET SBM

- Example of SBM with LO and IF baluns (emission)
- Pair of cold-FETs



• Balanced architecture $\Rightarrow \omega_{LO}$ suppressed @ drain port



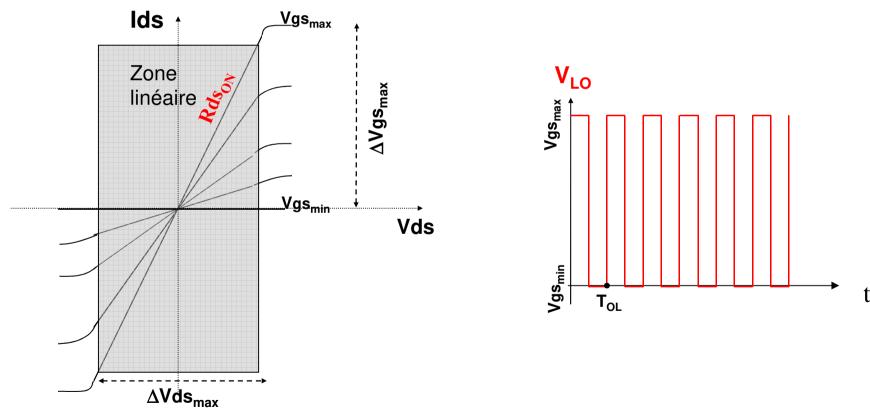
Cold-FET DBM

Ring mixer
4 Cold-FETs + 3 Baluns (RF, IF, LO)

Switch mode of Cold-FET

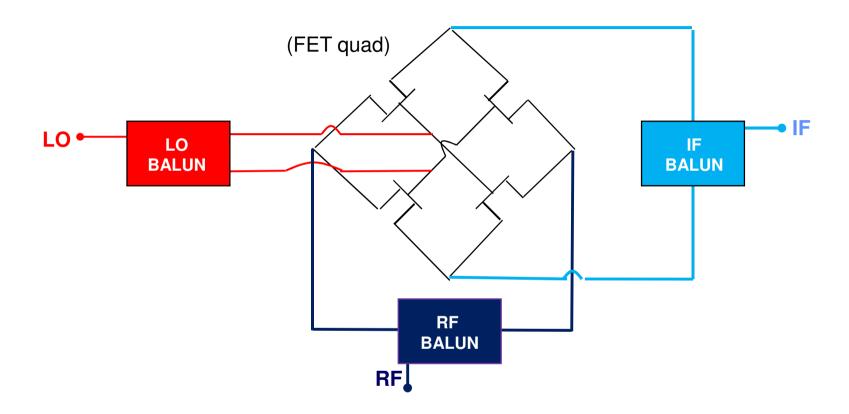


The Cold-FET is switched between R_{DSON} (minimum value of R_{DS}) and ∞ (maximum value of R_{DS})

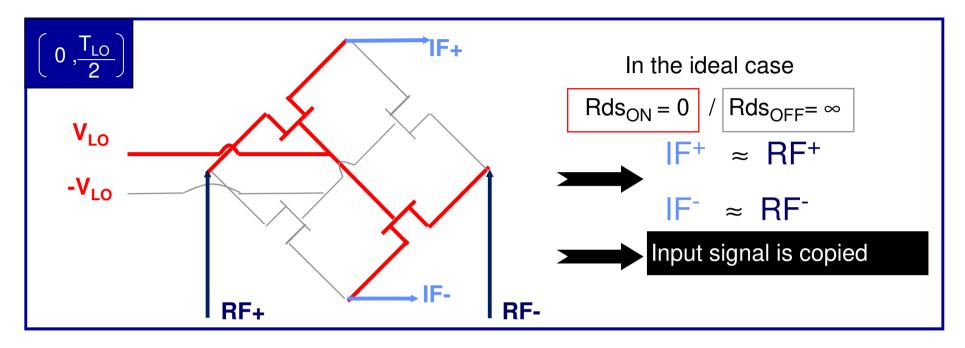


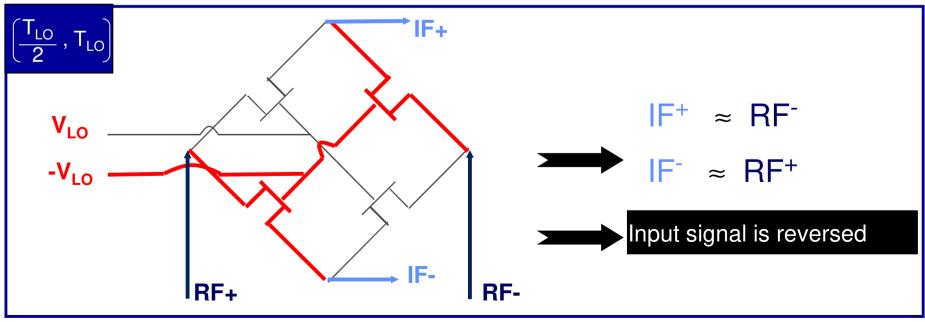
If the transistor size is increased
$$\rightarrow$$
 $\left[I_{DS_{MAX}}\uparrow\right]$ \rightarrow $\left[R_{DS_{ON}}\downarrow\right]$ \rightarrow $\left[Conversion\ losses\downarrow\right]$ However \rightarrow $\left[C_{GS}\uparrow\right]$ \rightarrow $\left[Frequency\ limitation\ \uparrow\right]$

Cold-FET « Switch Mode Mixer » : Ring DBM



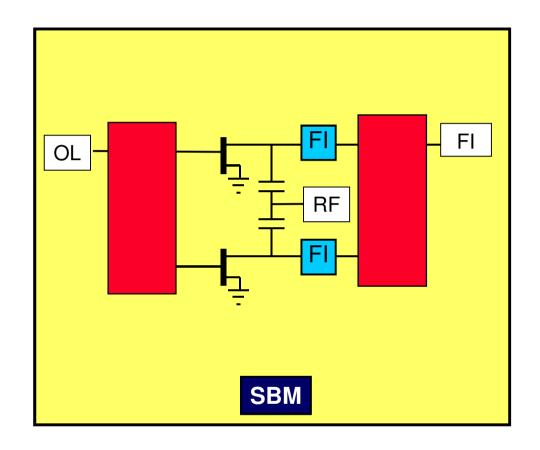
Operating principle of the ring Cold-FET DBM

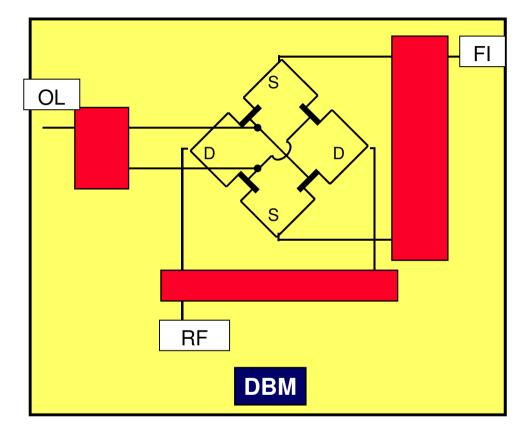




Main Cold-FET mixer architectures

Cold-FET Mixers





Example of IRM (Image Rejection Mixer)

Example of IRM

