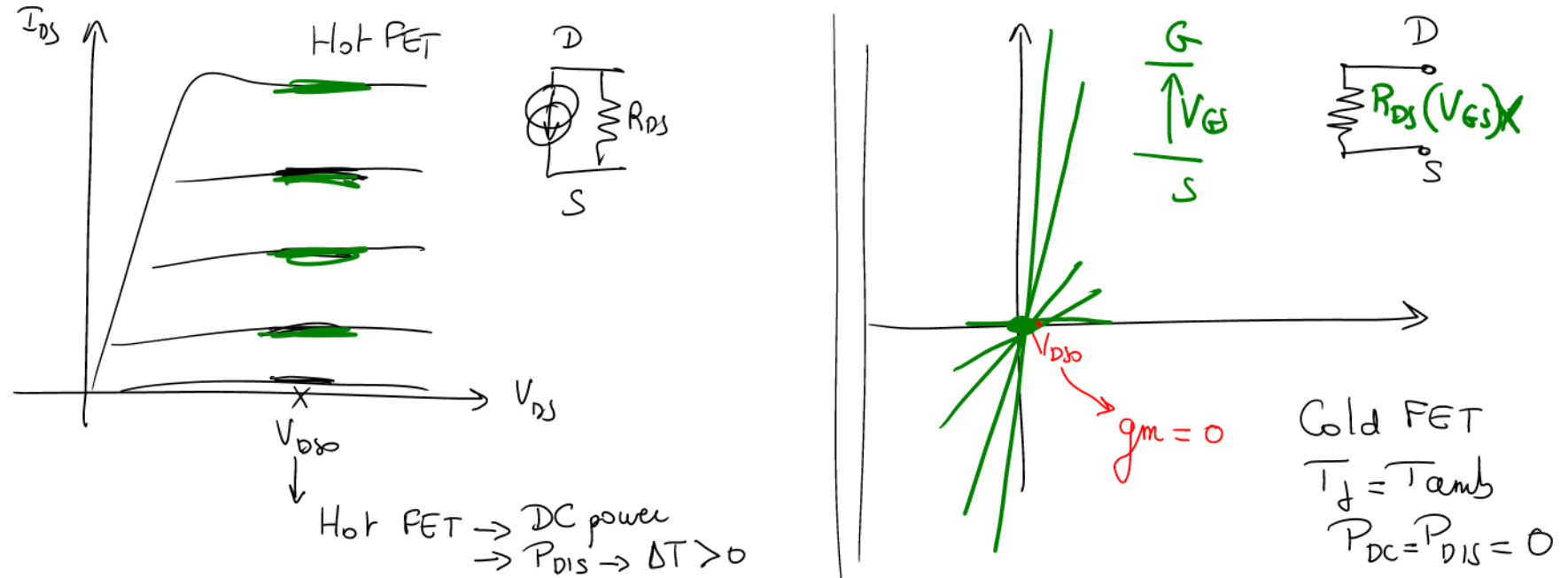


Outline

- ◆ Introduction
- ◆ Theory of frequency conversion $\rightarrow \otimes$ NL switches (sgn)
- ◆ Characteristic performances of mixers
- ◆ Active mixers
 - \rightarrow Single Ended (SEM) \rightarrow Gate Mixer / Drain Mixer
 - \rightarrow Gilbert Cell \rightarrow DBDM Double Balanced Differential Mixer

- ◆ Passive Mixers
 - \rightarrow Diodes \rightarrow SEM / SBM / DBM
 - \rightarrow Cold FETs \rightarrow SEM / SBM
- ◆ IRM: Image Reject Mixers



Passive "Resistive" Mixers

* Multiplication: Resistive Mixers

- ◆ **Principle** : Implementing a multiplication by using the Ohm's law

$$V = R.I \quad \text{or} \quad I = G.V$$

$$V_{\text{out}}(t) = R(t).I_{\text{in}}(t) \quad \text{or} \quad I_{\text{out}}(t) = G(t).V_{\text{in}}(t)$$

- ◆ **Varying nonlinear conductance $G(t)$** = nonlinear element driven by a large signal @ LO
→ Obtain a time varying conductance and its required frequency spectrum

- ◆ **Expansion of $G(t)$ in Fourier transform:**

$$\underbrace{G(t)}_{\text{Ideal term}} = G_0 + \underbrace{G_1 \cdot \cos(\omega_{\text{LO}} \cdot t)}_{\text{Spurs for a fundamental mixer}} + \underbrace{G_2 \cdot \cos(2\omega_{\text{LO}} \cdot t) + G_3 \cdot \cos(3\omega_{\text{LO}} \cdot t) + \dots}_{\text{(However 2nd harmonic can be used for subharmonic mixers)}}$$

Ideal term

Spurs for a fundamental mixer (However 2nd harmonic can be used for subharmonic mixers)

- ◆ The mixing is obtained by applying a low signal V_{RF} at the input of the time varying conductance to create the required converted current at IF

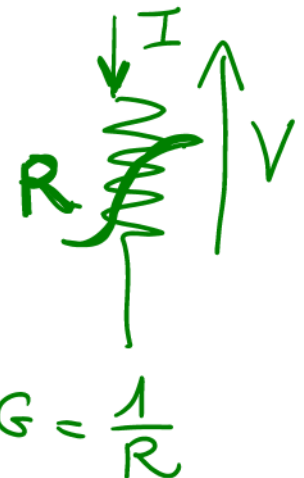
$$I_{\text{S}}(t) = G(t) \cdot V_{\text{RF}}(t)$$

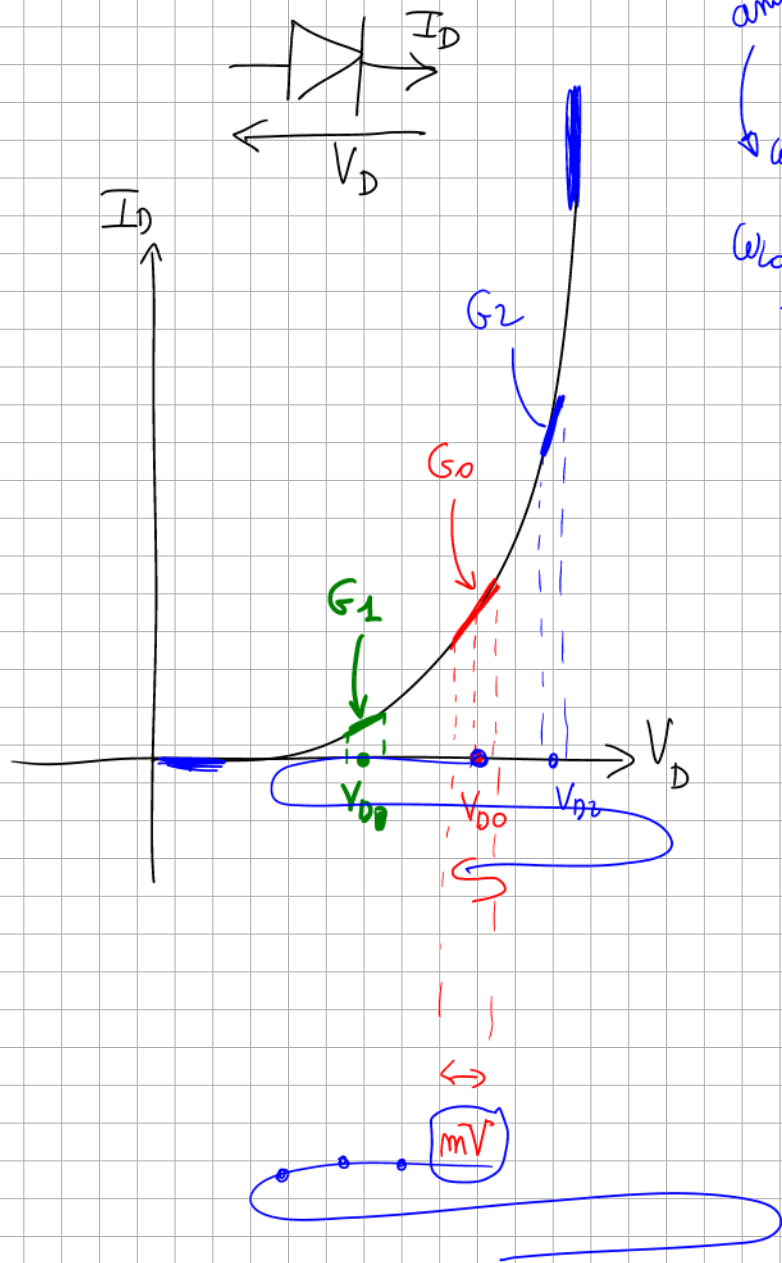
$$\begin{array}{ccc} \uparrow & \uparrow & \uparrow \\ \omega_{\text{IF}} & \omega_{\text{LO}} & \omega_{\text{RF}} \end{array}$$

Ohm law

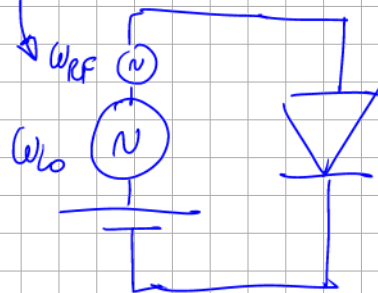
$$V = R \times I$$

$$I = G \times V$$

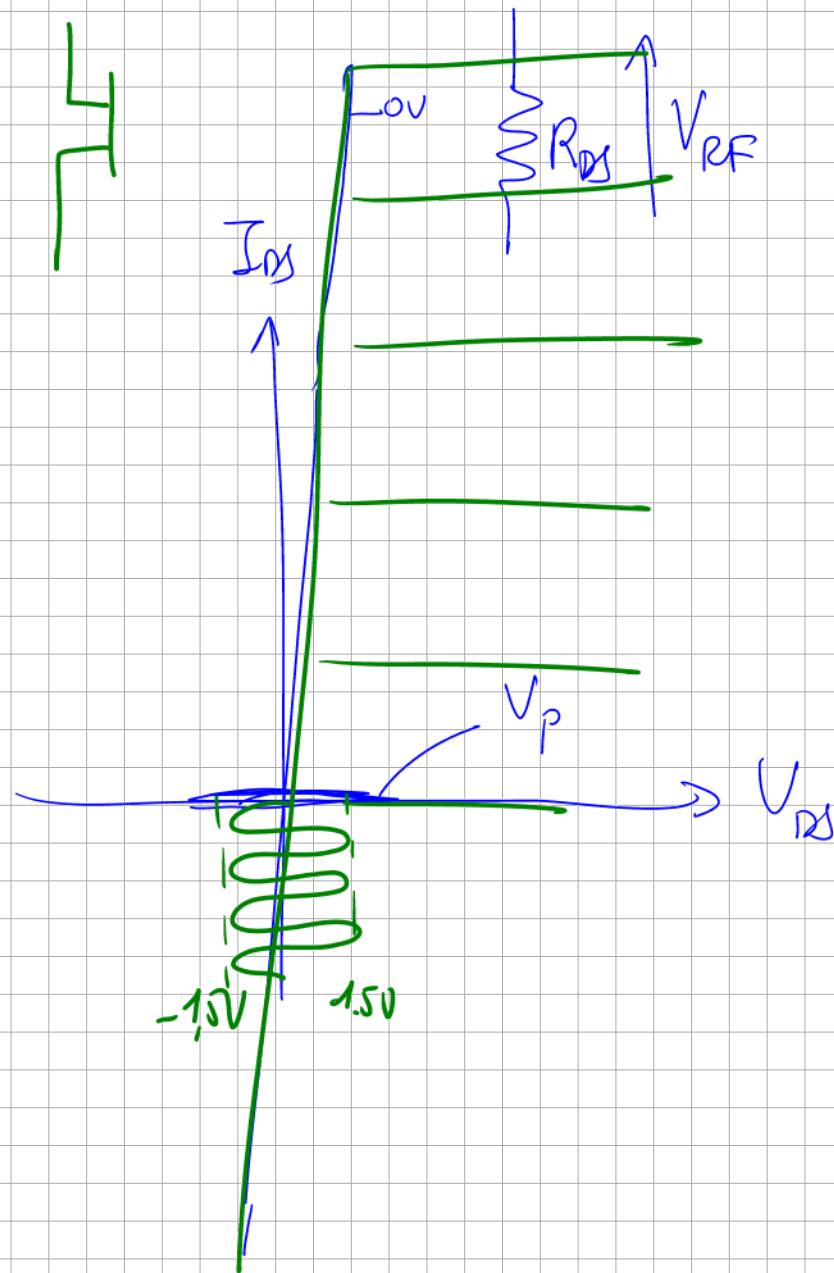




amplitude $\approx mV$
 $\approx 99V$



L_0



Principle of Diode Mixers

Minimum of conversion losses for passive mixers

◆ Ideal case of a diode mixer :

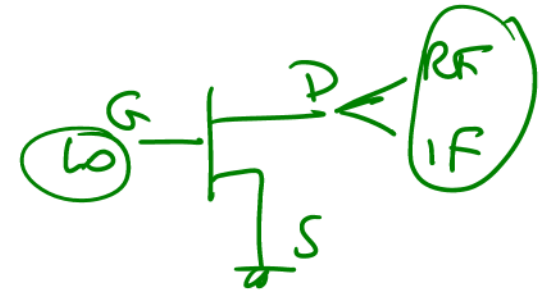
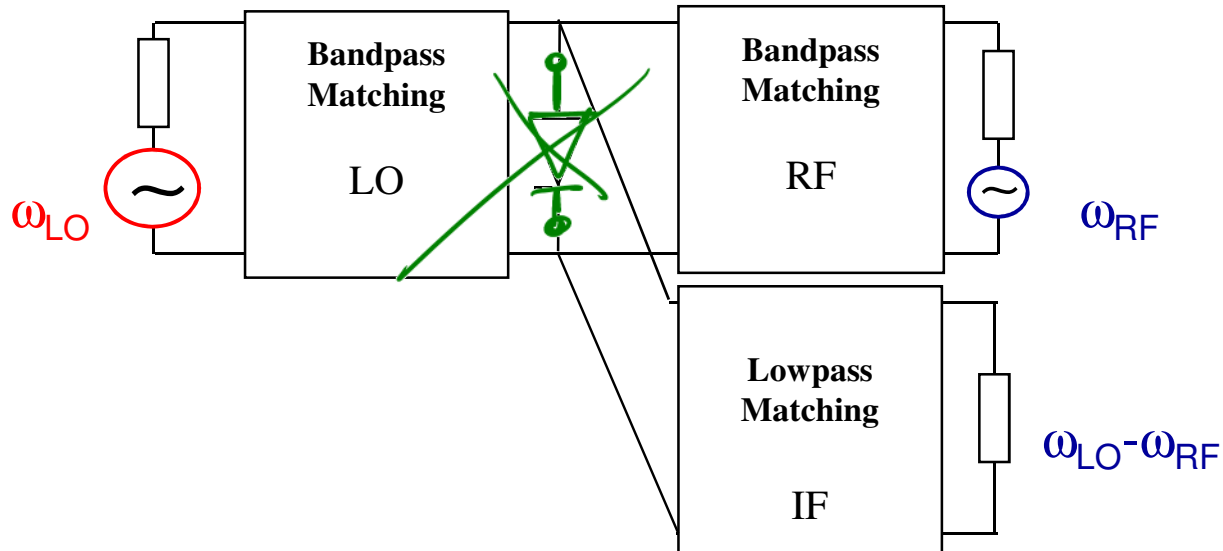
$$\rightarrow L_{C_{\text{MIN}}} \cong 4 \text{ dB}$$

◆ Actual case :

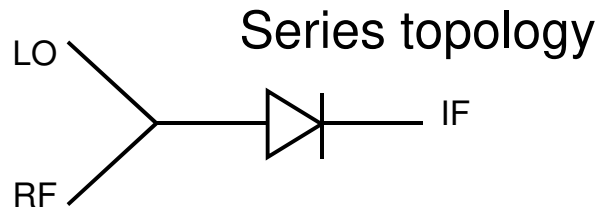
$$L_{C_{\text{MIN}}} \approx 5 \text{ à } 6 \text{ dB}$$

$$L_C = f \left[I-V_{\text{NL}}, V_{\text{LO}}(t), Z_L(\omega_{\text{SPURS}}) \right]$$

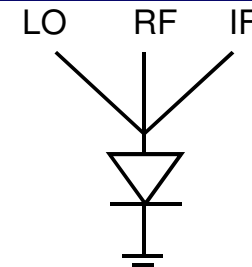
Diode-based SEM



Critical matching constraints for single diode mixers



Parallel topology



Advantages:

- Low L_c and P_{LO}

Disadvantages:

- No suppression of spurs
- Bandwidth and isolation are greatly limited by critical filtering issues
- Filter losses increase $L_c \rightarrow$ Very few diode-based SEM

SBM and DBM diode-based mixers

SBM: Single Balanced Mixer= 2 SEM with couplers or baluns

DBM: Double Balanced Mixer = 2 SBM with couplers or baluns



Advantages

- Suppression of some spurs by out of phase recombination
- Good LO-RF Isolation and sometimes LO-IF isolation without filtering issues

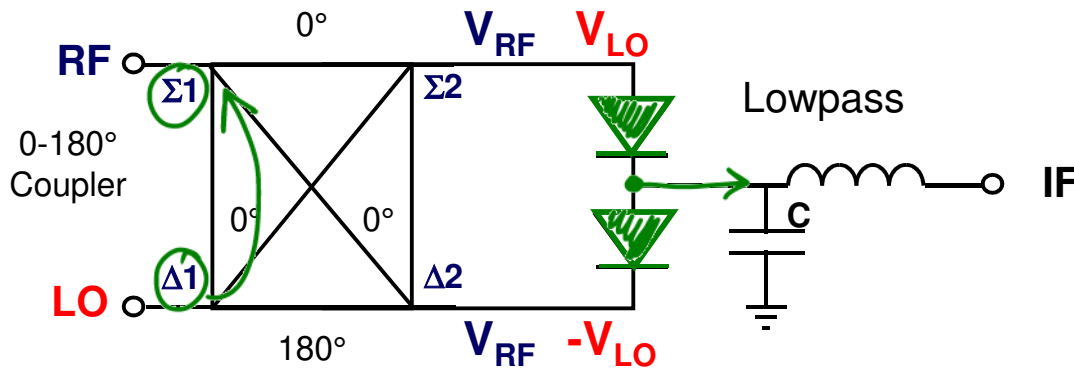
Disadvantages

- Requirement of higher levels of LO power



Example of a diode-based SBM (LO at Δ port)

Architecture with 180° coupler (LO @ Δ)



180° Coupler

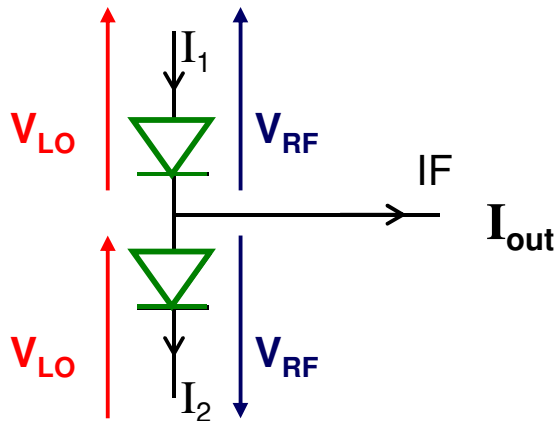
Input Matching @LO/RF \approx Input Matching@LO/RF of coupler

Isolation LO-RF \approx Isolation LO-RF of coupler

LO @ $\Sigma \rightarrow \text{mix}[\text{EH}(\text{LO}), *]$ rejected

LO @ $\Delta \rightarrow \text{mix}[*, \text{EH}(\text{RF})]$ rejected

Operation mode (LO @ Δ)



Diode current law

$$I(V) = \mathbf{F}(V) = a.V + b.V^2 + c.V^3 + \dots$$

$$I_1 = \mathbf{F}(V_{\text{LO}} + V_{\text{RF}})$$

$$I_2 = \mathbf{F}(V_{\text{LO}} - V_{\text{RF}})$$

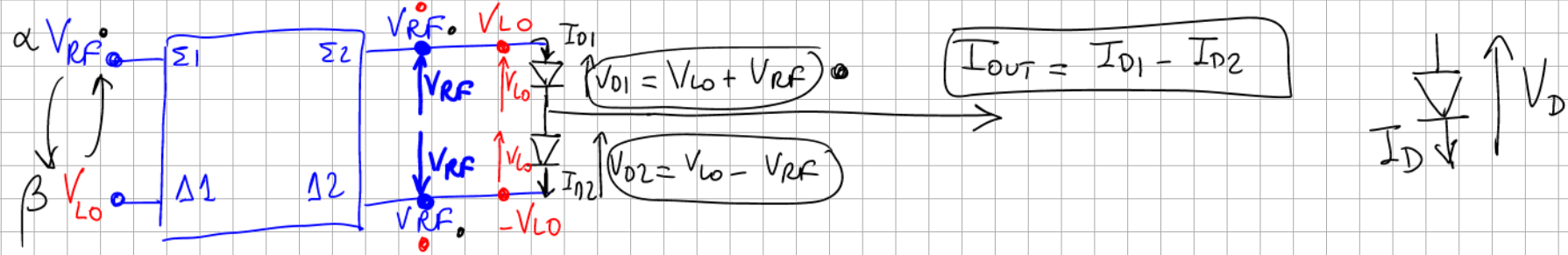
$$V_{\text{LO}}^p \cdot V_{\text{RF}}^q \rightarrow \pm p.\omega_{\text{LO}} \pm q.\omega_{\text{RF}}$$

$$I_{\text{OUT}} = I_1 - I_2 = 0 \Leftrightarrow \omega = \pm m.\omega_{\text{LO}} \pm n.\omega_{\text{RF}}$$

If **n is even** because $(V_{\text{RF}})^n = (-V_{\text{RF}})^n$

$\omega_{\text{LO}}, 2(\omega_{\text{LO}} - \omega_{\text{RF}}), \dots$ suppressed @ IF port \rightarrow (LO-IF Isolation) ...

$n=0$ $n=2$



~~$$I_D = \exp(V_D)$$~~

$$I_D = f_{NL}(V_D) = a V_D + b V_D^2 + c V_D^3 + \dots$$

$$\begin{cases} I_{D1} = a(V_{LO} + V_{RF}) + b(V_{LO} + V_{RF})^2 + c(V_{LO} + V_{RF})^3 \\ I_{D2} = a(\text{---}) + b(\text{---}) + c(\text{---}) \end{cases}$$

$$I_{OUT} = I_{D1} - I_{D2}$$

$$= 2aV_{RF} + 4bV_{LO}V_{RF} + 2cV_{RF}^3 + 6cV_{LO}^2V_{RF}$$

~~$$(a+b)^3 = a^3 + b^3 + 3a^2b + 3ab^2$$~~
~~$$(a-b)^3 = a^3 - b^3 - 3a^2b + 3ab^2$$~~

$$\omega_{RF}$$

spurs

$$\omega_{IF}$$

$$\omega_{\Sigma}$$

$$\omega_{RF}$$

$$3\omega_{RF}$$

$$\omega_{RF}$$

$$2\omega_{LO} + \omega_{RF}$$

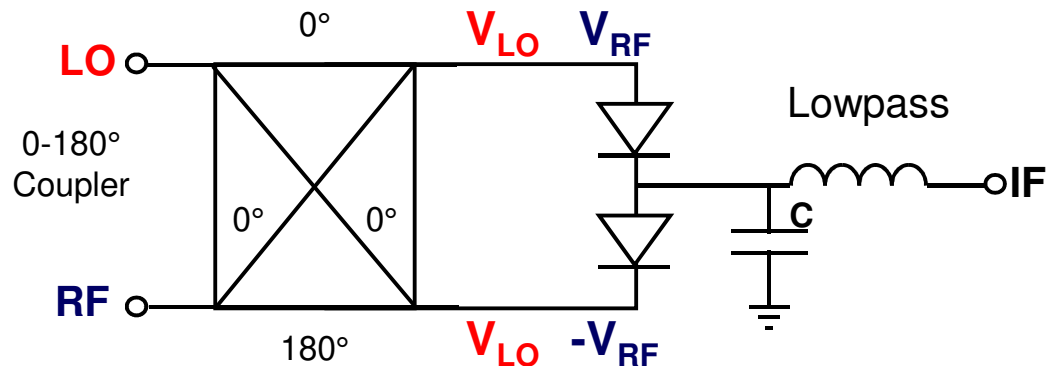
$$2\omega_{LO} - \omega_{RF}$$

$$ISO_{LO-RF} =$$

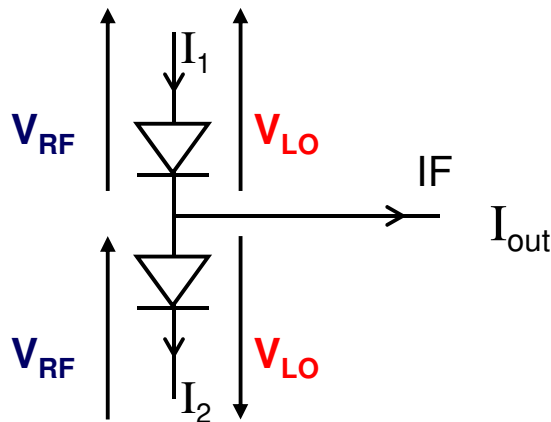
$$ISO_{LO-IF} = \infty$$

Diode-based SBM : LO and RF ports are reversed (LO at Σ port)

• Architecture with 180° coupler (LO @ Σ)



• If LO and RF are reversed



$$I(V) = F(V) = a.V + b.V^2 + c.V^3 + \dots$$

$$I_1 = F(V_{RF} + V_{LO})$$

$$I_2 = F(V_{RF} - V_{LO})$$

$$V_{LO}^p \cdot V_{RF}^q \rightarrow p\omega_{LO} \pm q\omega_{RF}$$

$$I_{OUT} = I_1 - I_2 = 0 \Leftrightarrow \omega = \pm m.\omega_{LO} \pm n.\omega_{RF}$$

$$\text{If } m \text{ is even because } (V_{LO})^m = (-V_{LO})^m$$



$\omega_{RF}, 2(\omega_{LO} - \omega_{RF}), \dots$ suppressed @IF port \rightarrow (RF-IF Isolation) ...

$m=0 \quad m=2$

Less critical