# Hardware Source Licence (HSL) Version 1.0



## **Summary:**

This Licence, "Hardware Source Licence" or "HSL" is adapted to apply to hardware, including physical components, designs, schematics, and related documentation. **This "HSL" allows free use for research, development, and private purposes, while commercial usage is subject to negotiation and payment.** The terms of this licence shall transition to the Open Source Hardware following the Effective Change Date as specified herein. By default the Change Licence to be applied will be the CERN Open Source Hardware Licence Strongly Reciprocal (OHL SR).

## **HSL Licence Parameters:**

- Licensed Work ("Product"): "Casing ASMV"
- Date of Publication of Licensed Work: "The GitHub Date of the latest updated file of the current Branch You are in."
- Change Licence: "Open Hardware Licence Strongly Reciprocal latest version 2"available from CERN on: <a href="https://ohwr.org/cern\_ohl\_s\_v2.pdf">https://ohwr.org/cern\_ohl\_s\_v2.pdf</a> and alternate link <a href="https://cern-ohl\_web\_cern\_ch/">https://cern-ohl\_web\_cern\_ch/</a>
- Change Date: "Four years from the date the Licensed Work is published"
- Additional Use Grant: None
- Licensor: "M-RAL Association"
- Licensor Contacts: "https://github.com/M-RAL & MermozRAL.Association@gmail.com"

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## 1. Definitions

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- Make: means to create or configure something, whether by manufacture, assembly, compiling, loading or applying or using or derivating the Licensed Work to create a hardware (Product).
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