

Features

- ☐ Triple wavelength 405nm, 650nm and 780nm
- ☐ 120MHz bandwidth high speed RF-Channels
- ☐ 8 selectable gain modes, sleep mode
- ☐ Tristate inputs for gain mode selection
- ☐ Two PD-pattern design
- ☐ Low noise design
- ☐ Integrated test mode for easy pick-up adjustment
- ☐ Small-size Ceramic glass-lid package
20-pin 4.5mm x 4.0mm x 1.25mm
- ☐ Integrated 49Bit OTP for parameter trimming
- ☐ High impedance outputs in sleep mode
- ☐ On chip supply blocking



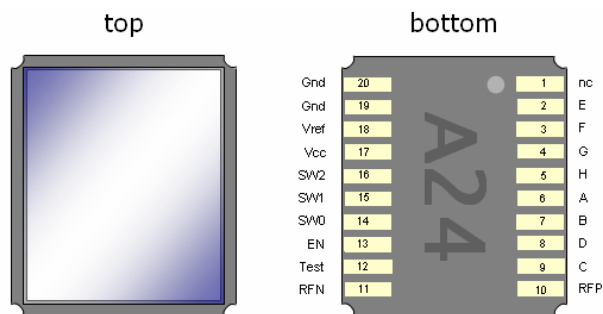
Ordering Information

Part No.	Temperature Code	Package Code	Option code
MLX75012	(-25 °C to 85 °C)	20L CSP Opto Ceramic	

Application Examples

- ☐ HD-DVD or Blu-Ray® applications read/write
- ☐ High-Speed DVD read/write
- ☐ CD read/write
- ☐ writable data storage optical devices

Pin Description



General Description

The MLX75012 is a triple wavelength photo detector IC (PDIC) with integrated amplifiers and control circuitry for use in optical pick-up heads. Its 20 photo detectors are optimized for the detection of 405nm, 650nm, and 780nm wavelength laser light used in HD-DVD, Blu-Ray®, DVD, and CD applications and are arranged in two 3-beam photo detector arrays in the chip centre. The ten signal channels consist of four main-detector channels (A, B, C, and D), four sub-detector channels (E, F, G, H), and two channels with balanced differential output (RFP, RFN). The device features a test mode for PDIC adjustment, a sleep mode, and 8 selectable gain modes, which are controllable by digital- and tri-state logic. The integrated 49Bit one-time programmable ROM allows parameter trimming of the MLX75012 for high product quality. The MLX75012 is manufactured in a 0.6µm BiCMOS-technology.

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1 Functional Block Diagram

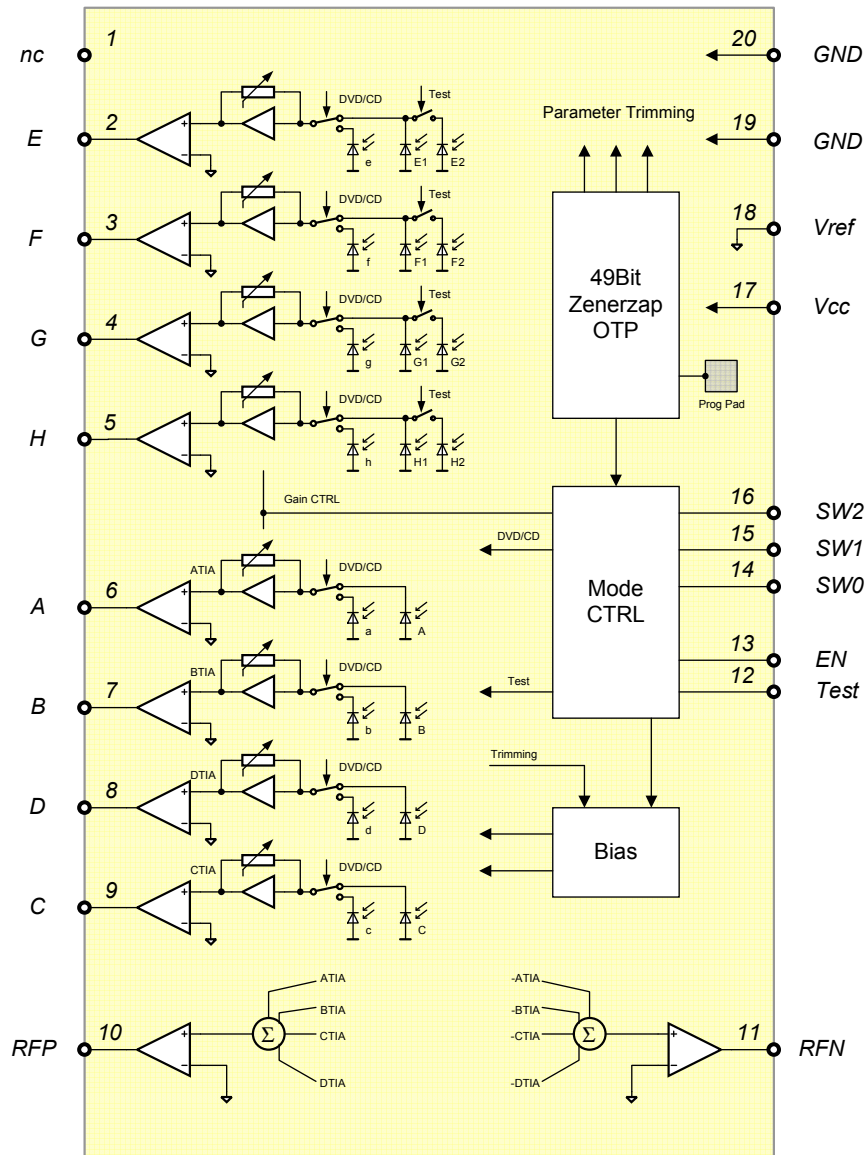


Fig. 1: Functional Block Diagram of MLX75012

2 Glossary of Terms

PDIC	Photo detector IC
OPU	Optical Pickup-Unit
PUH	Pick-up Head
OTP	One-time programmable

3 Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units
Supply Voltage (overvoltage)	V_{CC}	-0.3	6	V
Supply Voltage (operating)	V_{CC}	4.5	5.5	V
Output Voltage	V_{out}	-0.3	$V_{DD} + 0.3$	V
Output Current	I_{out}	-5	5	mA
Input Voltage	V_{in}	-0.3	$V_{DD} + 0.3$	V
Operating Temperature Range	T_A	-25	85	°C
Storage Temperature Range	T_S	-40	100	°C
ESD Sensitivity (AEC Q100 002)			4	kV
Power Consumption	P_{tot}		250	mW

Table 1: Absolute maximum ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

4 Pin Definitions and Descriptions

Pin №	Name	Type	Function
1	-	-	n.c.
2	E	Output	E1+E2 for HD/DVD-Mode / e for CD-Mode
3	F	Output	F1+F2 for HD/DVD-Mode / f for CD-Mode
4	G	Output	G1+G2 for HD/DVD-Mode / g for CD-Mode
5	H	Output	H1+H2 for HD/DVD-Mode / h for CD-Mode
6	A	Output	A for HD/DVD-Mode / a for CD-Mode
7	B	Output	B for HD/DVD-Mode / b for CD-Mode
8	D	Output	D for HD/DVD-Mode / d for CD-Mode
9	C	Output	C for HD/DVD-Mode / c for CD-Mode
10	RF+	Output	Pos(A+B+C+D)
11	RF-	Output	Neg(A+B+C+D)
12	TEST	Digital-IN	Adjustment Mode
13	EN	Digital-IN	Sleep Mode
14	SW0	Tristate-IN	Operating Mode Selection
15	SW1	Tristate-IN	Gain Mode Selection
16	SW2	Tristate-IN	Gain Mode Selection
17	Vcc	Supply	Power Supply Pin (4.5V to 5.5V)
18	Vref	Ref-IN	Voltage Reference (1.6V to Vcc-2.0V)
19	GND	Supply	Analog Ground
20	GND	Supply	Digital Ground

Table 2: Pin definitions and descriptions

Pin №	Name	Type	Functional Schematic
12	TEST	Digital-IN	
13	EN	Digital-IN	
14	SW0	Tristate-IN	
15	SW1	Tristate-IN	
16	SW2	Tristate-IN	

Table 3: Functional schematics of digital inputs

5 General Electrical Specifications

5.1 Normal Operating Conditions

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Supply voltage range	V_{CC}		4.5		5.5	V
Operation temperature	ϑ_{amb}		-25		85	°C
Input light Power main-channels ¹	I_{Lpdata}				1000	μW
Input light Power sub-channels ²	$I_{Lptrack}$				1000	μW
Tristate High select pin Voltage	$V_{selectH}$		3.1		V_{CC}	V
Tristate Middle select pin Voltage	$V_{selectM}$		1.3		2.6	V
Tristate Low select pin Voltage	$V_{selectL}$		0		0.8	V
Digital pin High Voltage	V_{High}		$V_{CC}-1.5$		V_{CC}	V
Digital pin Low Voltage	V_{Low}		0		1.5	V
Channel Output Resistive Load	R_L			10		kΩ
Channel Output Capacitive Load	C_L		10	20	50	pF
VREF voltage range	V_{REF}		1.6	2.0	$V_{CC}-2$	V

Table 4: Normal operating conditions

¹ Maximum input light power per channel

² Maximum input light power per channel

5.2 DC Characteristics HD/DVD

All parameter values at $\vartheta_{amb} = 25\text{ }^{\circ}\text{C}$, $V_{CC}=5\text{V}$, $V_{REF}=2.0\text{V}$, $R_L=10\text{k}\Omega$, $C_L=20\text{pF}$, $\lambda=405\text{nm}$ (HD) or $\lambda=650\text{nm}$ (DVD), unless otherwise specified;

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Current consumption	I_{cc}	shade		38	44	mA
Sleep Mode Current Consumption	$I_{cc\text{ sleep}}$	for EN=Low			35	μA
		for SWx=Low/Low/Low			110	μA
Output Offset Voltage (A...D) HD/DVD	$V_{off\text{ A,B,C,D}}$	Shade, BaseVoltage= V_{REF}	-25	0	+25	mV
Output Offset Voltage (E...H) HD/DVD	$V_{off\text{ E,F,G,H}}$	Shade, BaseVoltage= V_{REF}	-25	0	+25	mV
Output Offset Voltage (RF+, RF-)	$V_{off\text{ RF+,RF-}}$	Shade, BaseVoltage= V_{REF}	-40	0	+40	mV
Output Offset Voltage, calculated values, HD/DVD	ΔV_{off}	A-B, Shade	-35	0	+35	mV
		C-D, Shade	-35	0	+35	mV
		(A+B)-(C+D), Shade	-35	0	+35	mV
		(A+C)-(B+D), Shade	-35	0	+35	mV
		A+B+C+D, Shade	-60	0	+60	mV
		(E1+E2)-(H1+H2), Shade	-35	0	+35	mV
		(G1+G2)-(F1+F2), Shade	-35	0	+35	mV
		(E1+E2+F1+F2)-(G1+G2+H1+H2), Shade	-40	0	+40	mV
Offsetdrift	$\Delta V_{off} / \Delta \vartheta$	A,B,C,D,E,F,G,H			100	$\mu\text{V} / \text{K}$
Gain Variation (A,B,C,D)	$V_{out\text{ A,B,C,D}}$		-15	0	+15	%
Gain Variation (E,F,G,H)	$V_{out\text{ E,F,G,H}}$		-15	0	+15	%
Gain Variation (RF+,RF-)	$V_{out\text{ RF+,RF-}}$		-15	0	+15	%
Max. Output Voltage	V_{outmax}	(A...D, E...H, RF+),	4			V
Min. Output Voltage	V_{outmin}	(RF-), $P_o = 500\mu\text{W}$			1.0	V
Gaindrift	ΔGain				0.1	% / K
Linearity	$\Delta\text{Gain} / \Delta I_{light}$	all gain modes, all channels (0- 90% output swing)			8	%

Table 5: DC characteristics HD/DVD mode

5.3 DC Characteristics CD

All parameter values at $\vartheta_{amb} = 25\text{ }^{\circ}\text{C}$, $V_{CC}=5\text{V}$, $V_{REF}=2.0\text{V}$, $R_L=10\text{k}\Omega$, $C_L=20\text{pF}$, $\lambda=780\text{nm}$ (CD), unless otherwise specified;

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Current consumption	I_{cc}	shade		38	44	mA
Sleep Mode Current Consumption	$I_{cc\text{ sleep}}$	for EN=Low			35	μA
		for SWx=Low/Low/Low			110	μA
Output Offset Voltage (A...D) CD mode	$V_{off\text{ A,B,C,D}}$	Shade, BaseVoltage= V_{REF}	-50	0	+50	mV
Output Offset Voltage (E...H) CD mode	$V_{off\text{ E,F,G,H}}$	Shade, BaseVoltage= V_{REF}	-50	0	+50	mV
Output Offset Voltage (RF+, RF-) CD	$V_{off\text{ RF+,RF-}}$	Shade, BaseVoltage= V_{REF}	-60	0	+60	mV
Output Offset Voltage, calculated values, CD mode	ΔV_{off}	A-B, Shade	-50	0	+50	mV
		C-D, Shade	-50	0	+50	mV
		(A+B)-(C+D), Shade	-60	0	+60	mV
		(A+C)-(B+D), Shade	-60	0	+60	mV
		A+B+C+D, Shade	-100	0	+100	mV
		(E1+E2)-(H1+H2), Shade	-60	0	+60	mV
		(G1+G2)-(F1+F2), Shade	-60	0	+60	mV
		(E1+E2+F1+F2)-(G1+G2+H1+H2), Shade	-80	0	+80	mV
Offsetdrift	$\Delta V_{off} / \Delta \vartheta$	A,B,C,D,E,F,G,H			100	$\mu\text{V} / \text{K}$
Gain Variation (A,B,C,D)	$V_{out\text{ A,B,C,D}}$		-15	0	+15	%
Gain Variation (E,F,G,H)	$V_{out\text{ E,F,G,H}}$		-15	0	+15	%
Gain Variation (RF+,RF-)	$V_{out\text{ RF+,RF-}}$		-15	0	+15	%
Max. Output Voltage	V_{outmax}	(A...D, E...H, RF+),	4			V
Min. Output Voltage	V_{outmin}	(RF-), $P_o = 500\mu\text{W}$			1.0	V
Gaindrift	ΔGain				0.1	% / K
Linearity	$\Delta\text{Gain} / \Delta I_{light}$	all gain modes, all channels (0- 90% output swing)			8	%

Table 6: DC characteristics CD mode

5.4 AC Characteristics HD/DVD

All parameter values at $\vartheta_{amb} = 25\text{ }^{\circ}\text{C}$, $V_{CC}=5\text{V}$, $V_{REF}=2.0\text{V}$, $R_L=10\text{k}\Omega$, $C_L=20\text{pF}$, $\lambda=405\text{nm}$ (HD) or $\lambda=650\text{nm}$ (DVD), unless otherwise specified;

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Group Delay (A~D, RF+/-)	ΔG_d	100kHz~bandwidth limit			2	ns
RF Settling Time (write)	$T_{RFset\ write}$	Output-step1.5V			10	
RF Settling Time (read)	$T_{RFset\ read}$	Output-step1.5V			20	
A,B,C,D,E,F,G,H Settling Time (write)	$T_{set\ write}$	Output-step1.5V			15	
A,B,C,D,E,F,G,H Settling Time (read)	$T_{set\ read}$	Output-step1.5V			25	
Slew Rate (RF)	$SR_{RF+,RF-}$	Output-step 1V, Gain Mode (1...32) Gain Mode 64 Gain Mode 128	200 175 150			V/us
Slew Rate (A~D)	$SR_{A,B,C,D}$	Output-step 1V, Gain Mode (1...32) Gain Mode 64 Gain Mode 128	150 110 75			V/us
Slew Rate (E~H)	$SR_{E,F,G,H}$	Output-step 1V, Gain Mode (1...32) Gain Mode 64 Gain Mode 128	150 110 75			V/us
Noise Level (A~D)	$V_{nA,B,C,D}$	RBW=30kHz, 1...65MHz Highest Gain			-78	dBm
Noise Level (E~H)	$V_{nE,F,G,H}$	RBW=30kHz, 1...65MHz Highest Gain			-73	dBm
Noise Level (RF+/-)	V_{nRF}	RBW=30kHz, 1...130MHz Highest Gain			-78	dBm
Peaking (A~D)	S_{peak}				1	dB
Power Supply Rejection Ratio	PSRR	<10kHz			-45	dB
Gain Switch Response Time	t_{switch}			10	50	us
Sleep-Mode Wake-up Time	$t_{wake-up}$				50	us

Table 7: AC characteristics HD/DVD

5.5 AC Characteristics CD

All parameter values at $\vartheta_{amb} = 25\text{ }^{\circ}\text{C}$, $V_{CC}=5\text{V}$, $V_{REF}=2.0\text{V}$, $R_L=10\text{k}\Omega$, $C_L=20\text{pF}$, $\lambda=780\text{nm}$ (CD), unless otherwise specified;

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Group Delay (A~D, RF+/-)	ΔG_d	100kHz~bandwidth limit			3	ns
RF Settling Time (write)	$T_{RFset\ write}$	Output-step1.5V			20	
RF Settling Time (read)	$T_{RFset\ read}$	Output-step1.5V			40	
A,B,C,D,E,F,G,H Settling Time (write)	$T_{set\ write}$	Output-step1.5V			25	
A,B,C,D,E,F,G,H Settling Time (read)	$T_{set\ read}$	Output-step1.5V			45	
Slew Rate (RF)	$SR_{RF+,RF-}$	Output-step 1V, Gain Mode (1...32) Gain Mode 64 Gain Mode 128	150 110 75			V/us
Slew Rate (A~D)	$SR_{A,B,C,D}$	Output-step 1V, Gain Mode (1...32) Gain Mode 64 Gain Mode 128	150 110 75			V/us
Slew Rate (E~H)	$SR_{E,F,G,H}$	Output-step 1V, Gain Mode (1...32) Gain Mode 64 Gain Mode 128	100 75 50			V/us
Noise Level (A~D)	$V_{nA,B,C,D}$	RBW=30kHz, 1...65MHz Highest Gain			-78	dBm
Noise Level (E~H)	$V_{nE,F,G,H}$	RBW=30kHz, 1...65MHz Highest Gain			-73	dBm
Noise Level (RF+/-)	V_{nRF}	RBW=30kHz, 1...130MHz Highest Gain			-78	dBm
Peaking (A~D)	S_{peak}				1	dB
Power Supply Rejection Ratio	PSRR	<10kHz			-45	dB
Gain Switch Response Time	t_{switch}			10	50	us
Sleep-Mode Wake-up Time	$t_{wake-up}$				50	us

Table 8: AC characteristics CD

5.6 Sensitivity and Bandwidth

HD-DVD / Blu-Ray® / DVD Mode (SW0 = H) Detector-Pattern 1														
Gain Mode			A, B, C, D				E, F, G, H				RFP / RFN			
No	SW1	SW2	Sens. [mV/uW]		BW [MHz]		Sens. [mV/uW]		BW [MHz]		Sens. [mV/uW]		BW [MHz]	
			405nm	650nm	min.	typ.	405nm	650nm	min.	Typ.	405nm	650nm	min.	typ.
1	L	M	0.23	0.45	80	100	0.56	1.125	60	80	0.15	0.3	110	120
2	L	M	0.45	0.9	80	100	1.125	2.25	60	80	0.3	0.6	110	120
4	M	L	0.9	1.8	80	100	2.25	4.5	60	80	0.6	1.2	110	120
8	M	M	1.8	3.6	80	100	4.5	9	60	80	1.2	2.4	110	120
16	M	H	3.6	7.2	80	100	9	18	60	80	2.4	4.8	110	120
32	H	L	7.2	14.4	80	100	18	36	45	60	4.8	9.6	90	110
64	H	M	14.4	28.8	65	80	36	72	30	50	9.6	19.2	70	90
128	H	H	28.8	57.6	45	55	72	144	20	35	19.2	38.4	45	55

Table 9: Sensitivity and bandwidth characteristics HD/DVD (405nm and 650nm),
Operating Point: 200mV(DC), 70mV(AC) Output Voltage

CD Mode (SW0 = M or L) Detector-Pattern 2										
Gain Mode			A, B, C, D			E, F, G, H			RFP / RFN	
No	SW1	SW2	Sens. [mV/uW]	BW [MHz]		Sens. [mV/uW]	BW [MHz]		Sens. [mV/uW]	BW [MHz]
			780nm	min.	typ.	780nm	min.	typ.	780nm	min. typ.
1	L	M	0.9	80	90	2.25	60	80	0.6	80 100
2	L	M	1.8	80	90	4.5	60	80	1.2	80 100
4	M	L	3.6	80	90	9	60	80	2.4	80 100
8	M	M	7.2	80	90	18	60	80	4.8	80 100
16	M	H	14.4	60	90	36	40	60	9.6	80 100
32	H	L	28.8	40	90	72	30	40	19.2	60 100
64	H	M	57.6	30	55	144	25	30	38.4	40 60
128	H	H	115.2	20	30	288	15	20	76.8	30 40

Table 10: Sensitivity and bandwidth characteristics CD (780nm)
Operating Point: 200mV(DC), 70mV(AC) Output Voltage

6 Outstanding Features

The MLX75012 opens the door to the new world of violet laser based storage-technology. By combining the traditional red and infrared applications in a high-speed-, low-power design a real universal device was created that can be used in any thinkable application ranging from HD-DVD recorders over gaming-equipment to the use in ultra-slim drives in new generation laptops. To allow small-size and cost-effective solutions in optical pick-up design, the MLX75012 offers two complete three-beam Detector patterns that can be used by dual-wavelength lasers. At the same time, the robust 20-pin CSP Ceramic Glass-Lid package offers small-size outer dimensions.

The system concept of the MLX75012 universal PDIC is based on newly developed blue enhanced detectors that ensure a high spectral sensitivity even before the first amplifier is involved. A 49-Bit one time programmable ROM guarantees high parameter stability by trimming at the device manufacturing process.

To support the adjustment process of pick-up systems in manufacturing, the MLX75012 offers a special test-mode in which one of the tracking detector-array can be switched off.

7 Performance Graphs

7.1 Bandwidth measured data

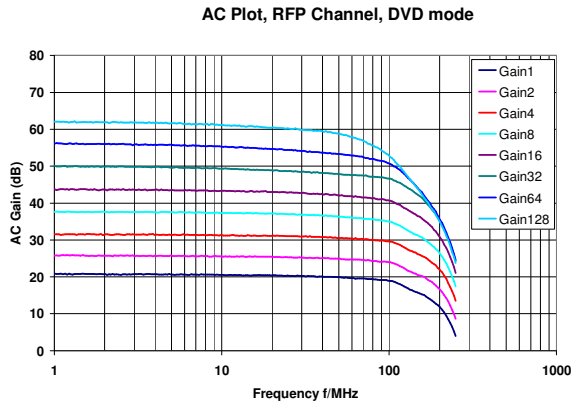


Fig. 2: RFP Channel AC Response, HD/DVD mode, $\lambda=405\text{nm}$

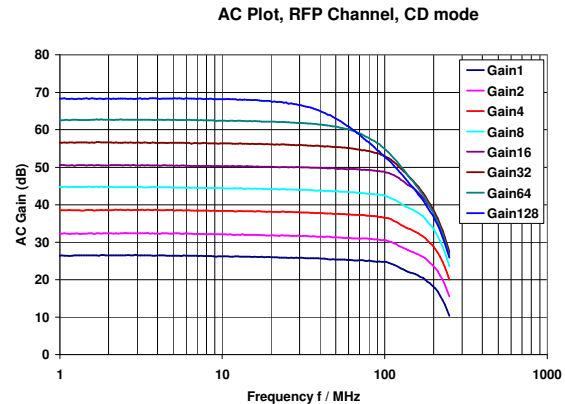


Fig. 3: RFP Channel AC Response, CD mode, $\lambda=780\text{nm}$

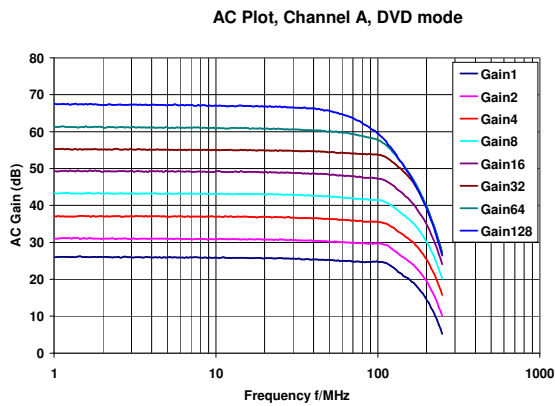


Fig. 4: AC Response, Channel A, HD/DVD mode, $\lambda=405\text{nm}$

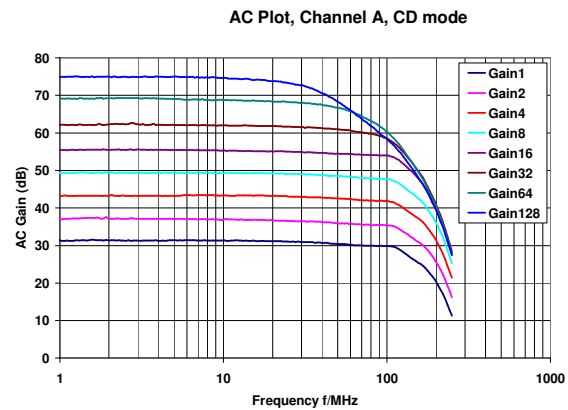


Fig. 5: AC Response, Channel A, CD mode, $\lambda=780\text{nm}$

7.2 Transient behavior

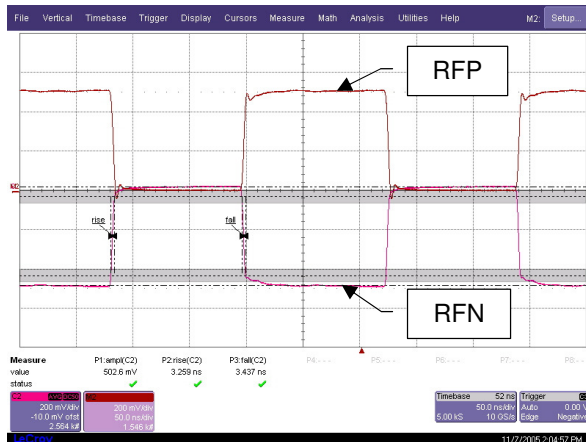


Fig. 6: RF Channels Transient Output Signal, Gain Mode=128, HD-DVD/Blu-Ray®/DVD Mode

7.3 Main-detector sensitivity mapping

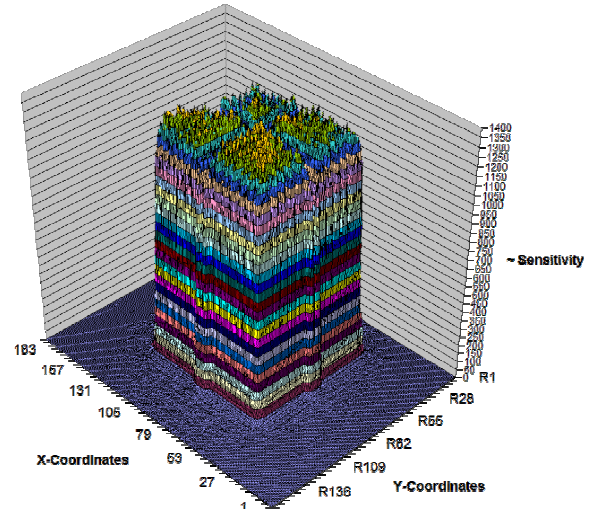


Fig. 7: Laser Scan of Central Main Detector, Pattern1 with 635nm Laser. HD-DVD/Blu-Ray®/DVD Mode

7.4 Noise measured data

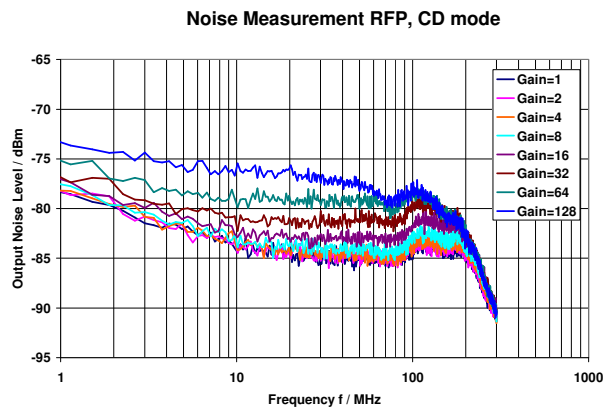


Fig. 8: RFP Channel Output Noise at 30kHz Bandwidth, CD mode

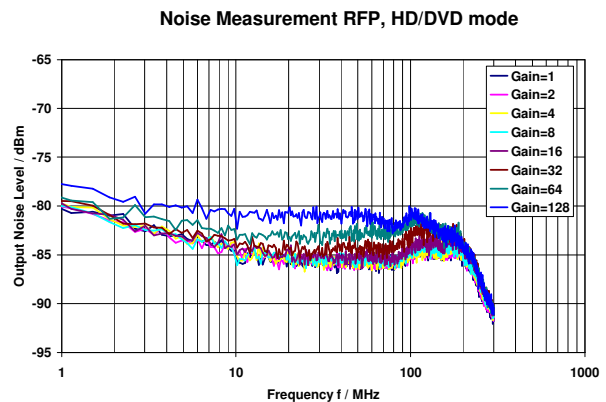


Fig. 9: RFP Channel Output Noise at 30kHz Bandwidth, HD-DVD/Blu-Ray®/DVD Mode

8 Standard information regarding manufacturability of Melexis products

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

Reflow Soldering SMD's (Surface Mount Device)s

- IPC/JEDEC J-STD-020
Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)
- EIA/JEDEC JESD22-A113
Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing (reflow profiles according to table 2)

Wave Soldering SMD's (Surface Mount Device)s and THD's (Through Hole Device)s

- EN60749-20
Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat
- EIA/JEDEC JESD22-B106 and EN60749-15
Resistance to soldering temperature for through-hole mounted devices

Iron Soldering THD's (Through Hole Device)s

- EN60749-15
Resistance to soldering temperature for through-hole mounted devices

Solderability SMD's (Surface Mount Device)s and THD's (Through Hole Device)s

- EIA/JEDEC JESD22-B102 and EN60749-21
Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

Melexis is contributing to global environmental conservation by promoting **lead free** solutions. For more information on qualifications of **RoHS** compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website:

<http://www.melexis.com/quality.asp>

9 ESD Precautions

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD). Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

10 Photo Diode Pattern

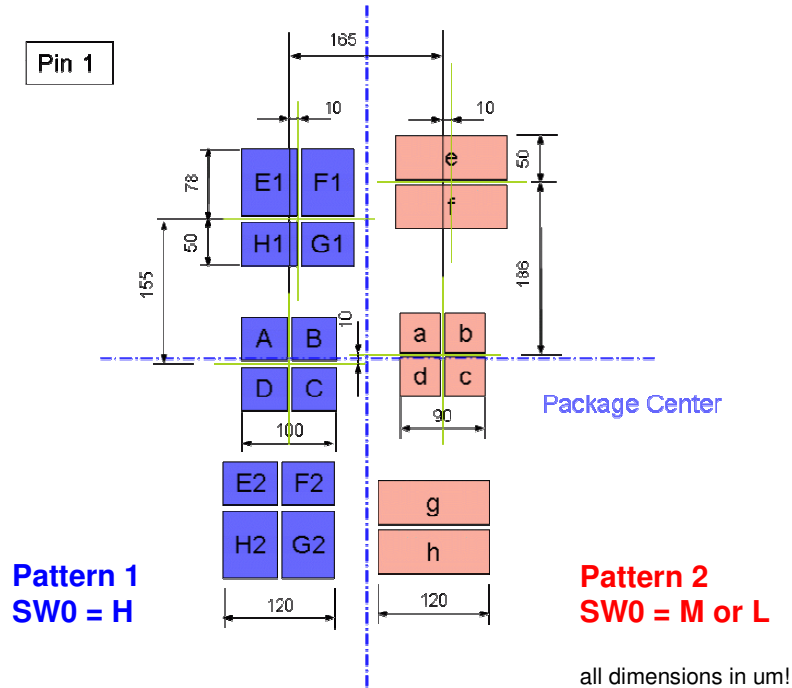


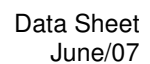
Fig. 10: The two Detector Patterns of MLX75012. Gap between detectors is 5 μm . The Patterns are switched by changing from CD- to DVD-Mode at pin 16 (SW0).

11 Package Information

Parameter	Unit	Typical Dimensions ³ for 20L CSP SMD Ceramic Package with Glass-Lid
Glass Type	Refractive Index	1.5300 @ 486nm 1.5204 @ 656nm
Glass x/y Dimesion	[μm/μm]	3500 / 4000
Glass Thickness	[μm]	300
Glass Type		Borosilicate Glass D263
Air gap above Detector	[μm]	300
No. of Pins		20
Package Height	[mm]	1.25 (incl. Glass)
Package Width	[mm]	4
Package Length	[mm]	4.5
Pin Pitch	[mm]	0.4
Pin Length	[mm]	0.7
Exposed Pad		no
Marking		yes, on bottom: 3digit Lot/Time Code
MSL		MSL-3
Pin 1 Marking		yes, Bottom (dot of fired tungsten) and Top (die-paddle)
Chip Position Tolerance x/y	[μm]	centered ±70
max Chip Rotation θ	[deg]	±2
Chip ARC		Yes

Table 11: Package Measures

³ For Tolerances please see the package drawing on page 17



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