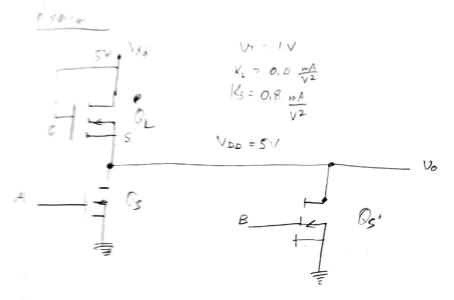
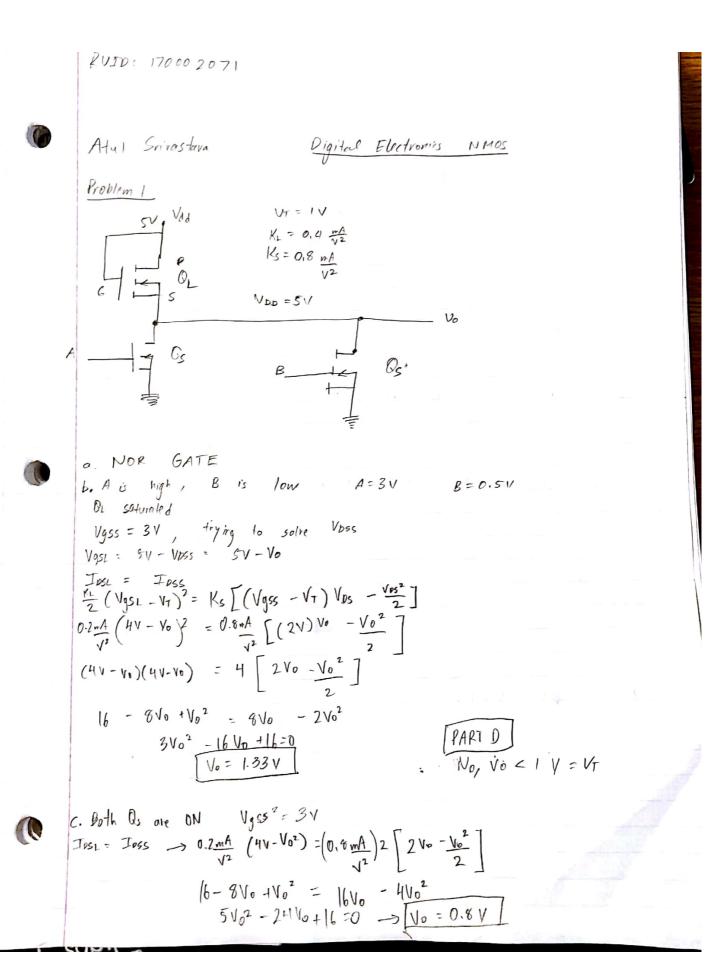
Digital Electronics NMOS

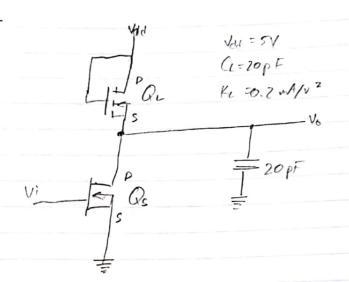


= NCR GATE

=
$$\frac{1}{4}$$
 | $\frac{1}{4}$ | $\frac{$







Ks = 1 mx

NT= 1 V

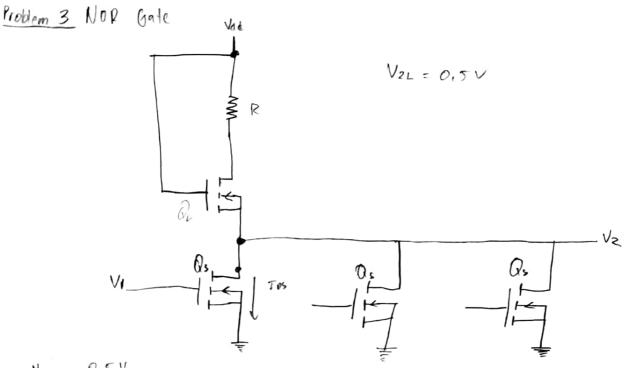
$$T_{DSS} = T_{DSL}$$

$$K_{S} \left[(V_{gSS} - V_{T}) V_{DS} - \frac{V_{DS}^{2}}{2} \right] = \frac{K_{L}}{2} (V_{gSL} - V_{T})^{2}$$

$$T_{SO} = \frac{V_{DS}^{2}}{2} = \frac{$$

$$(4-40)(4-40) = 10(340 - \frac{10^2}{2})$$

$$16 - 600 + 00^2 = 300 - 500^2$$

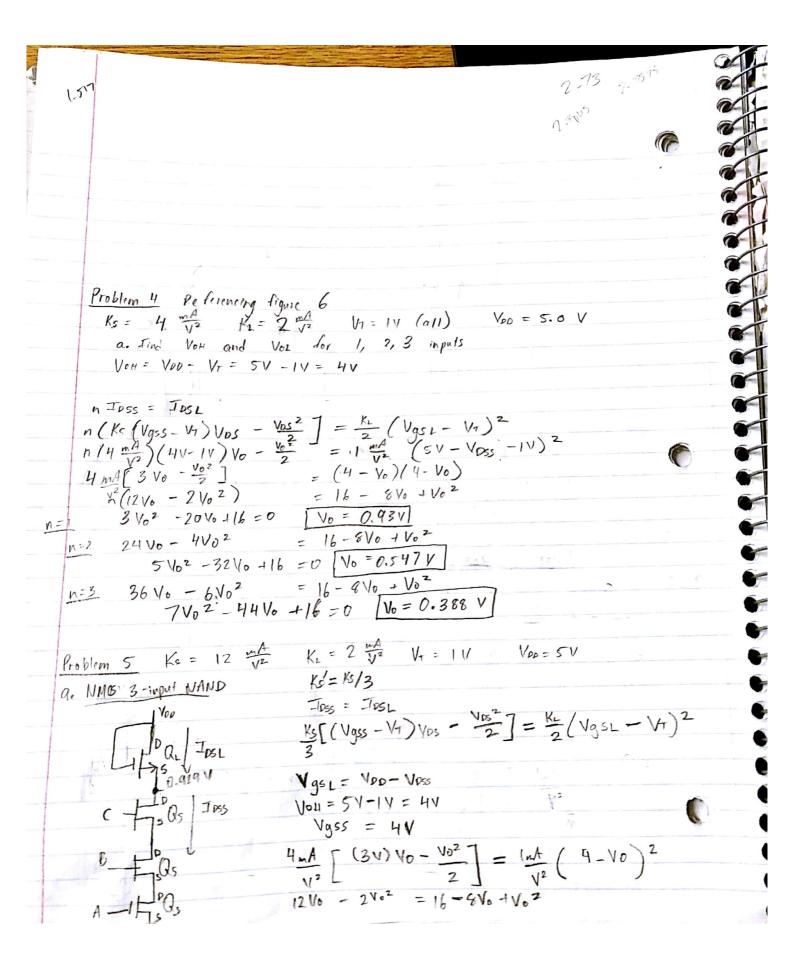


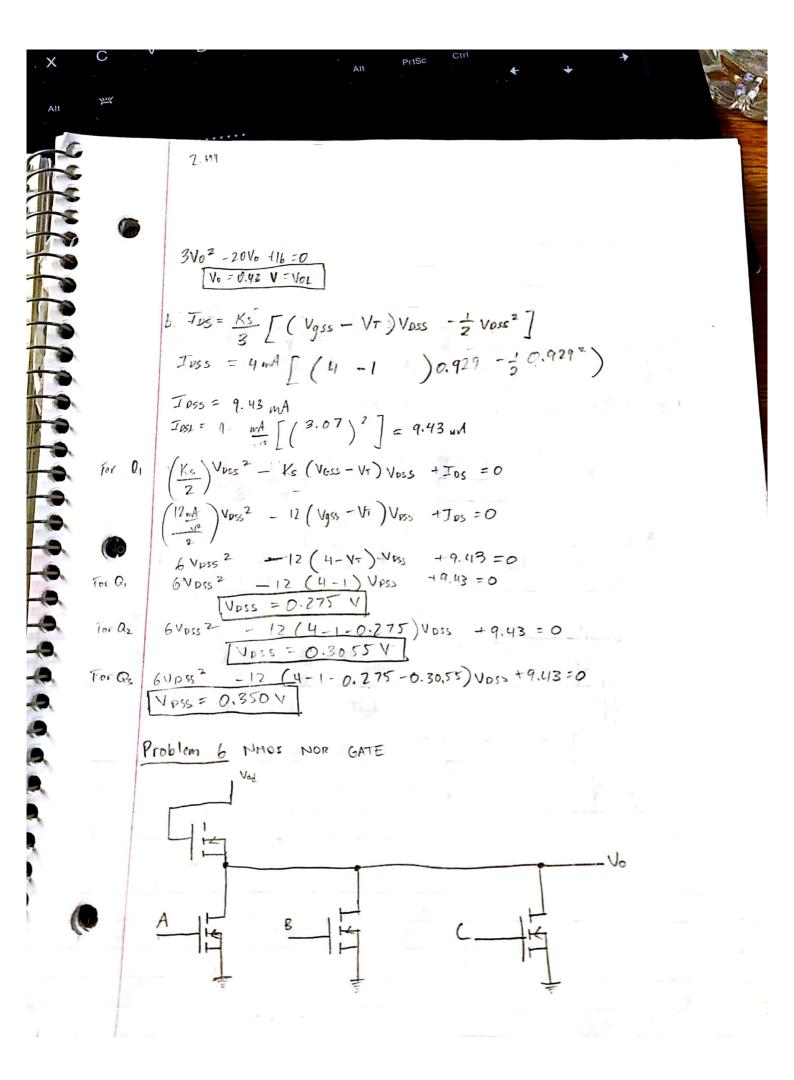
$$a. V_{DSS} = 0.5 V$$
 $V_{GS1} = V_{GL} - V_{SL} = 5V - 0.5V = 4.5V$
 $V_{US1} = V_{GS1} - V_{1} = 4.5V - 1V = 3.5V$
 $I_{DS1} = \frac{\kappa_{L}}{2} (V_{gS1} - V_{1})^{2}$
 $I_{DS1} = \frac{0.75 \mu A}{2 v_{2}} (B.5V)^{2} = 1.53 \mu A$

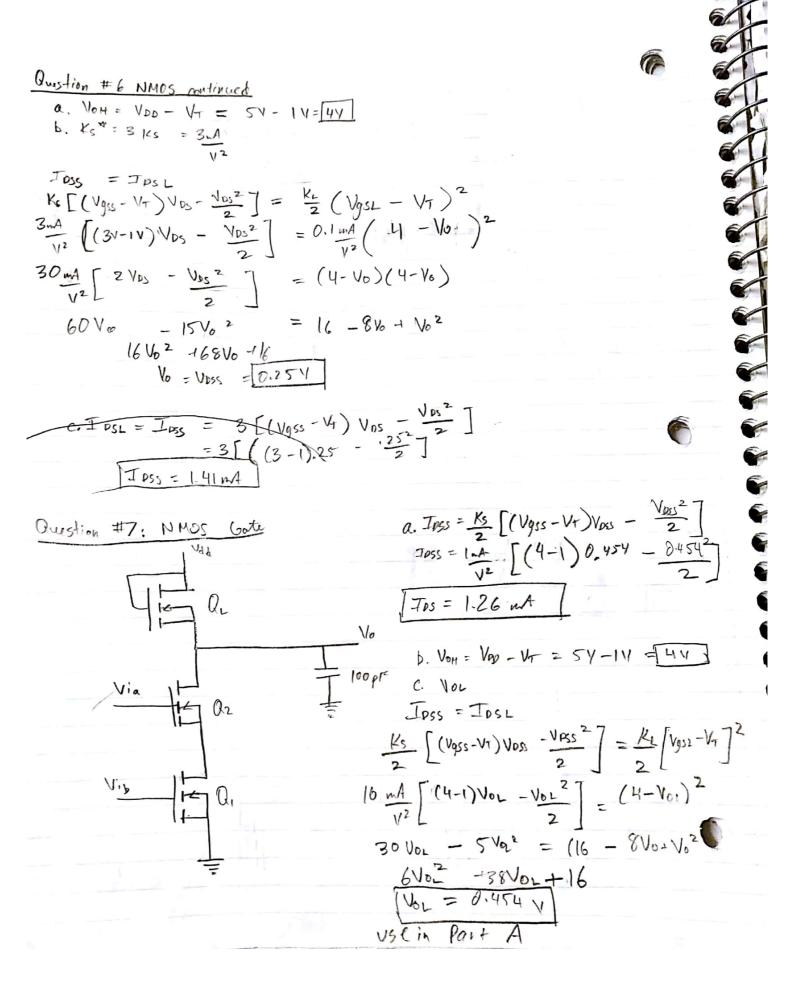
b.
$$I_{DSS} = K_S I (V_{QSS} - V_1) V_{DS} - \frac{V_{DS}^2}{2} I_{SSMA} = K_S I (3 V_1) 0.5 V_1 - \frac{0.5^2}{2} I_{SSMA} = K_S I (3 V_1) 0.5 V_2$$

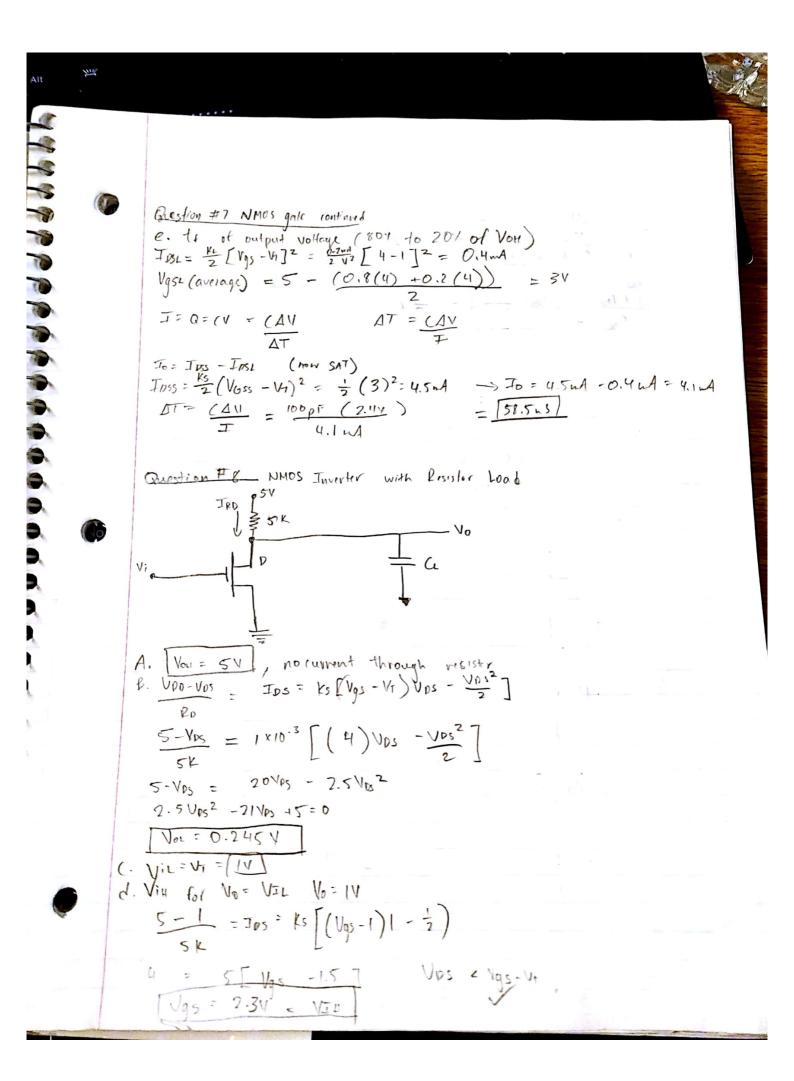
C. All 3 Qs ON,
$$K_{s}^{2} = 3K_{s}$$
 $\exists DSS = [K_{S}(V_{QSS} - V_{T})V_{DS} - \frac{V_{DS}^{2}}{2}]$
 $\exists DSS = V_{QSS} - V_{T} - [(V_{QSS} - V_{T})^{2} - 2\frac{z_{DS}}{K_{s}}]$
 $V_{DSS}^{2} = 3V - [(3V)^{2} - 2\frac{z_{DS}}{3.33}\frac{V_{s}}{V_{s}})]^{1/2}$
 $V_{DSS}^{2} = 0.157$
 $V_{DSL}^{2} = 5V - 0.157V - (1.53A)(654D) = [3.842Y]$
 $V_{DSR}^{2} = 5V - V_{S}^{2} - 0.157V = 3.843V$
 $V_{DSR}^{2} = 5V - V_{S}^{2} - 0.157V = 3.843V$
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 $V_{DSR}^{2} = 5V - V_{S}^{2} - 0.157V = 3.843V$

15 SAT









(1)
$$\frac{0}{1}$$
 $\frac{1}{1}$ $\frac{1}{1}$

$$V_{JL} = V_{IS} - V_{IL} = 1.55 \text{ y}$$

$$(f_{P}^{2} \mid V_{P})^{1/2}$$

$$V_{OH} = V_{DD} + V_{IL} \left[1 - \frac{V_{P}}{|V_{P}^{2}|} \right]^{1/2} \longrightarrow 3.32 \text{ V}$$

$$V_{SH} = \frac{V_{IS} - 2V_{IL}}{(3 V_{P})^{1/2}} \longrightarrow 2.55 \text{ V}$$

$$(3 V_{D})^{1/2}$$

$$V_{OI} = -\frac{V_{IL}}{(3 V_{P})^{1/2}} \longrightarrow 0.775 \text{ V}$$