NAME?

MILESTONE 1 WORK:

Monday, Oct 5

Met with team [2 hours]

- We decided to build a stack based processor. Designed format types with team.

Tuesday, Oct 6

Met with team [2 hours]

Discussed how to convert instructions to machine code.

M2 task assignment:
- 1st meeting: break instructions into small steps and move data from one register to another, determine single-cycle or multi-cycle
  Luke & Austin: RTL Description of each instruction\
  Jinhao & Yiju : A list of generic components specifications needed for RTL
- 2nd meeting: debug and test the processor through Xilinx ISE and fix existed problems

M2 entries?