

Atharva Kulkarni

☎ (639)-384-5812 | ✉ kulkarni.atharva@usask.ca | in /atharvapk | 🌐 ATGr8



EDUCATION

University of Saskatchewan

Saskatoon, SK

Bachelor of Engineering - Department of Electrical and Computer Engineering, Co-Op

Sept. 2020 – Present

- **Relevant Coursework:** Analog and Digital Electronics, Engineering Design, Digital Signal Processing, Magnetic Fields and Circuits, Object Oriented Programming, VLSI Design, FPGA programming
- **Awards:** Dean's Honor List, University of Saskatchewan Entrance Scholarship

WORK EXPERIENCE

Mine Electrical Engineering Intern

May 2023 – August 2023

Nutrien 

Cory, SK

- Designed, prototyped and developed Circuitry and PCBs for wireless communications in the Mine for creating a mesh of data, which would eliminate the need for humans to go in dangerous areas to collect data.
- Wrote Firmware and Software for node to node communications in the mesh, performed impedance matching for the antenna, analysis of power draw and RF propagation for data transfer in the Mines.
- Aided in cut over of PLCs from old to newer PLCs, basic exposure to SCADA systems through hands-on experimentation and troubleshoot sensors in the field.

Avionics Electrical Design Intern

January 2023 – April 2023

Reaction Dynamics 

Montreal, QC

- Designed flight hardware and wrote test software for the world's first attempt at an orbital rocket with a hybrid rocket motor.
- Designed and developed new iterations of multi-layered and mixed signal PCBs and circuitry for data acquisition, over voltage protection, engine control, and board interfacing to go on the rocket and ground support systems.
- Wrote embedded software, in C/C++, for implementing CRCs and testing flight electronics.

Embedded Software Developer

May 2022 – August 2022

University of Saskatchewan/RADSAT-SK 

Saskatoon, SK

- Wrote Embedded Software and designed Flight Hardware for Saskatchewan's first home-built satellite in space, RADSAT-SK, launched from the International Space Station (ISS) in 2023.
- Designed circuitry for overcharge/undercharge protection and remove before flight pin logic, and the PCB for a custom designed timer and inhibits board to prevent interference in the communications of the ISS.
- Implemented SPI communications on the OBC and reset functionality to the satellite's transceiver using C for better error management in case of component failure.

ENGINEERING PROJECTS

Rocket Electronics Stack

January 2023 – Present

U of S Space Design Team - Rocketry 

Saskatoon, SK

- Designed the first iteration of the U of S rocket's GPS and Power Distribution Board. Both boards used STM32 microcontrollers and a CAN bus architecture.
- Designed circuitry for power delivery, bus voltage and current sensing, microcontroller programming and integrating the GPS transceiver with the MCU and to an antenna through a SMA connector.

Electrical Power System (EPS)

August 2022 – December 2022

U of S Space Design Team 

Saskatoon, SK

- Designed an open-source EPS for satellite applications, procured manufacturing of PCBs and soldered SMD components.
- Designed the Battery Management System, Maximum Power Point Tracking Input from Solar Panels, Charging Circuitry and Voltage Buck Converters to meet requirements of various satellite missions.

Full Duplex Transceiver (RF Board)

September 2022 – December 2022

Personal Project 

Saskatoon, SK

- Designed a full Duplex UHF/VHF uplink and downlink transceiver for satellite applications, making it open source due to the alternatives being expensive.
- Designed the balun, filter and impedance matching network for the Si4460 RF IC.

Power and Electrical Team Lead

December 2021 – December 2022

RADSAT-SK Cube-Satellite Project ☞

Saskatoon, SK

- Found mission-critical faults in the design of a custom timer and inhibits board PCB, Managed a team of 4 in redesigning the custom PCB within 3 weeks, while also communicating with partner companies to design mission-critical hardware.
- Led a team of 5 to conduct Circuit Simulations and verification testing on the custom designed Timer PCB, using LTSpice.

Attitude Determination and Control Systems Team Lead

March 2021 – May 2022

RADSAT-SK Cube-Satellite Project ☞

Saskatoon, SK

- Led a team of 5 on conducting simulations and, using a MATLAB toolkit, for Attitude and altitude Determination of the CubeSat.
- Conducted tests on magnets and hysteresis rods of the satellite to check their serviceability by implementing Test Circuitry, using an Oscilloscope and Gauss meter.

Avionics Team Co-Lead

Sept. 2021 – January 2022

U of S Space Design Team - Rocketry ☞

Saskatoon, SK

- Programmed the Barometer and IMU sensors to give readings of altitude, pressure and orientation using C/C++ and Arduino.
- Integrated the Barometer, IMU sensors and temperature sensors with the Teensy 4.1 microcontroller.

President and Founder

Oct. 2020 – Oct. 2021

U of S Drone Team ☞

Saskatoon, SK

- Directly in charge of managing over 40 students of different disciplines.
- Using Python to program a drone's software to make the drone take multiple photos of previously selected areas on a field.

PUBLICATIONS

Radsat-SK Cube-Satellite Frame Design

June 2021

CSME 2021 Symposium

Charlottetown, PE

- B. Entz et al., "Radsat-SK Cube-Satellite Frame Design", Progress in Canadian Mechanical Engineering. Volume 4, Jun. 2021, <https://doi.org/10.32393/csme.2021.239> ☞

SUMMARY OF SKILLS

Programming Languages: Python, C/C++, Embedded C, MATLAB, VHDL, SystemVerilog, SystemC, HTML/CSS
Embedded Systems and Robotics: GDB Debugging, FreeRTOS, Various sensors and Microcontrollers, FPGAs, Kalman Filters

Electronics Testing and Design: PCB Design using KiCad and Altium Designer, Circuit Simulations using LTSpice, experience with soldering and reflowing, EMC Knowledge, Power Electronics Design

RF and Antenna Design: Experience with Impedance Matching, Network Analysis, RF Filters, Oscilloscopes, Inverted F Antennas and Spectrum Analyzers

Leadership and Communication: Conducting Meeting and Interviews, Leading a multi-disciplinary group of diverse people, Making Gantt Charts and Work Breakdown Structures, Writing Documentation and Technical Procedures

References available upon request