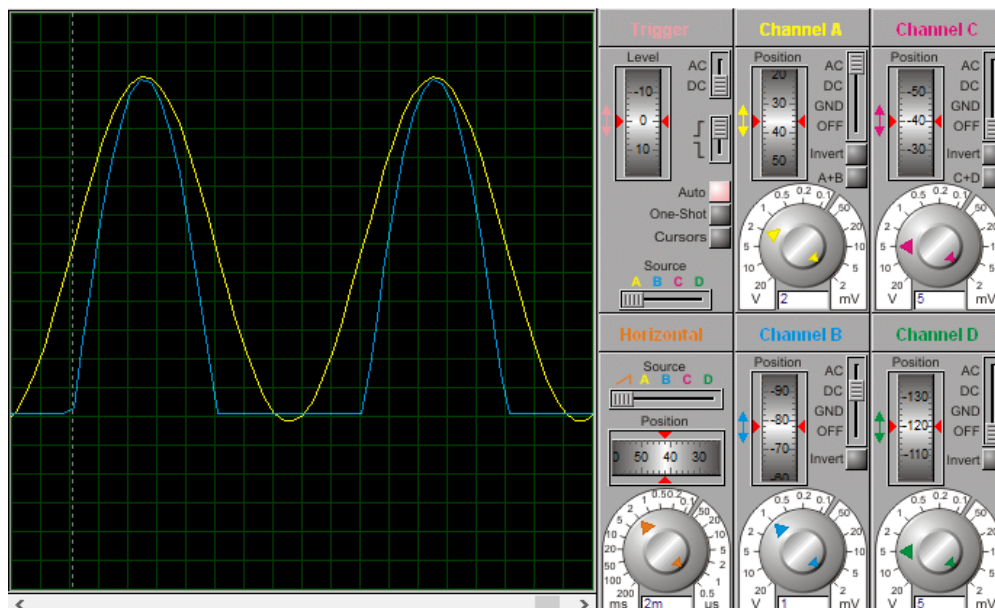
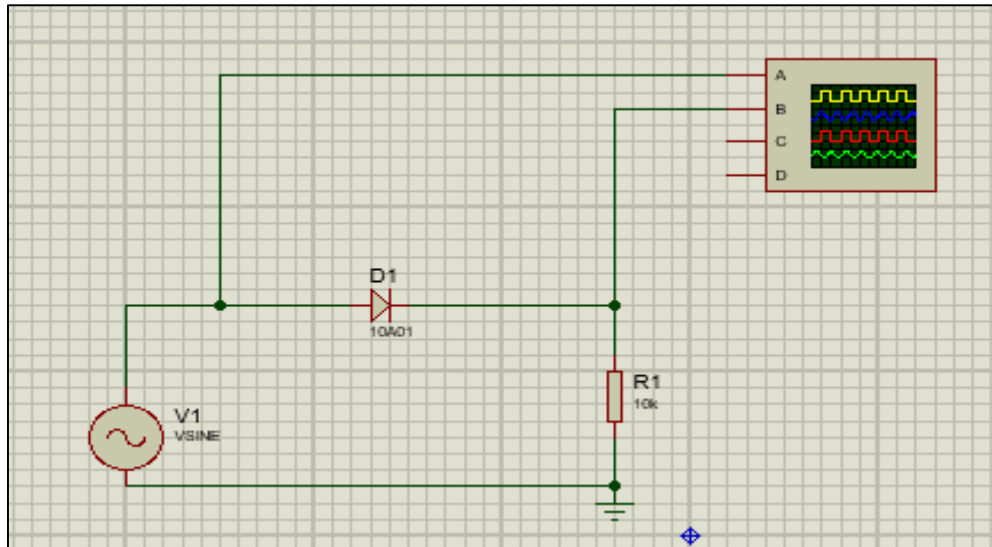


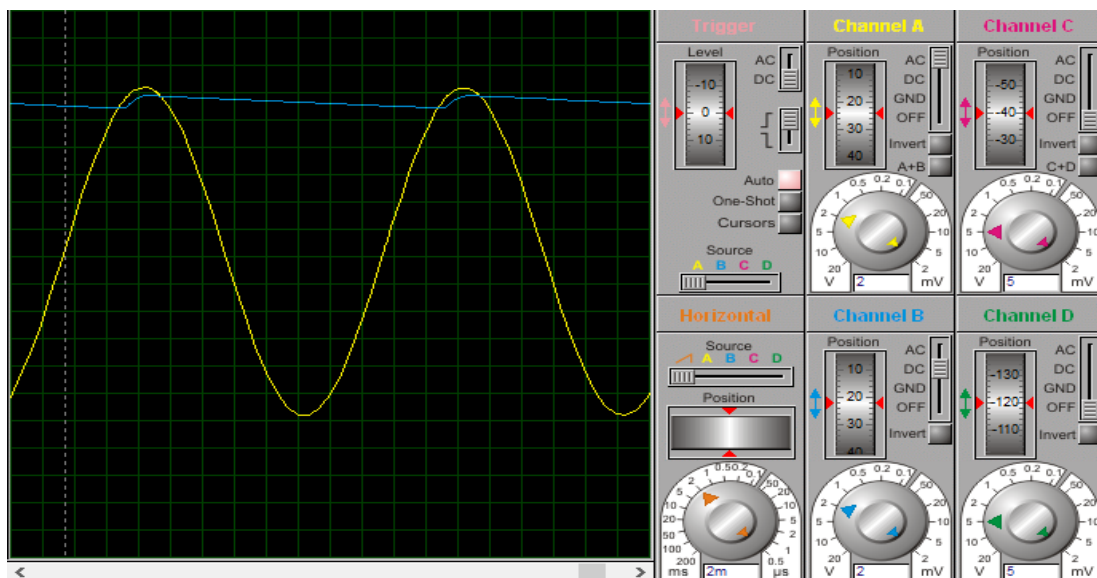
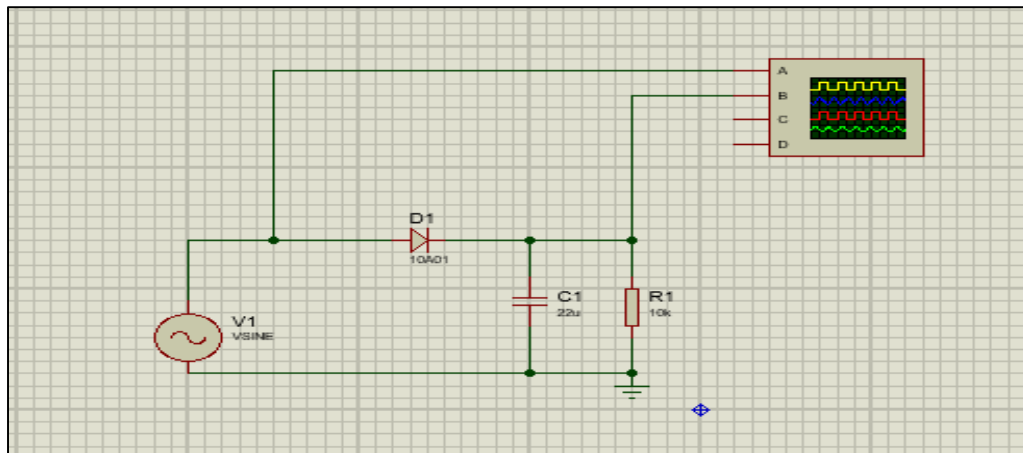
# TP ESE (Himeur Manar, Nadij Salwa, Atouk Asmaa)

## TP 1 : redressement mono-alternance

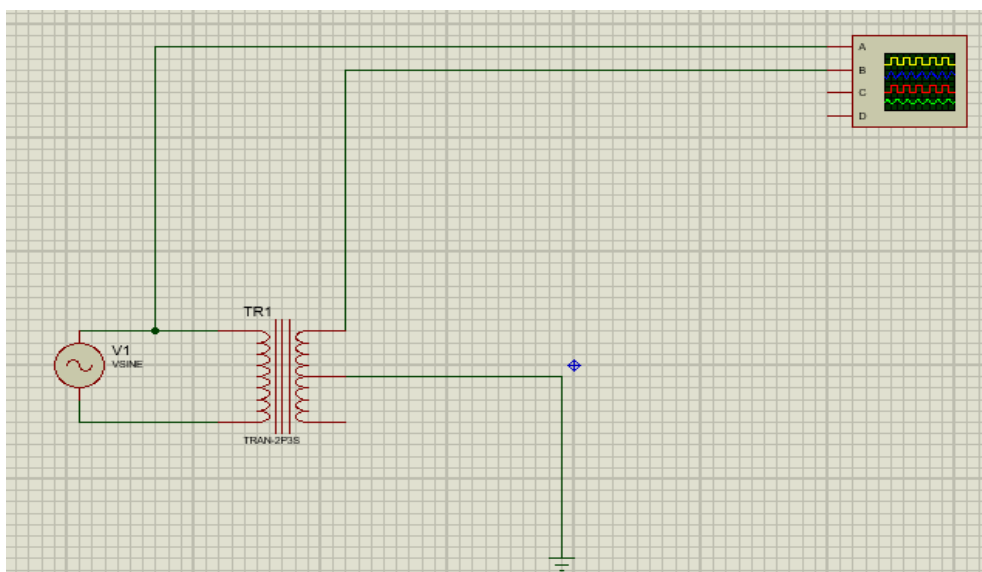
### 1.1 Redressement mono-alternance sans filtrage

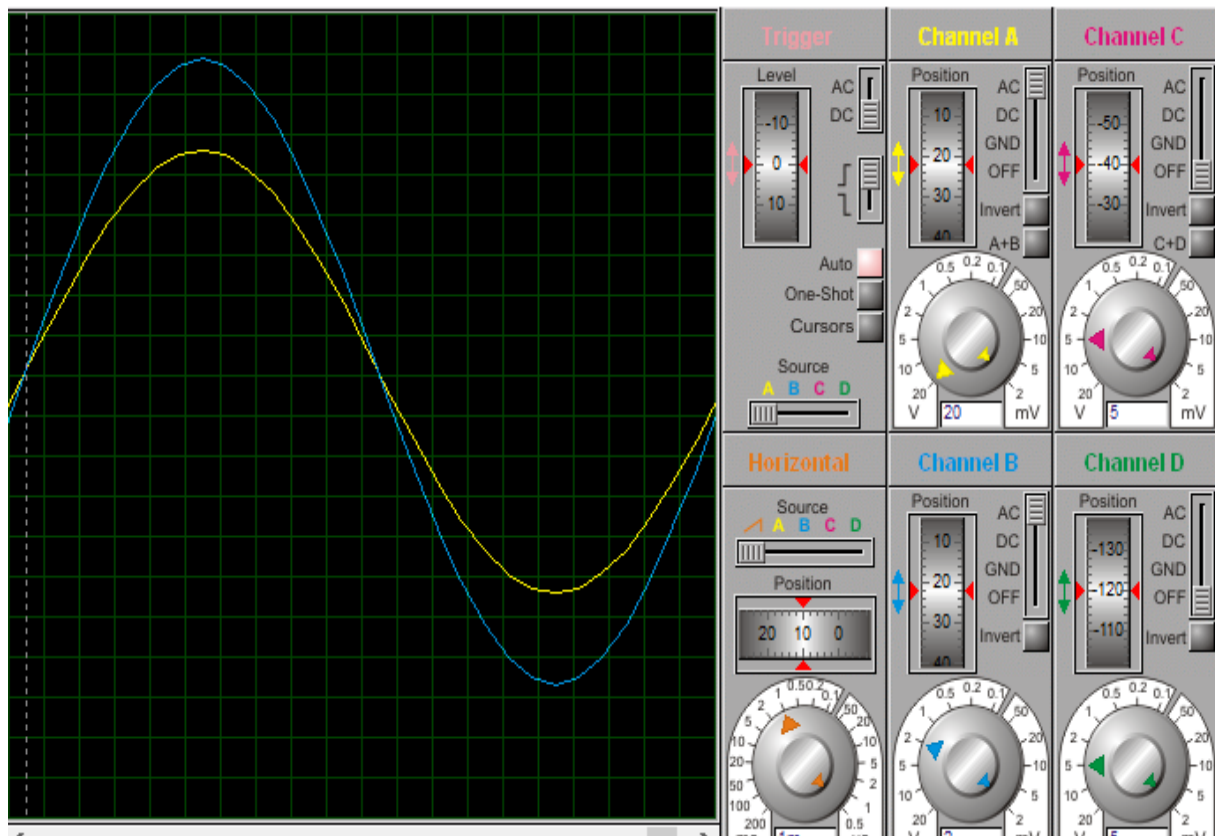


## 1.2 Redressement mono-alternance avec filtrage



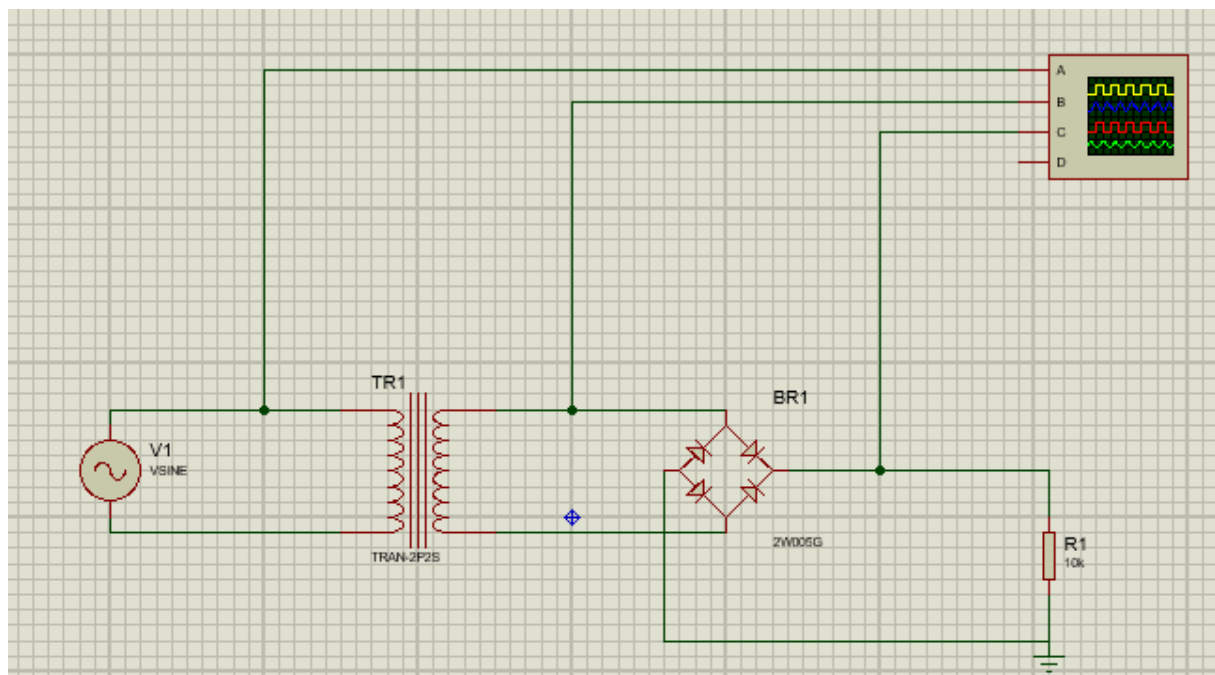
## TP 2 : redressement double alternances transformateur point milieu

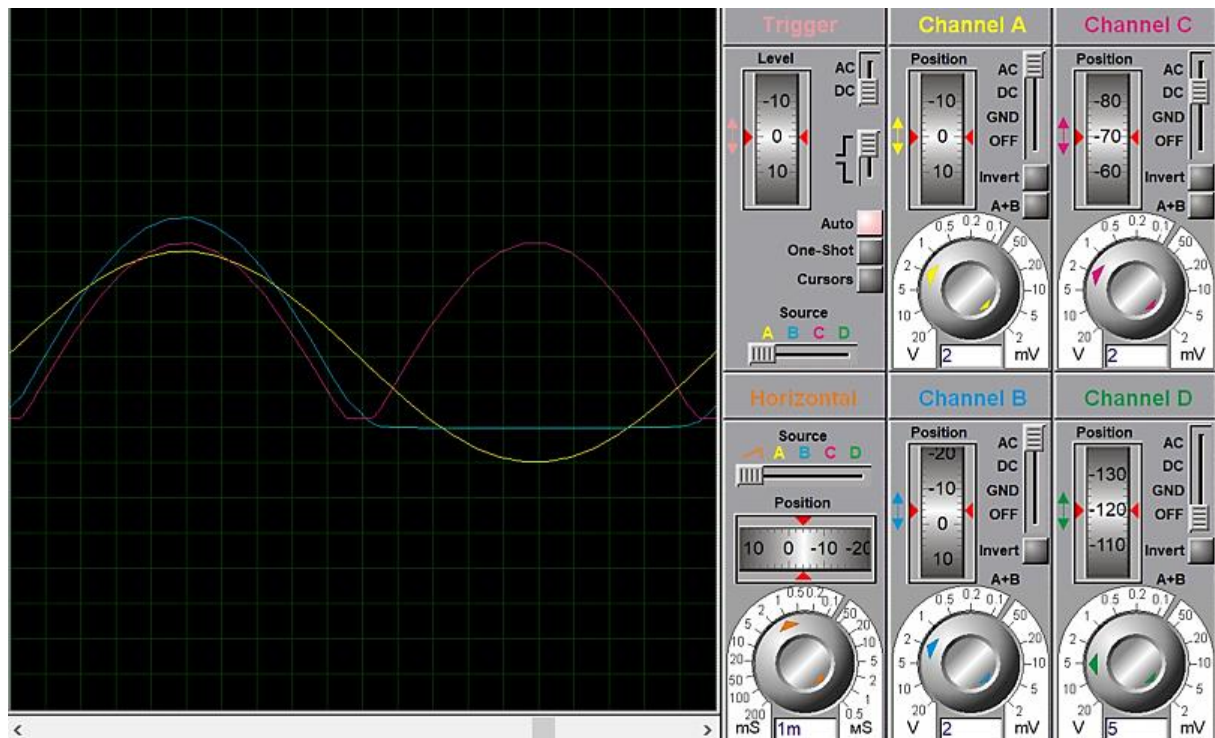




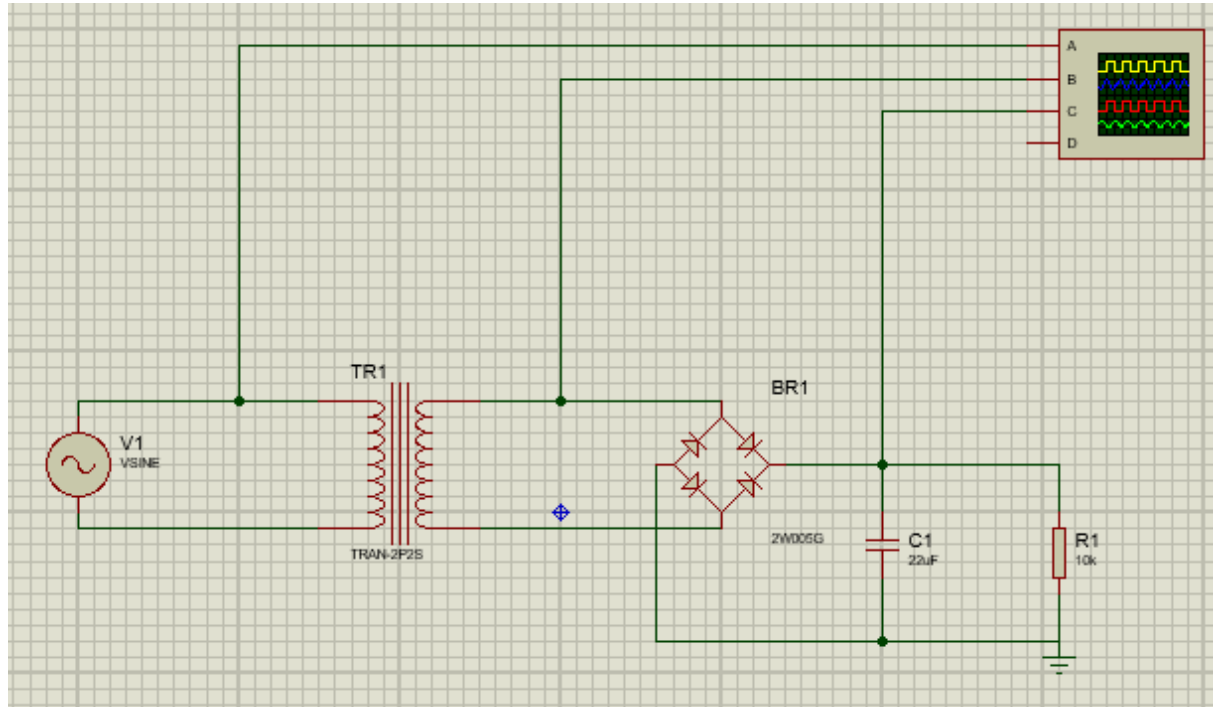
### TP 3 : redressement double alternances

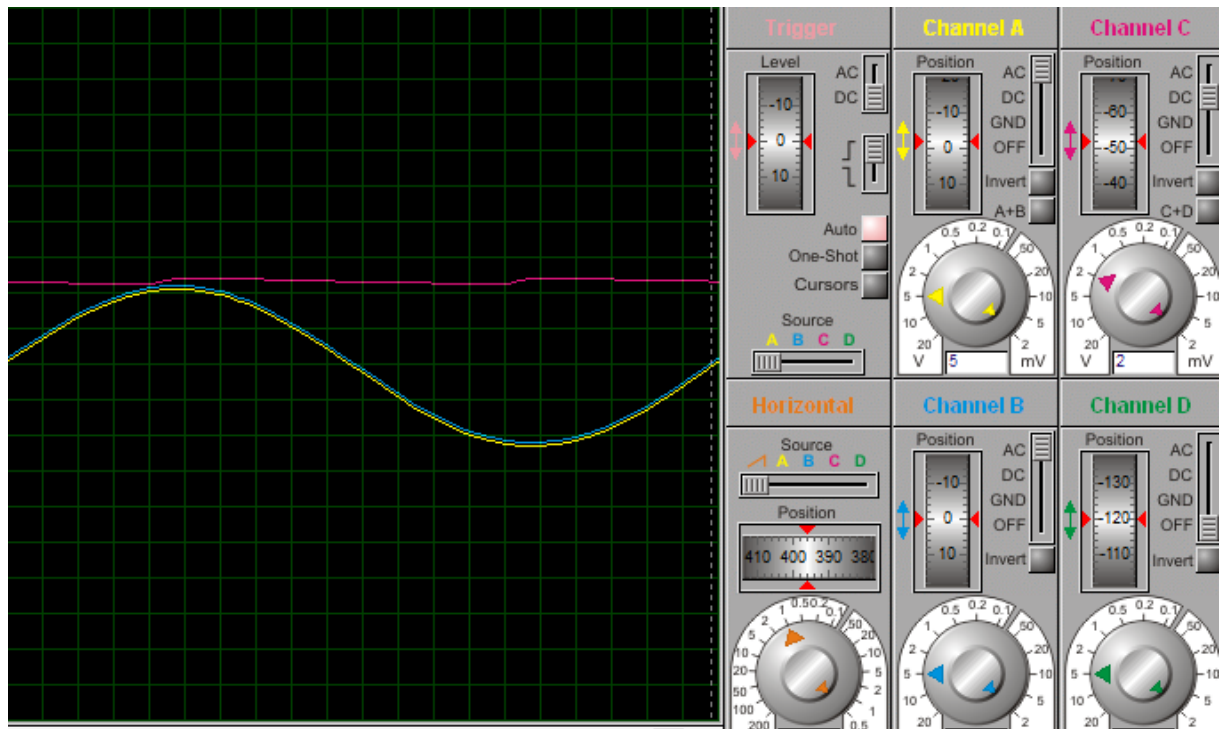
- Pont de Graetz (4 diodes) sans filtrage



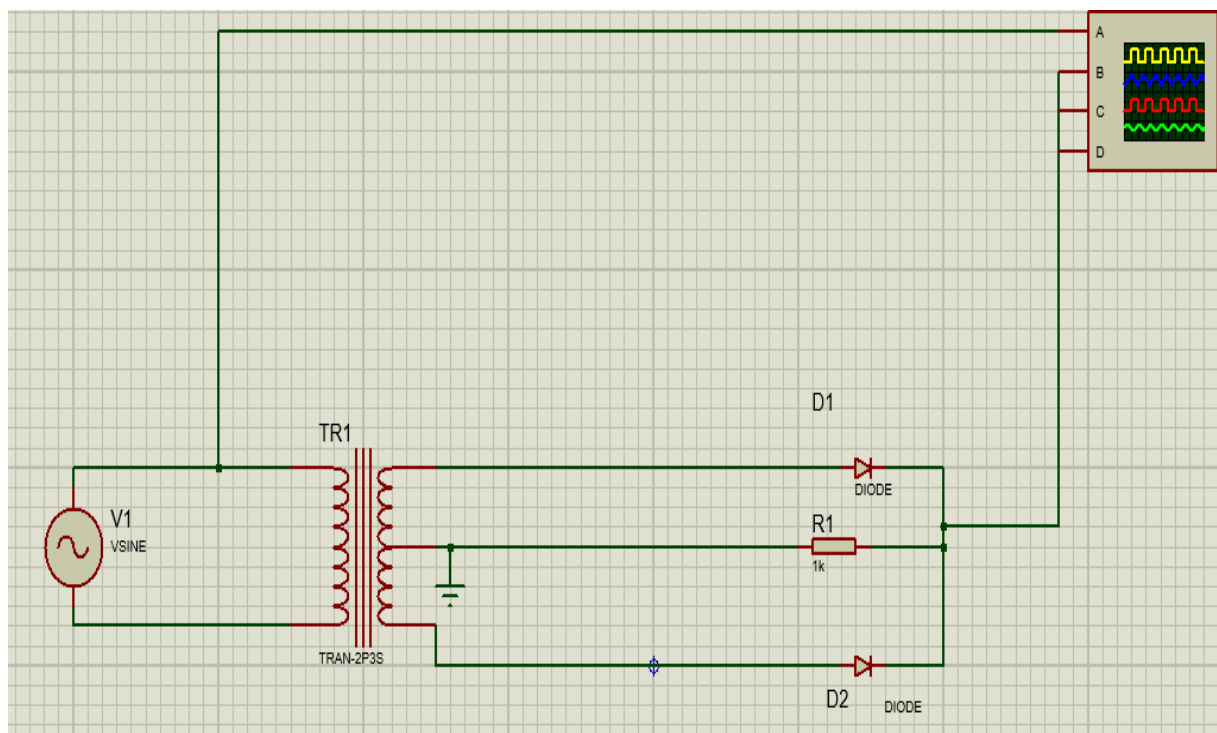


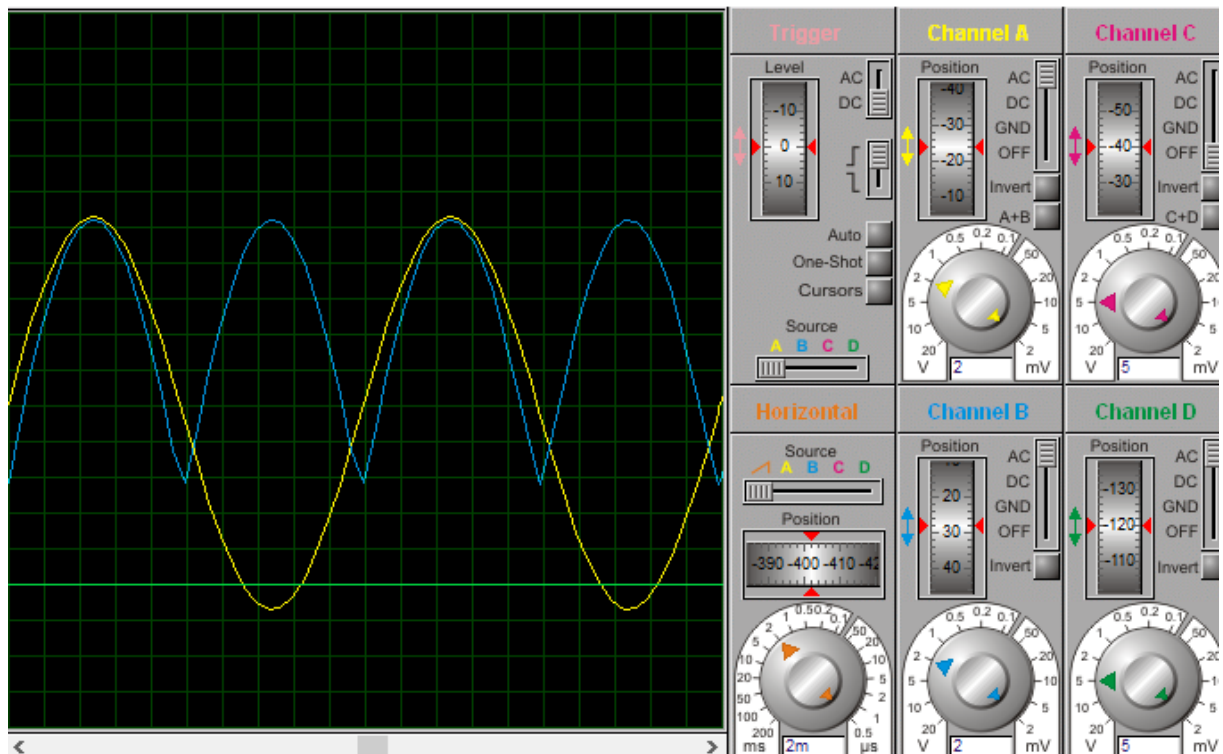
- Pont de Graetz (4 diodes) avec filtrage



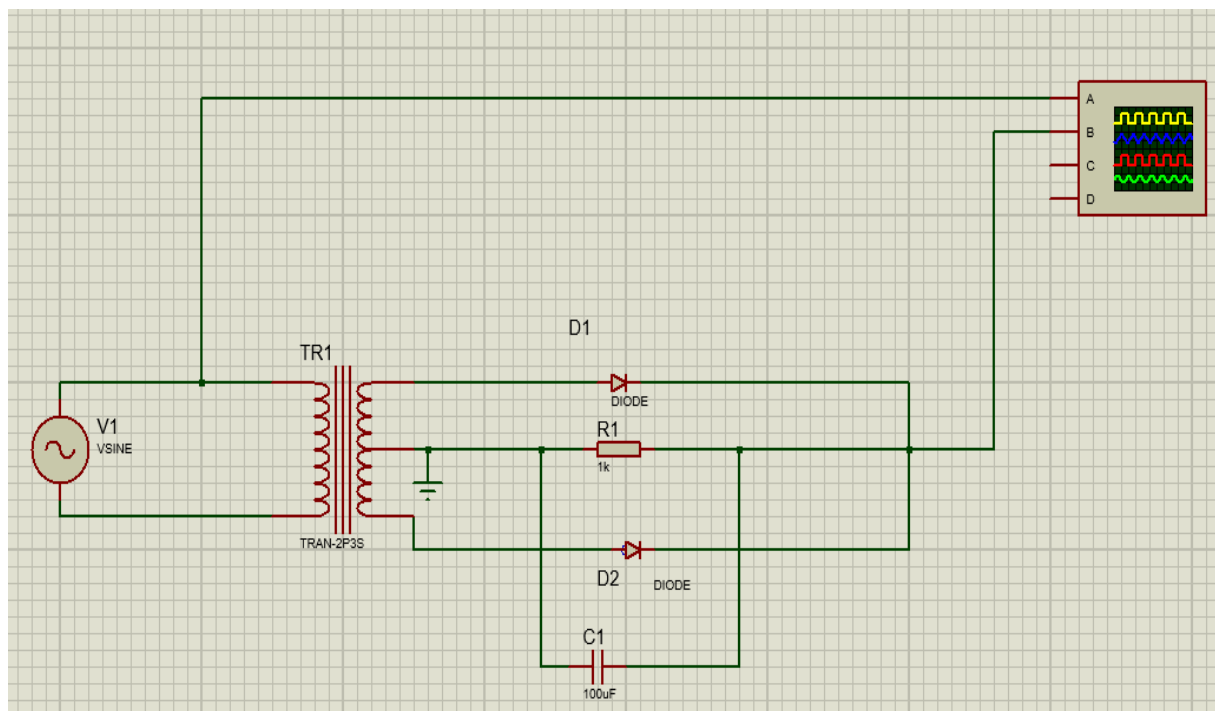


- redressement double alternances (2 diodes) sans filtrage

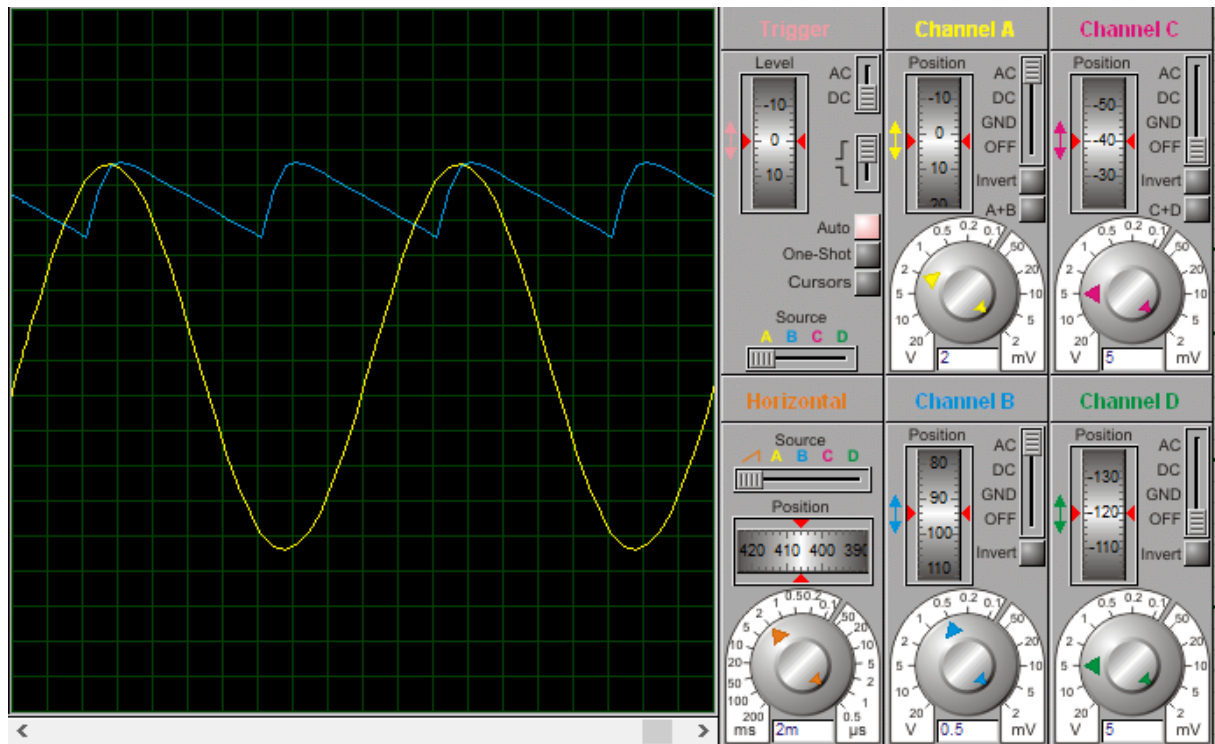




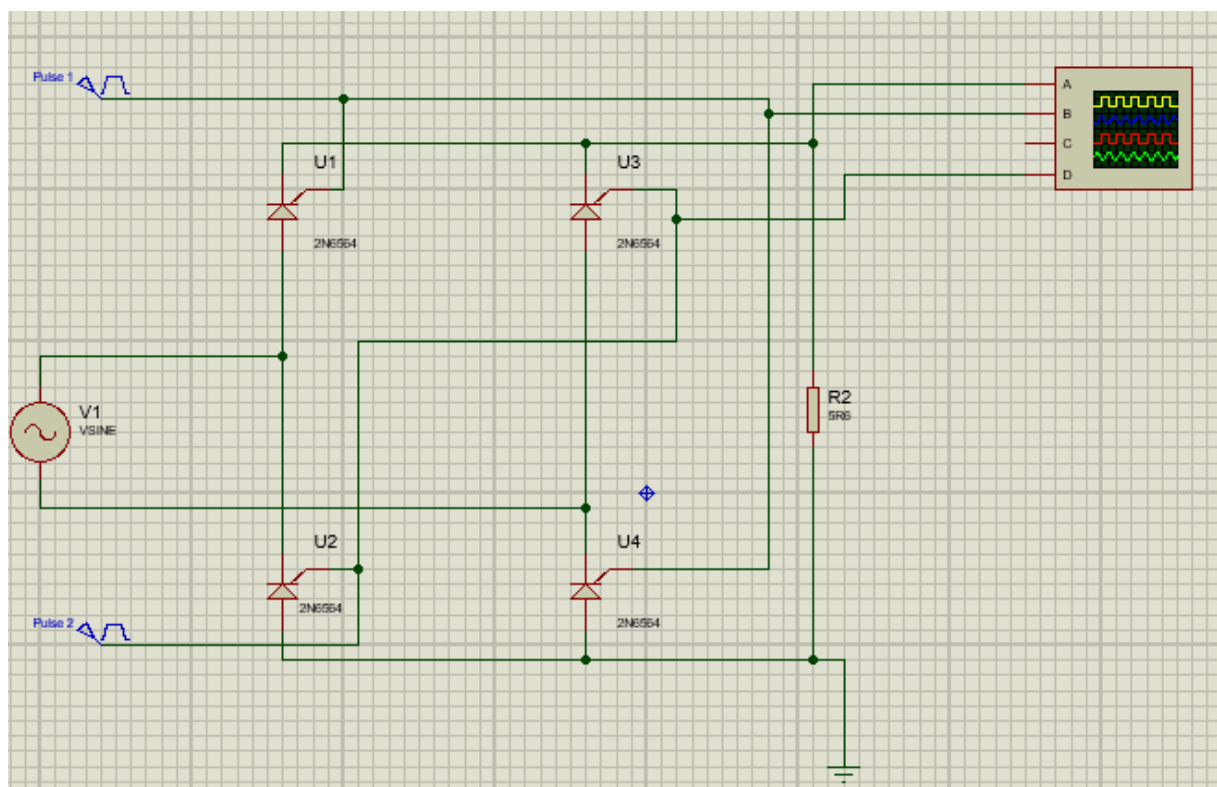
- redressement double alternances (2 diodes) avec filtrage

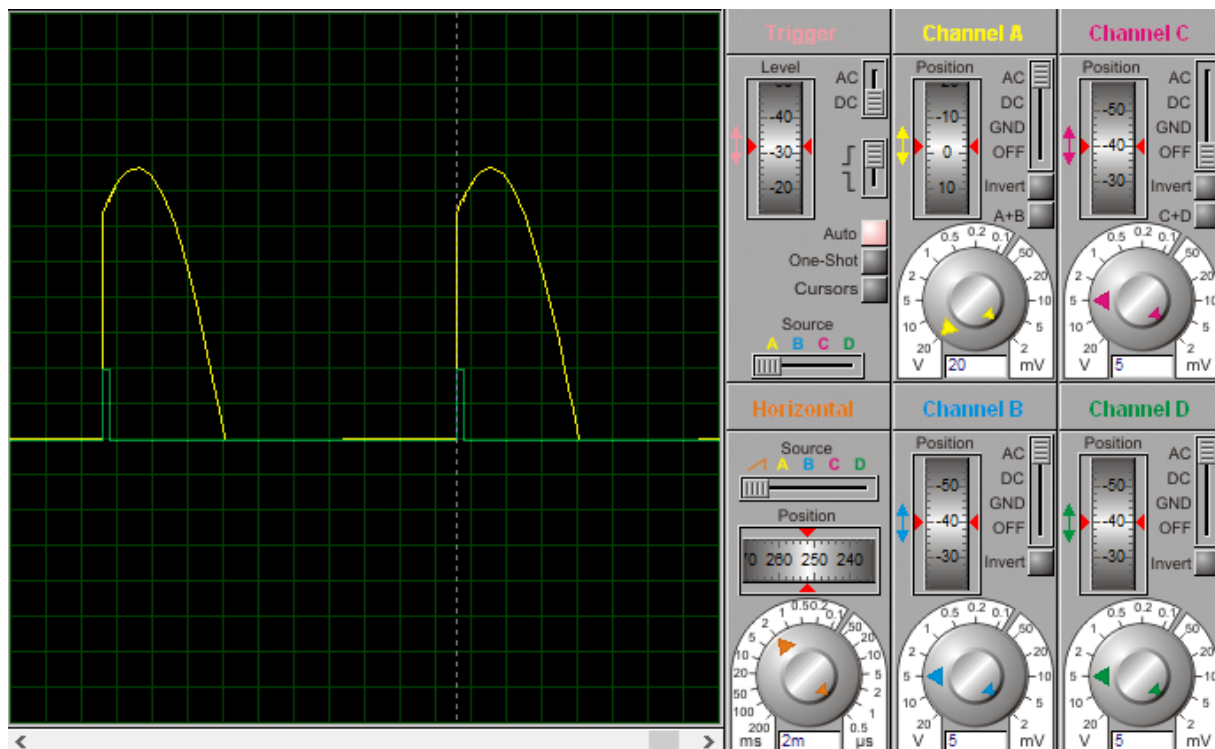




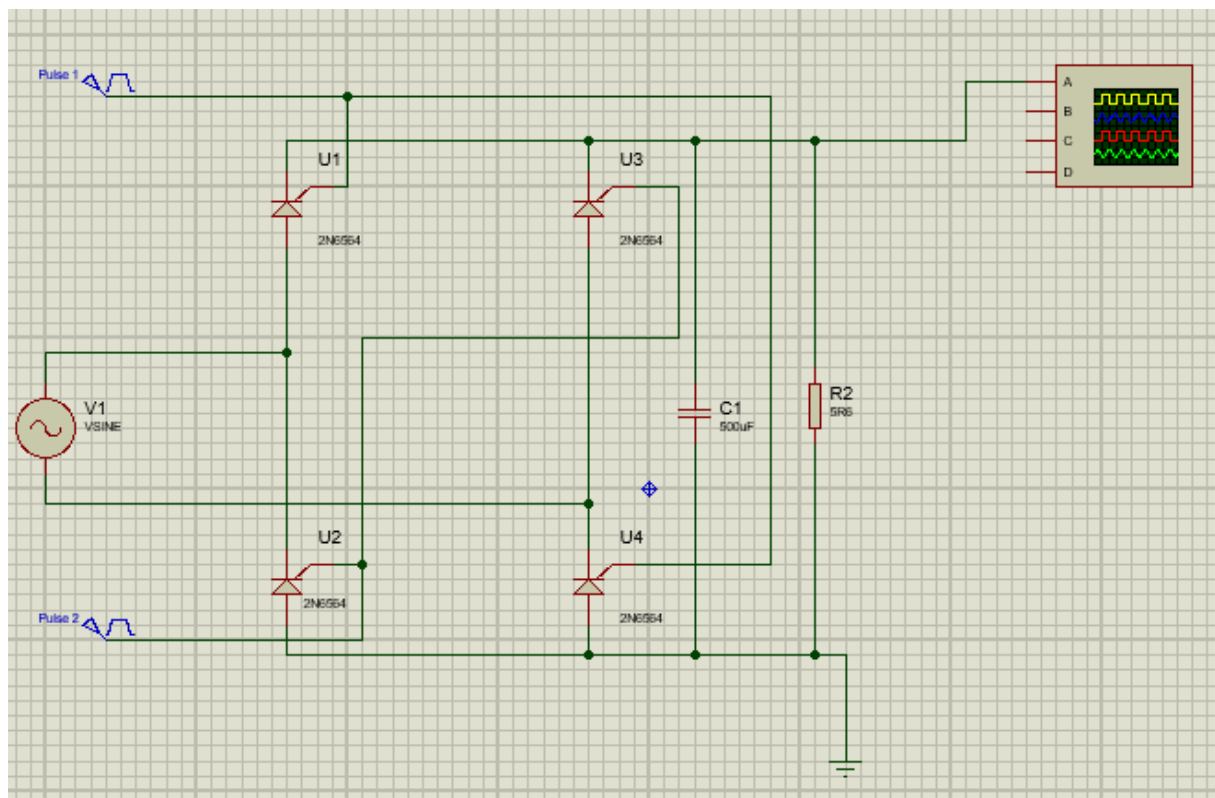


TP 4 : redressement commandé avec thyristor – sans filtrage

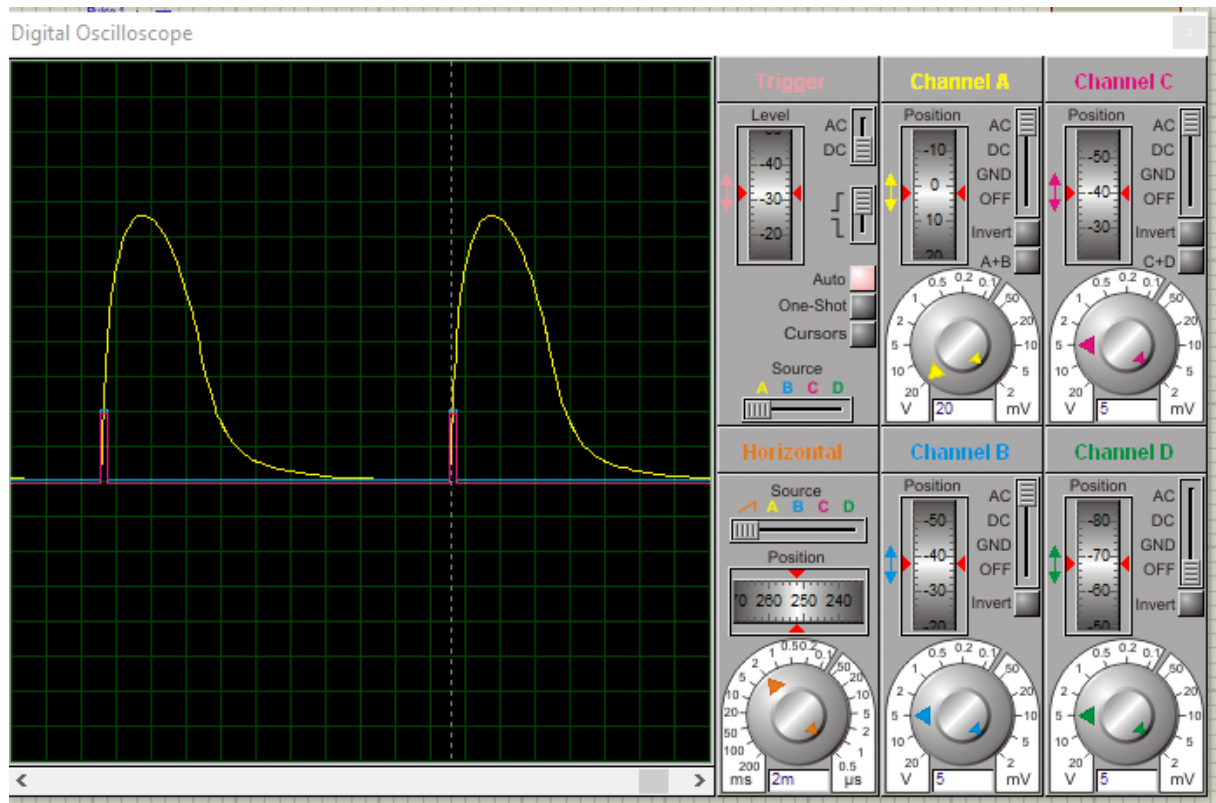




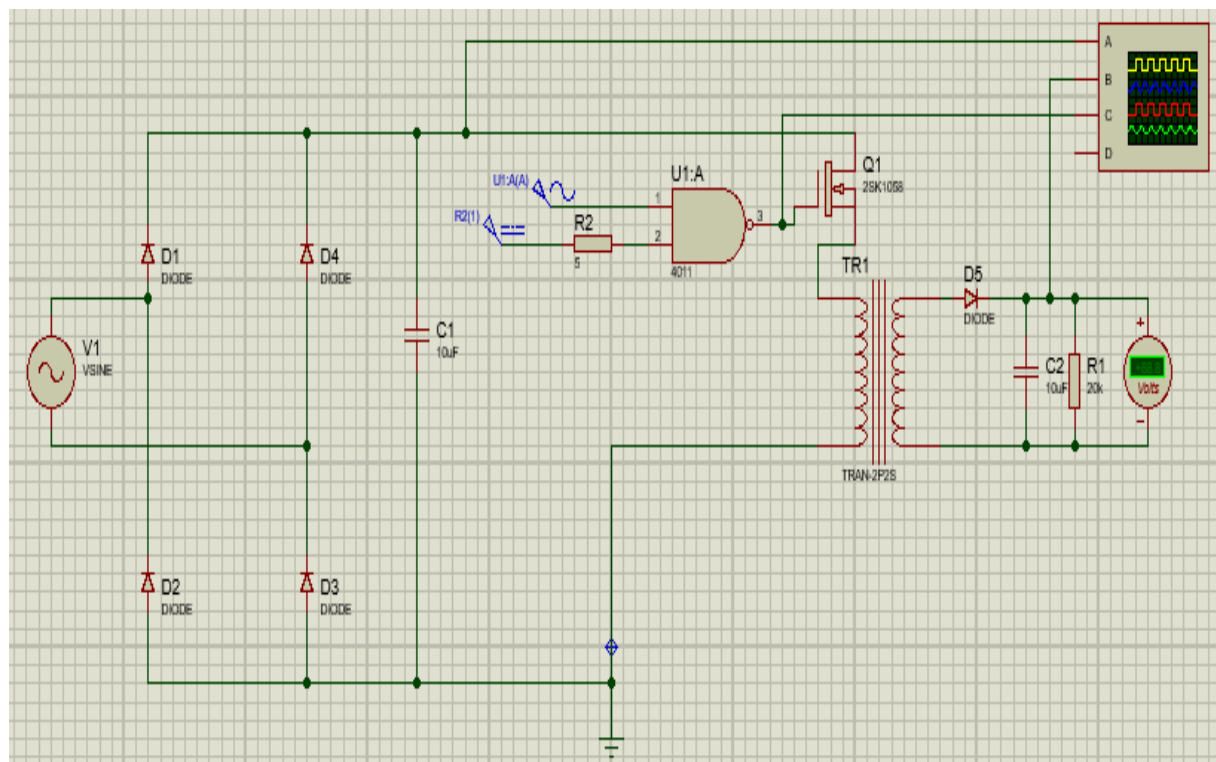
- Redressement commandé avec thyristor avec filtrage

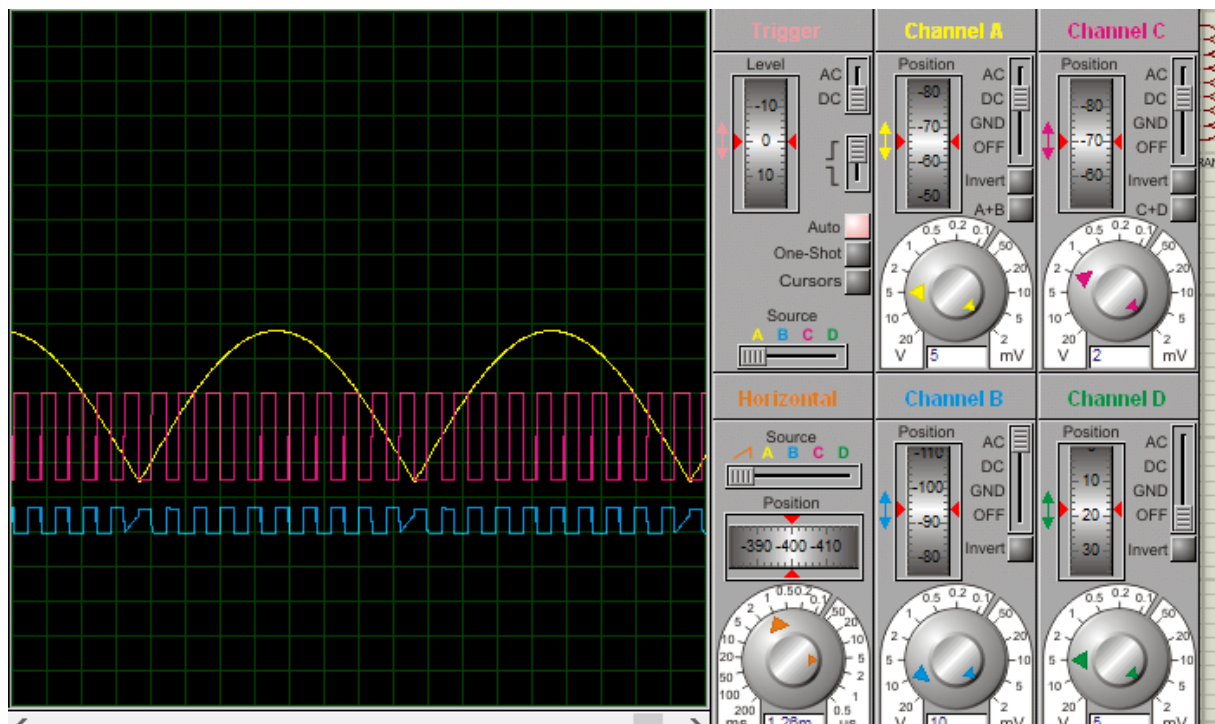






## TP 5 : redressement commandé avec CMOS





TP 6 : Alimentation à découpage

