

CHAPTER 2

Exercise 2.1

(a) $Y = \bar{A}\bar{B} + A\bar{B} + AB$

(b) $Y = \bar{A}\bar{B}\bar{C} + ABC$

(c) $Y = \bar{A}\bar{B}\bar{C} + \bar{A}B\bar{C} + A\bar{B}\bar{C} + A\bar{B}C + ABC$

(d)

$$Y = \bar{A}\bar{B}\bar{C}\bar{D} + \bar{A}\bar{B}\bar{C}D + \bar{A}\bar{B}C\bar{D} + \bar{A}B\bar{C}\bar{D} + \bar{A}B\bar{C}D + \bar{A}BC\bar{D} + \bar{A}BCD$$

(e)

$$Y = \bar{A}\bar{B}\bar{C}\bar{D} + \bar{A}\bar{B}\bar{C}D + \bar{A}\bar{B}C\bar{D} + \bar{A}B\bar{C}\bar{D} + \bar{A}B\bar{C}D + \bar{A}BC\bar{D} + \bar{A}BCD + ABCD$$

Exercise 2.2

(a) $Y = \bar{A}B + A\bar{B} + AB$

(b) $Y = \bar{A}\bar{B}C + \bar{A}B\bar{C} + \bar{A}BC + A\bar{B}\bar{C} + ABC$

(c) $Y = \bar{A}\bar{B}C + AB\bar{C} + ABC$

(d) $Y = \bar{A}\bar{B}\bar{C}\bar{D} + \bar{A}\bar{B}C\bar{D} + \bar{A}B\bar{C}\bar{D} + \bar{A}BC\bar{D} + \bar{A}BCD + \bar{A}\bar{B}\bar{C}D + \bar{A}\bar{B}CD$

(e) $Y = \bar{A}\bar{B}CD + \bar{A}B\bar{C}\bar{D} + \bar{A}BCD + A\bar{B}\bar{C}\bar{D} + A\bar{B}\bar{C}D + A\bar{B}CD + A\bar{B}CD$

Exercise 2.3

(a) $Y = (A + \bar{B})$

(b)

$$Y = (A + B + \bar{C})(A + \bar{B} + C)(A + \bar{B} + \bar{C})(\bar{A} + B + C)(\bar{A} + B + \bar{C})(\bar{A} + \bar{B} + C)$$

(c) $Y = (A + B + \bar{C})(A + \bar{B} + \bar{C})(\bar{A} + \bar{B} + C)$

(d)

$$Y = (A + \bar{B} + C + D)(A + \bar{B} + C + \bar{D})(A + \bar{B} + \bar{C} + D)(A + \bar{B} + \bar{C} + \bar{D})(\bar{A} + B + C + \bar{D})$$

$$(\bar{A} + B + \bar{C} + \bar{D})(\bar{A} + \bar{B} + C + D)(\bar{A} + \bar{B} + C + \bar{D})(\bar{A} + \bar{B} + \bar{C} + D)$$

(e)

$$Y = (A + B + C + \bar{D})(A + B + \bar{C} + D)(A + \bar{B} + C + D)(A + \bar{B} + \bar{C} + \bar{D})(\bar{A} + B + C + D)$$

$$(\bar{A} + B + \bar{C} + \bar{D})(\bar{A} + \bar{B} + C + D)(\bar{A} + \bar{B} + \bar{C} + D)$$

Exercise 2.4

(a) $Y = A + B$

(b) $Y = (A + B + C)(\bar{A} + B + \bar{C})(\bar{A} + \bar{B} + \bar{C})$

(c) $Y = (A + B + C)(A + \bar{B} + C)(A + \bar{B} + \bar{C})(\bar{A} + B + C)(\bar{A} + B + \bar{C})$

(d)

$$Y = (A + B + C + \bar{D})(A + \bar{B} + C + D)(A + \bar{B} + C + \bar{D})(\bar{A} + B + C + \bar{D})$$

$$(\bar{A} + B + \bar{C} + \bar{D})(\bar{A} + \bar{B} + C + D)(\bar{A} + \bar{B} + C + \bar{D})(\bar{A} + \bar{B} + \bar{C} + D)$$

$$(\bar{A} + \bar{B} + \bar{C} + \bar{D})$$

(e)

$$Y = (A + B + C + D)(A + B + C + \bar{D})(A + B + \bar{C} + D)(A + \bar{B} + C + D)$$

$$(\bar{A} + \bar{B} + \bar{C} + \bar{D})(\bar{A} + \bar{B} + C + D)(\bar{A} + \bar{B} + C + \bar{D})(\bar{A} + \bar{B} + \bar{C} + D)$$

$$(\bar{A} + \bar{B} + \bar{C} + \bar{D})$$

Exercise 2.5

(a) $Y = A + \bar{B}$

(b) $Y = \bar{A}\bar{B}\bar{C} + ABC$

(c) $Y = \bar{A}\bar{C} + A\bar{B} + AC$

(d) $Y = \bar{A}\bar{B} + \bar{B}\bar{D} + ACD$

(e)

$$Y = \bar{A}\bar{B}\bar{C}\bar{D} + \bar{A}\bar{B}CD + \bar{A}B\bar{C}\bar{D} + \bar{A}BC\bar{D} + A\bar{B}\bar{C}\bar{D} + A\bar{B}C\bar{D} + AB\bar{C}\bar{D} + ABCD$$

This can also be expressed as:

$$Y = \overline{(A \oplus B)}\overline{(C \oplus D)} + (A \oplus B)(C \oplus D)$$

Exercise 2.6

(a) $Y = A + B$

(b) $Y = A\bar{C} + \bar{A}C + B\bar{C}$ or $Y = A\bar{C} + \bar{A}C + \bar{A}B$

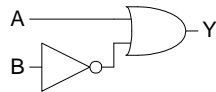
(c) $Y = AB + \bar{A}\bar{B}C$

(d) $Y = BC + \bar{B}\bar{D}$

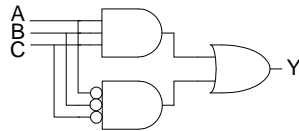
(e) $Y = A\bar{B} + \bar{A}BC + \bar{A}CD$ or $Y = A\bar{B} + \bar{A}BC + \bar{B}CD$

Exercise 2.7

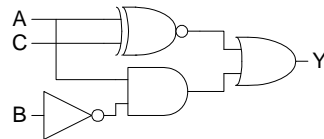
(a)



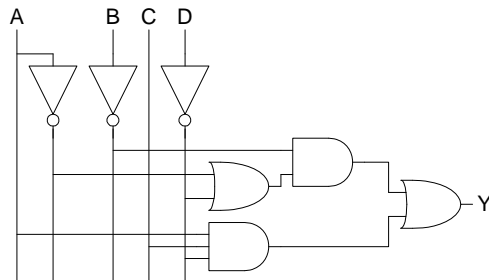
(b)



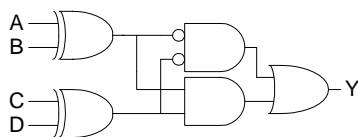
(c)



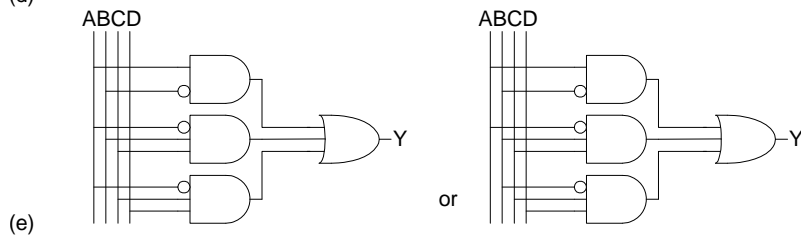
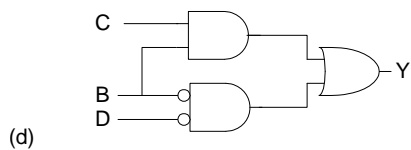
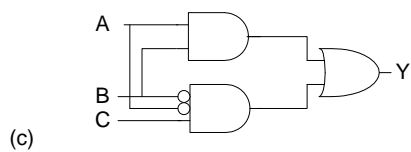
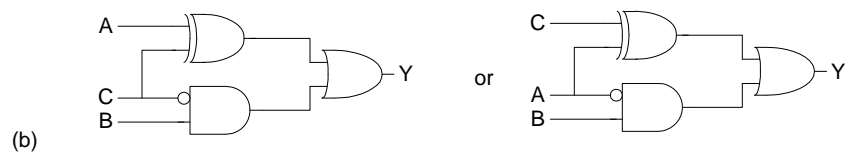
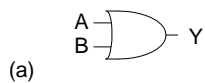
(d)



(e)



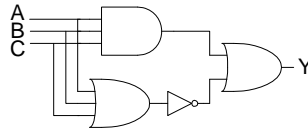
Exercise 2.8



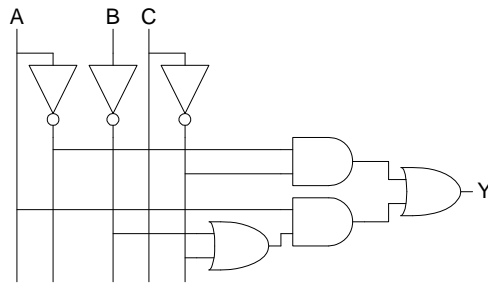
Exercise 2.9

(a) Same as 2.7(a)

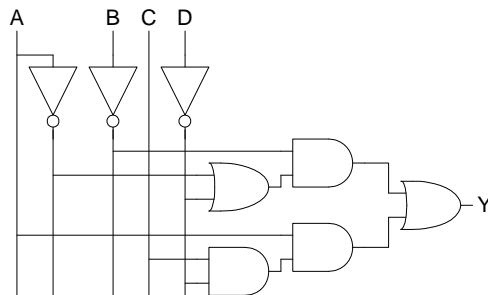
(b)



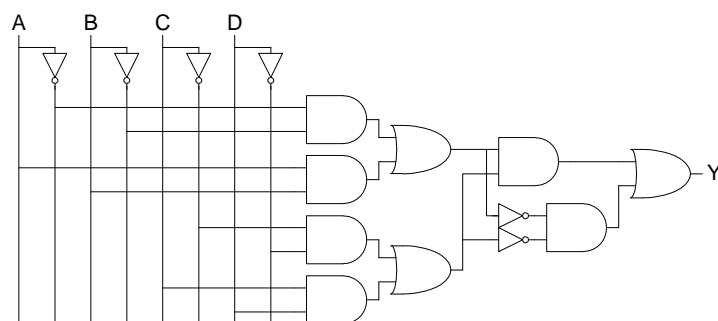
(c)



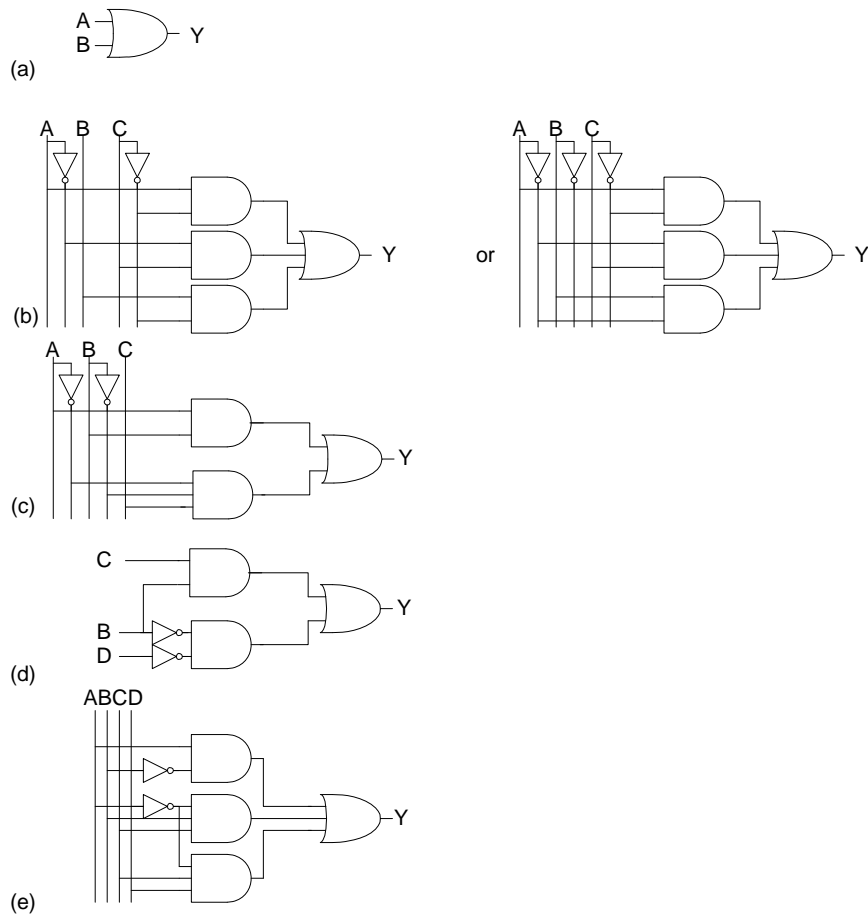
(d)



(e)

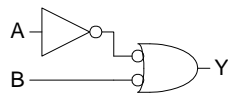


Exercise 2.10

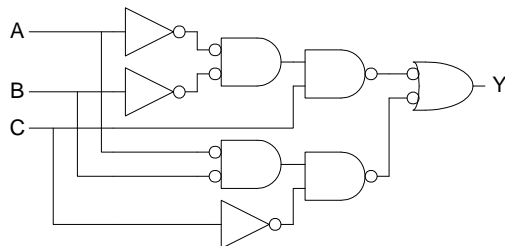


Exercise 2.11

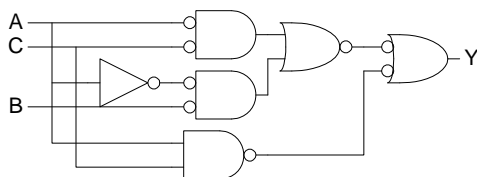
(a)



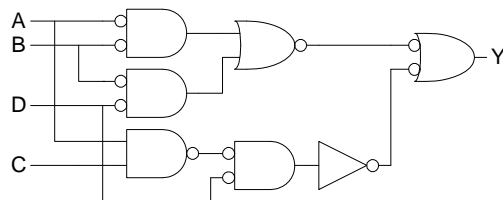
(b)



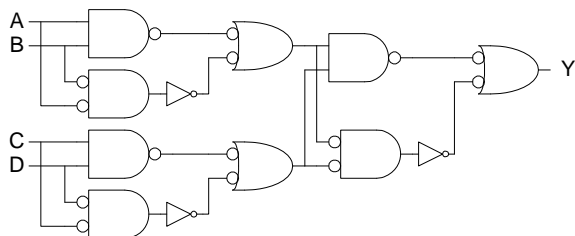
(c)



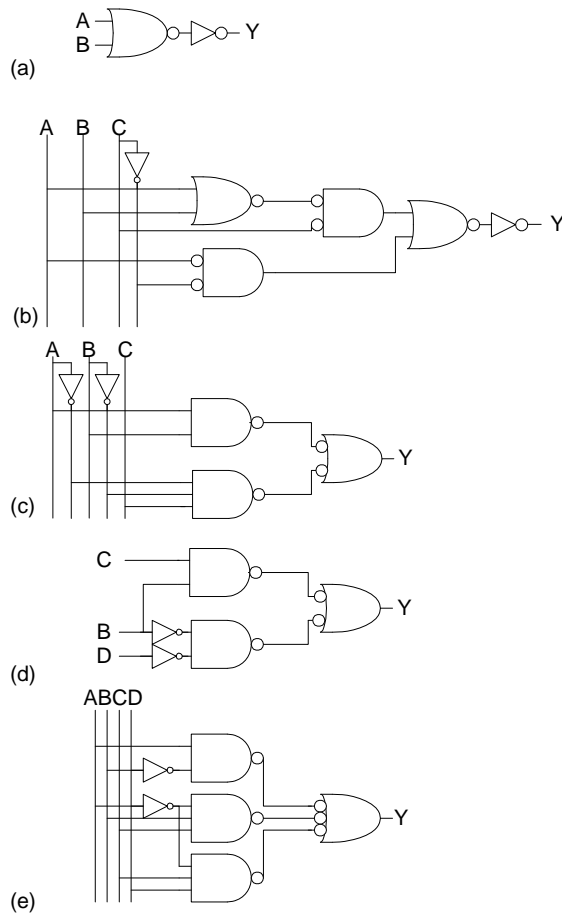
(d)



(e)



Exercise 2.12



Exercise 2.13

- (a) $Y = AC + \overline{B}C$
 (b) $Y = \overline{A}$
 (c) $Y = \overline{A} + \overline{B}\overline{C} + \overline{B}\overline{D} + BD$

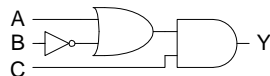
Exercise 2.14

- (a) $Y = \overline{A}B$
 (b) $Y = \overline{A} + \overline{B} + \overline{C} = \overline{ABC}$

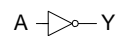
$$(c) Y = A(\bar{B} + \bar{C} + \bar{D}) + \bar{B}\bar{C}\bar{D} = \overline{ABCD} + \bar{B}\bar{C}\bar{D}$$

Exercise 2.15

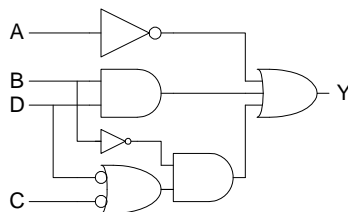
(a)



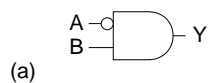
(b)



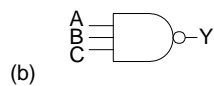
(c)



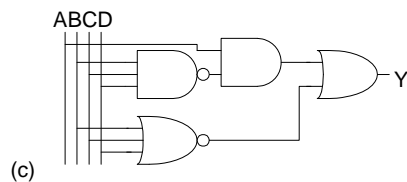
Exercise 2.16



(a)



(b)



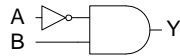
(c)

Exercise 2.17

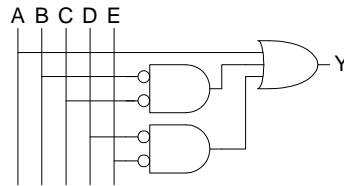
(a) $Y = B + \bar{A}\bar{C}$



(b) $Y = \bar{A}B$



(c) $Y = A + \bar{B}\bar{C} + \bar{D}\bar{E}$



Exercise 2.18

(a) $Y = \bar{B} + C$

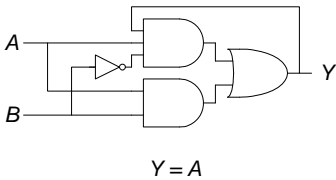
(b) $Y = (A + \bar{C})D + B$

(c) $Y = B\bar{D}E + BD(\overline{A \oplus C})$

Exercise 2.19

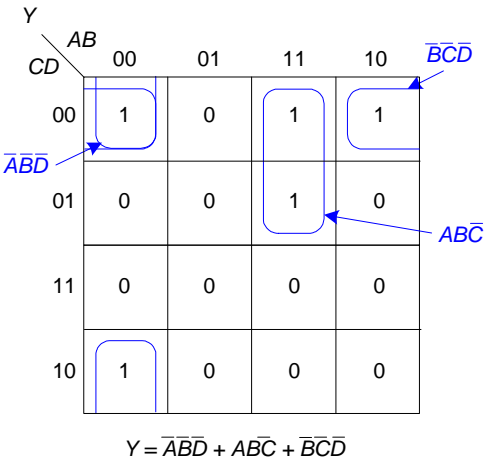
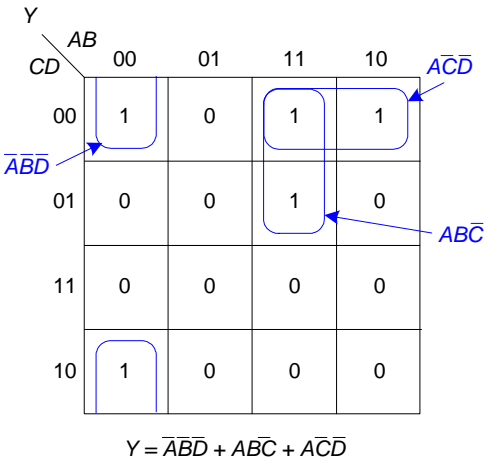
4 gigarows = 4×2^{30} rows = 2^{32} rows, so the truth table has 32 inputs.

Exercise 2.20



Exercise 2.21

Ben is correct. For example, the following function, shown as a K-map, has two possible minimal sum-of-products expressions. Thus, although $\overline{A}\overline{C}\overline{D}$ and $\overline{B}\overline{C}\overline{D}$ are both prime implicants, the minimal sum-of-products expression does not have both of them.



Exercise 2.22

(a)

B	$B \bullet B$
0	0
1	1

(b)

B	C	D	$(B \bullet C) + (B \bullet D)$	$B \bullet (C + D)$
0	0	0	0	0
0	0	1	0	0
0	1	0	0	0
0	1	1	0	0
1	0	0	0	0
1	0	1	1	1
1	1	0	1	1
1	1	1	1	1

(c)

B	C	$(B \bullet C) + (B \bullet \bar{C})$
0	0	0
0	1	0
1	0	1
1	1	1

Exercise 2.23

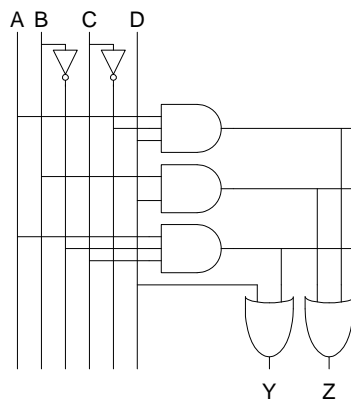
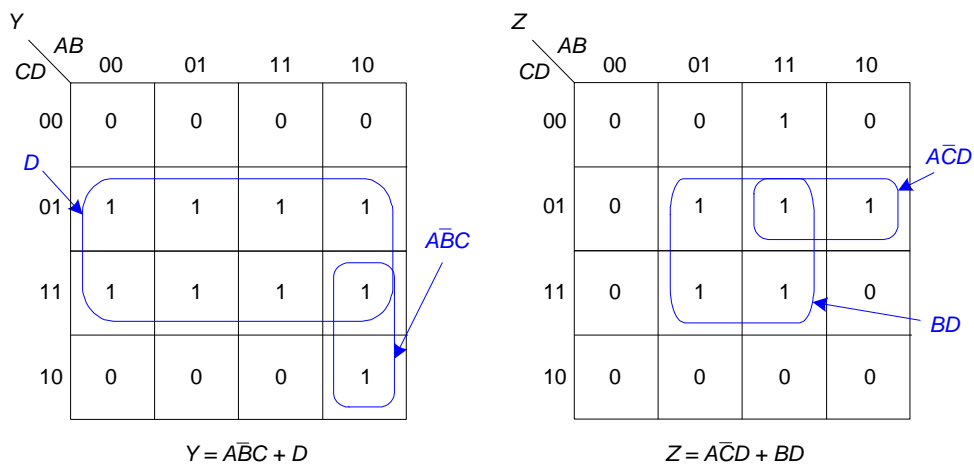
B_2	B_1	B_0	$\overline{B_2 \bullet B_1 \bullet B_0}$	$\overline{B_2} + \overline{B_1} + \overline{B_0}$
0	0	0	1	1
0	0	1	1	1
0	1	0	1	1
0	1	1	1	1
1	0	0	1	1
1	0	1	1	1
1	1	0	1	1
1	1	1	0	0

Exercise 2.24

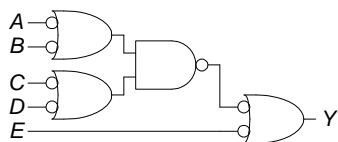
$$Y = \bar{A}D + \bar{A}\bar{B}C + \bar{A}\bar{C}D + ABCD$$

$$Z = \bar{A}\bar{C}D + BD$$

Exercise 2.25

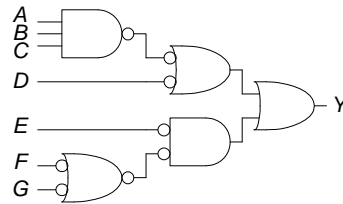


Exercise 2.26



$$Y = (\bar{A} + \bar{B})(\bar{C} + \bar{D}) + \bar{E}$$

Exercise 2.27



$$Y = ABC + \bar{D} + (\bar{F} + \bar{G})\bar{E}$$

$$= ABC + \bar{D} + \bar{E}\bar{F} + \bar{E}\bar{G}$$

Exercise 2.28

Two possible options are shown below:

Y CD \ AB	AB			
	00	01	11	10
00	X	0	1	1
01	X	X	1	0
11	0	X	1	1
10	X	0	X	X

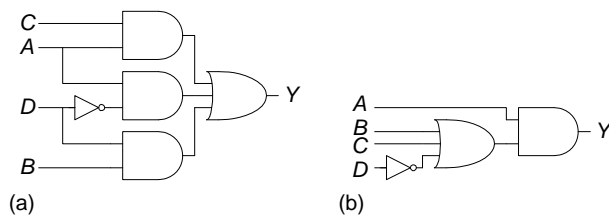
(a) $Y = A\bar{D} + AC + BD$

Y CD \ AB	AB			
	00	01	11	10
00	X	0	1	1
01	X	X	1	0
11	0	X	1	1
10	X	0	X	X

(b) $Y = A(B + C + \bar{D})$

Exercise 2.29

Two possible options are shown below:



Exercise 2.30

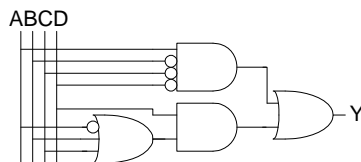
Option (a) could have a glitch when $A=1$, $B=1$, $C=0$, and D transitions from 1 to 0. The glitch could be removed by instead using the circuit in option (b).

Option (b) does not have a glitch. Only one path exists from any given input to the output.

Exercise 2.31

$$Y = \bar{A}D + A\bar{B}\bar{C}\bar{D} + BD + CD = A\bar{B}\bar{C}\bar{D} + D(\bar{A} + B + C)$$

Exercise 2.32



Exercise 2.33

The equation can be written directly from the description:

$$E = \bar{S}\bar{A} + AL + H$$

Exercise 2.34

(a)

S_c $D_{1:0} \backslash D_{3:2}$		00	01	11	10
		00	01	11	10
00	1	1	0	1	
01	1	1	0	1	
11	1	1	0	0	
10	0	1	0	0	

$$S_c = \bar{D}_3 D_0 + \bar{D}_3 D_2 + \bar{D}_2 \bar{D}_1$$

S_d $D_{1:0} \backslash D_{3:2}$		00	01	11	10
		00	01	11	10
00	1	0	0	1	
01	0	1	0	0	
11	1	0	0	0	
10	1	1	0	0	

$$S_d = \bar{D}_3 D_1 \bar{D}_0 + \bar{D}_3 \bar{D}_2 D_1 + \bar{D}_2 \bar{D}_1 \bar{D}_0 + \bar{D}_3 D_2 \bar{D}_1 D_0$$

S_e $D_{1:0} \backslash D_{3:2}$		00	01	11	10
		00	01	11	10
00		1	0	0	1
01		0	0	0	0
11		0	0	0	0
10		1	1	0	0

$$S_e = \bar{D}_2 \bar{D}_1 \bar{D}_0 + \bar{D}_3 D_1 \bar{D}_0$$

$$D_2 D_1 D_0 + D_3 D_2 D_1 D_0$$

S_f $D_{1:0} \backslash D_{3:2}$	00	01	11	10
00	1	1	0	1
01	0	1	0	1
11	0	0	0	0
10	0	1	0	0

$$S_f = \bar{D}_3 \bar{D}_1 \bar{D}_0 + \bar{D}_3 D_2 \bar{D}_1 + \bar{D}_3 D_2 \bar{D}_0 + D_3 \bar{D}_2 \bar{D}_1$$

S_g $D_{1:0}$		$D_{3:2}$			
		00	01	11	10
00	00	0	1	0	1
	01	0	1	0	1
	11	1	0	0	0
	10	1	1	0	0

$$S_g = \bar{D}_3 \bar{D}_2 D_1 + \bar{D}_3 D_1 \bar{D}_0 + \bar{D}_3 D_2 \bar{D}_1 + D_3 \bar{D}_2 \bar{D}_1$$

(b)

S_a

$D_{3:2}$	00	01	11	10
$D_{1:0}$				
00	1	0	X	1
01	0	1	X	1
11	1	1	X	X
10	0	1	X	X

$$S_a = \bar{D}_2 \bar{D}_1 \bar{D}_0 + D_2 D_0 + D_3 + D_2 D_1 + D_1 D_0$$

S_b

$D_{3:2}$	00	01	11	10
$D_{1:0}$				
00	1	1	X	1
01	1	0	X	1
11	1	1	X	X
10	1	0	X	X

$$S_b = \bar{D}_1 \bar{D}_0 + D_1 D_0 + \bar{D}_2$$

S_c

$D_{3:2}$	00	01	11	10
$D_{1:0}$				
00	1	1	X	1
01	1	1	X	1
11	1	1	X	X
10	0	1	X	X

$$S_c = \bar{D}_1 + D_0 + D_2$$

S_d

$D_{3:2}$	00	01	11	10
$D_{1:0}$				
00	1	0	X	1
01	0	1	X	0
11	1	0	X	X
10	1	1	X	X

$$S_d = D_2 \bar{D}_1 D_0 + \bar{D}_2 \bar{D}_0 + \bar{D}_2 D_1 + D_1 \bar{D}_0$$

S_e		$D_{3:2}$			
$D_{1:0}$		00	01	11	10
	00	1	0	X	1
	01	0	0	X	0
	11	0	0	X	X
	10	1	1	X	X

$$S_e = \bar{D}_2 \bar{D}_0 + D_1 \bar{D}_0$$

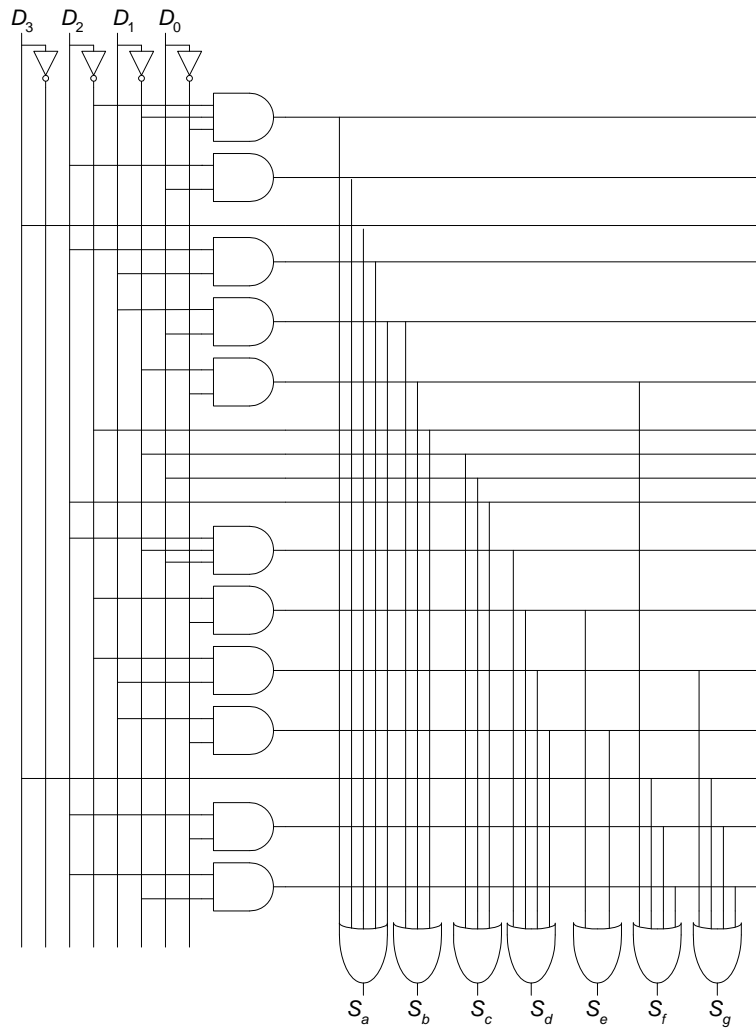
S_f		$D_{3:2}$			
$D_{1:0}$		00	01	11	10
	00	1	1	X	1
	01	0	1	X	1
	11	0	0	X	X
	10	0	1	X	X

$$S_f = \bar{D}_1 \bar{D}_0 + D_2 \bar{D}_1 + D_2 \bar{D}_0 + D_3$$

S_g		$D_{3:2}$			
$D_{1:0}$		00	01	11	10
	00	0	1	X	1
	01	0	1	X	1
	11	1	0	X	X
	10	1	1	X	X

$$S_g = \bar{D}_2 D_1 + D_2 \bar{D}_0 + D_2 \bar{D}_1 + D_3$$

(c)



Exercise 2.35

Decimal Value	A_3	A_2	A_1	A_0	D	P
0	0	0	0	0	0	0
1	0	0	0	1	0	0
2	0	0	1	0	0	1
3	0	0	1	1	1	1
4	0	1	0	0	0	0
5	0	1	0	1	0	1
6	0	1	1	0	1	0
7	0	1	1	1	0	1
8	1	0	0	0	0	0
9	1	0	0	1	1	0
10	1	0	1	0	0	0
11	1	0	1	1	0	1
12	1	1	0	0	1	0
13	1	1	0	1	0	1
14	1	1	1	0	0	0
15	1	1	1	1	1	0

P has two possible minimal solutions:

D

$A_{3:2}$

0001

$A_{1:0}$

0001

00	0	0	1	0
01	0	0	0	1
11	1	0	1	0
10	0	1	0	0

$$D = \overline{A}_3\overline{A}_2A_1A_0 + \overline{A}_3A_2A_1\overline{A}_0 + A_3\overline{A}_2\overline{A}_1A_0 + A_3A_2\overline{A}_1\overline{A}_0 + A_3A_2A_1A_0$$

P

$A_{3:2}$

0001

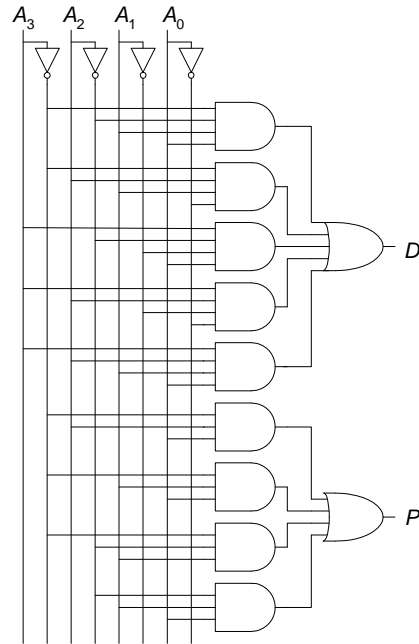
$A_{1:0}$

0001

00	0	0	0	0
01	0	1	1	0
11	1	1	0	1
10	1	0	0	0

$$P = \overline{A}_3\overline{A}_2A_0 + \overline{A}_3A_1A_0 + \overline{A}_3\overline{A}_2A_1 + \overline{A}_2A_1A_0$$
$$P = \overline{A}_3A_1A_0 + \overline{A}_3\overline{A}_2A_1 + \overline{A}_2A_1A_0 + \overline{A}_2\overline{A}_1A_0$$

Hardware implementations are below (implementing the first minimal equation given for P).



Exercise 2.36

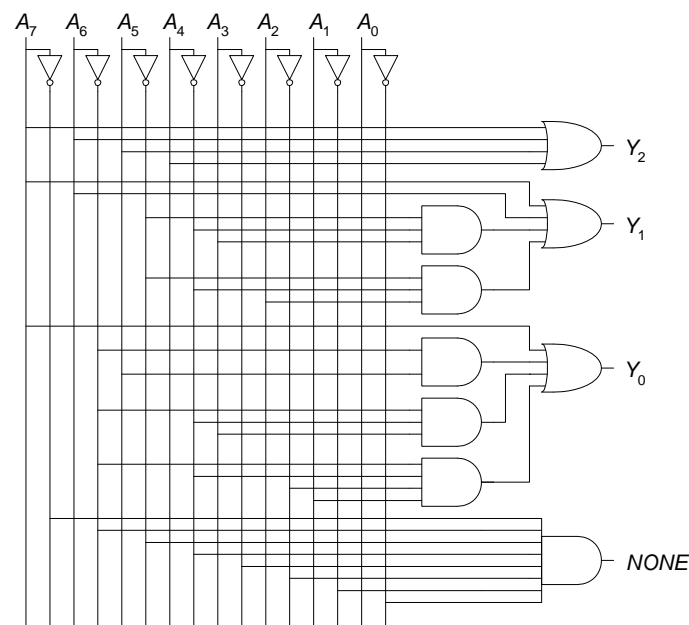
A_7	A_6	A_5	A_4	A_3	A_2	A_1	A_0	Y_2	Y_1	Y_0	NONE
0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	1	X	0	0	1	0
0	0	0	0	0	1	X	X	0	1	0	0
0	0	0	0	1	X	X	X	0	1	1	0
0	0	0	1	X	X	X	X	1	0	0	0
0	0	1	X	X	X	X	X	1	0	1	0
0	1	X	X	X	X	X	X	1	1	0	0
1	X	X	X	X	X	X	X	1	1	1	0

$$Y_2 = A_7 + A_6 + A_5 + A_4$$

$$Y_1 = A_7 + A_6 + \overline{A_5}\overline{A_4}A_3 + \overline{A_5}\overline{A_4}A_2$$

$$Y_0 = A_7 + \overline{A_6}A_5 + \overline{A_6}\overline{A_4}A_3 + \overline{A_6}\overline{A_4}\overline{A_2}A_1$$

$$NONE = \overline{A_7}\overline{A_6}\overline{A_5}\overline{A_4}\overline{A_3}\overline{A_2}\overline{A_1}\overline{A_0}$$



Exercise 2.37

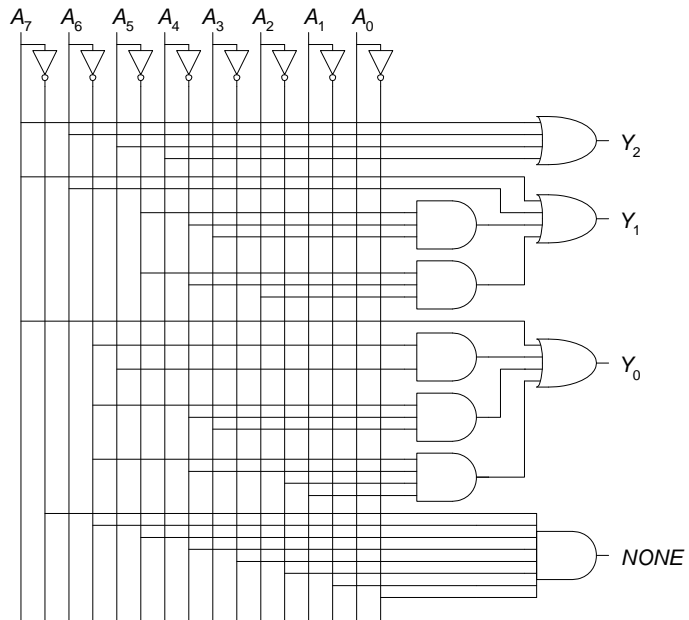
The equations and circuit for $Y_{2:0}$ is the same as in Exercise 2.25, repeated here for convenience.

A_7	A_6	A_5	A_4	A_3	A_2	A_1	A_0	Y_2	Y_1	Y_0
0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	1	x	0	0	1
0	0	0	0	0	1	x	x	0	1	0
0	0	0	0	1	x	x	x	0	1	1
0	0	0	1	x	x	x	x	1	0	0
0	0	1	x	x	x	x	x	1	0	1
0	1	x	x	x	x	x	x	1	1	0
1	x	x	x	x	x	x	x	1	1	1

$$Y_2 = A_7 + A_6 + A_5 + A_4$$

$$Y_1 = A_7 + A_6 + \overline{A_5}\overline{A_4}A_3 + \overline{A_5}\overline{A_4}A_2$$

$$Y_0 = A_7 + \overline{A_6}A_5 + \overline{A_6}\overline{A_4}A_3 + \overline{A_6}\overline{A_4}\overline{A_2}A_1$$



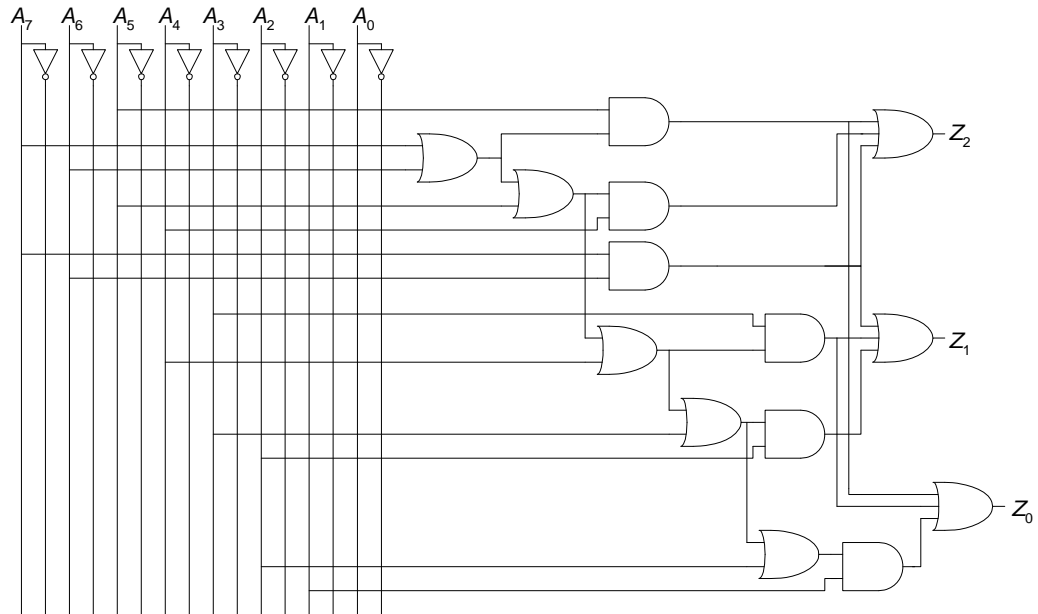
The truth table, equations, and circuit for $Z_{2:0}$ are as follows.

A_7	A_6	A_5	A_4	A_3	A_2	A_1	A_0	Z_2	Z_1	Z_0
0	0	0	0	0	0	1	1	0	0	0
0	0	0	0	0	1	0	1	0	0	0
0	0	0	0	1	0	0	1	0	0	0
0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	0	0	1	0	0	0
0	1	0	0	0	0	0	1	0	0	0
1	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	1	1	X	0	0	1
0	0	0	0	1	0	1	X	0	0	1
0	0	0	1	0	0	1	X	0	0	1
0	0	1	0	0	0	1	X	0	0	1
0	1	0	0	0	0	1	X	0	0	1
1	0	0	0	0	0	1	X	0	0	1
0	0	0	0	1	1	X	X	0	1	0
0	0	0	1	0	1	X	X	0	1	0
0	0	1	0	0	1	X	X	0	1	0
0	1	0	0	0	1	X	X	0	1	0
1	0	0	0	0	1	X	X	0	1	0
0	0	0	1	1	X	X	X	0	1	1
0	0	1	0	1	X	X	X	0	1	1
0	1	0	0	1	X	X	X	0	1	1
1	0	0	0	1	X	X	X	0	1	1
0	0	1	1	X	X	X	X	1	0	0
0	1	0	1	X	X	X	X	1	0	0
1	0	0	1	X	X	X	X	1	0	0
0	1	1	X	X	X	X	X	1	0	1
1	0	1	X	X	X	X	X	1	0	1
1	1	X	X	X	X	X	X	1	1	0

$$Z_2 = A_4(A_5 + A_6 + A_7) + A_5(A_6 + A_7) + A_6A_7$$

$$Z_1 = A_2(A_3 + A_4 + A_5 + A_6 + A_7) + A_3(A_4 + A_5 + A_6 + A_7) + A_6A_7$$

$$Z_0 = A_1(A_2 + A_3 + A_4 + A_5 + A_6 + A_7) + A_3(A_4 + A_5 + A_6 + A_7) + A_5(A_6 + A_7)$$



Exercise 2.38

$$Y_6 = A_2A_1A_0$$

$$Y_5 = A_2A_1$$

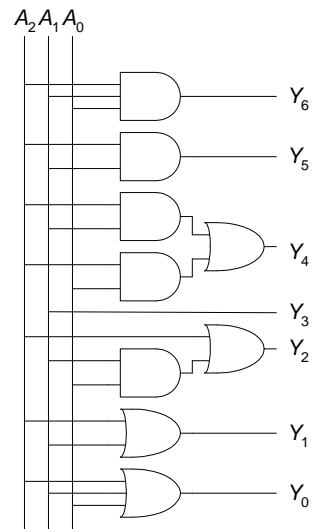
$$Y_4 = A_2A_1 + A_2A_0$$

$$Y_3 = A_2$$

$$Y_2 = A_2 + A_1A_0$$

$$Y_1 = A_2 + A_1$$

$$Y_0 = A_2 + A_1 + A_0$$



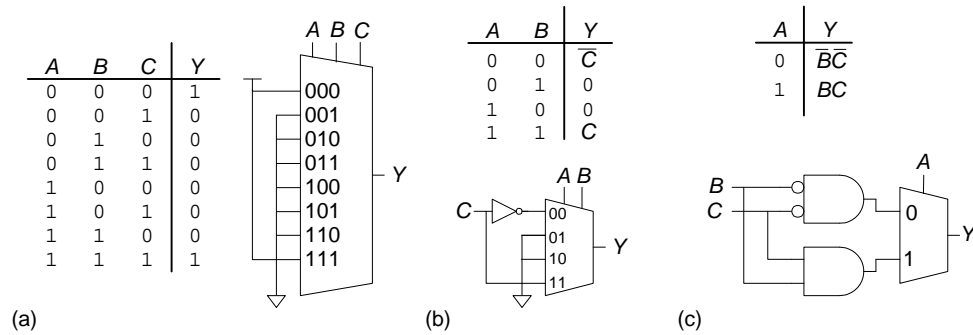
Exercise 2.39

$$Y = A + \overline{C \oplus D} = A + CD + \overline{C}\overline{D}$$

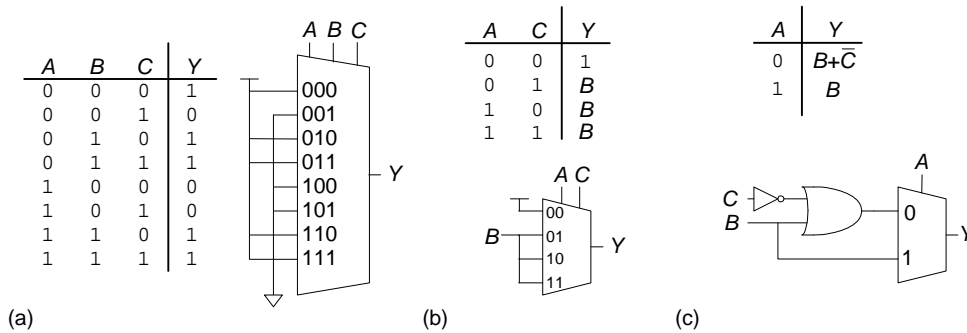
Exercise 2.40

$$Y = \overline{\overline{C}}\overline{\overline{D}}(A \oplus B) + \overline{\overline{A}}\overline{\overline{B}} = \overline{\overline{A}}\overline{\overline{C}}\overline{\overline{D}} + \overline{\overline{B}}\overline{\overline{C}}\overline{\overline{D}} + \overline{\overline{A}}\overline{\overline{B}}$$

Exercise 2.41



Exercise 2.42



Exercise 2.43

$$t_{pd} = 3t_{pd_NAND2} = \mathbf{60\ ps}$$

$$t_{cd} = t_{cd_NAND2} = \mathbf{15\ ps}$$

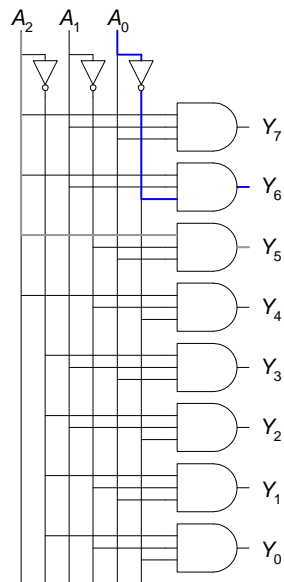
Exercise 2.44

$$\begin{aligned} t_{pd} &= t_{pd_AND2} + 2t_{pd_NOR2} + t_{pd_NAND2} \\ &= [30 + 2(30) + 20]\ ps \\ &= \mathbf{110\ ps} \end{aligned}$$

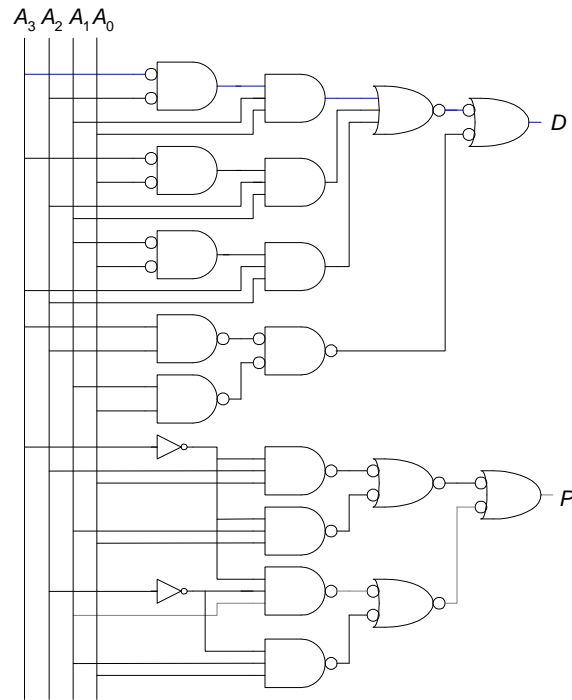
$$\begin{aligned} t_{cd} &= 2t_{cd_NAND2} + t_{cd_NOR2} \\ &= [2(15) + 25]\ ps \\ &= \mathbf{55\ ps} \end{aligned}$$

Exercise 2.45

$$\begin{aligned}t_{pd} &= t_{pd_NOT} + t_{pd_AND3} \\&= 15 \text{ ps} + 40 \text{ ps} \\&= \mathbf{55 \text{ ps}} \\t_{cd} &= t_{cd_AND3} \\&= \mathbf{30 \text{ ps}}\end{aligned}$$



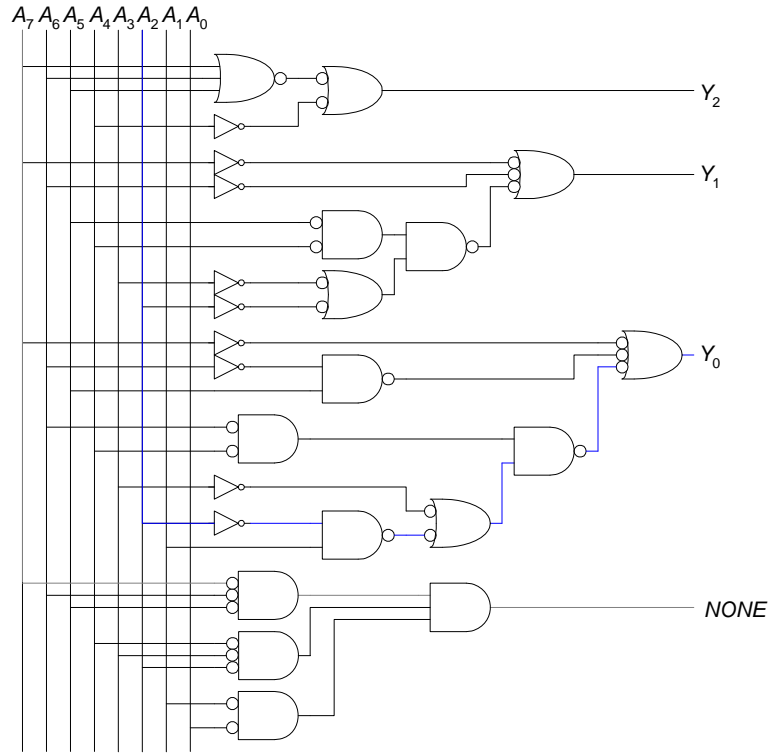
Exercise 2.46



$$\begin{aligned} t_{pd} &= t_{pd_NOR2} + t_{pd_AND3} + t_{pd_NOR3} + t_{pd_NAND2} \\ &= [30 + 40 + 45 + 20] \text{ ps} \\ &= \mathbf{135 \text{ ps}} \end{aligned}$$

$$\begin{aligned} t_{cd} &= 2t_{cd_NAND2} + t_{cd_OR2} \\ &= [2(15) + 30] \text{ ps} \\ &= \mathbf{60 \text{ ps}} \end{aligned}$$

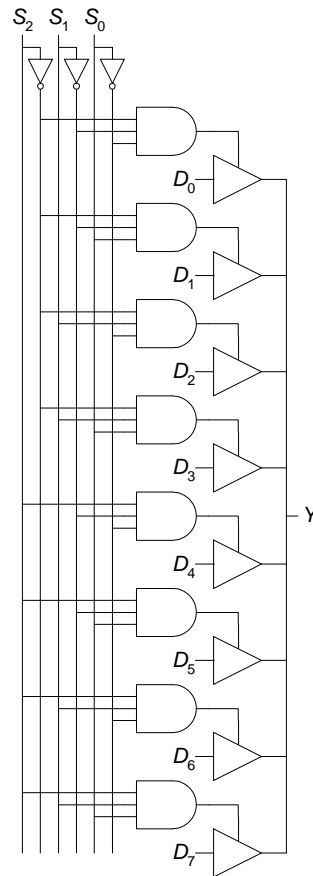
Exercise 2.47



$$\begin{aligned} t_{pd} &= t_{pd_INV} + 3t_{pd_NAND2} + t_{pd_NAND3} \\ &= [15 + 3(20) + 30] \text{ ps} \\ &= \mathbf{105 \text{ ps}} \end{aligned}$$

$$\begin{aligned} t_{cd} &= t_{cd_NOT} + t_{cd_NAND2} \\ &= [10 + 15] \text{ ps} \\ &= \mathbf{25 \text{ ps}} \end{aligned}$$

Exercise 2.48



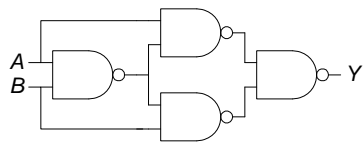
$$t_{pd_dy} = t_{pd_TRI_AY} \\ = \mathbf{50 \text{ ps}}$$

Note: the propagation delay from the control (select) input to the output is the circuit's critical path:

$$t_{pd_sy} = t_{pd_NOT} + t_{pd_AND3} + t_{pd_TRI_SY} \\ = [30 + 80 + 35] \text{ ps} \\ = \mathbf{145 \text{ ps}}$$

However, the problem specified to minimize the delay from data inputs to output, t_{pd_dy} .

Question 2.1



Question 2.2

Month	A_3	A_2	A_1	A_0	Y
Jan	0	0	0	1	1
Feb	0	0	1	0	0
Mar	0	0	1	1	1
Apr	0	1	0	0	0
May	0	1	0	1	1
Jun	0	1	1	0	0
Jul	0	1	1	1	1
Aug	1	0	0	0	1
Sep	1	0	0	1	0
Oct	1	0	1	0	1
Nov	1	0	1	1	0
Dec	1	1	0	0	1

Y \ $A_{3:2}$ \ $A_{1:0}$	00	01	11	10
00	X	0	1	1
01	1	1	X	0
11	1	1	X	0
10	0	0	X	1

$$Y = \bar{A}_3 A_0 + A_3 \bar{A}_0 = A_3 \oplus A_0$$


```
graph LR
    A3 --- XOR
    A0 --- XOR
    XOR --- Y
```

Question 2.3

A tristate buffer has two inputs and three possible outputs: 0, 1, and Z. One of the inputs is the data input and the other input is a control input, often called the *enable* input. When the enable input is 1, the tristate buffer transfers the data input to the output; otherwise, the output is high impedance, Z. Tristate buffers are used when multiple sources drive a single output at different times. One and only one tristate buffer is enabled at any given time.

Question 2.4

(a) An AND gate is not universal, because it cannot perform inversion (NOT).

(b) The set {OR, NOT} is universal. It can construct any Boolean function. For example, an OR gate with NOT gates on all of its inputs and output performs the AND operation. Thus, the set {OR, NOT} is equivalent to the set {AND, OR, NOT} and is universal.

(c) The NAND gate by itself is universal. A NAND gate with its inputs tied together performs the NOT operation. A NAND gate with a NOT gate on its output performs AND. And a NAND gate with NOT gates on its inputs performs OR. Thus, a NAND gate is equivalent to the set {AND, OR, NOT} and is universal.

Question 2.5

A circuit's contamination delay might be less than its propagation delay because the circuit may operate over a range of temperatures and supply voltages, for example, 3-3.6 V for LVCMOS (low voltage CMOS) chips. As temperature increases and voltage decreases, circuit delay increases. Also, the circuit may have different paths (critical and short paths) from the input to the output. A gate itself may have varying delays between different inputs and the output, affecting the gate's critical and short paths. For example, for a two-input NAND gate, a HIGH to LOW transition requires two nMOS transistor delays, whereas a LOW to HIGH transition requires a single pMOS transistor delay.

