# Where are You at? Level Setting Supplier Cybersecurity Maturity

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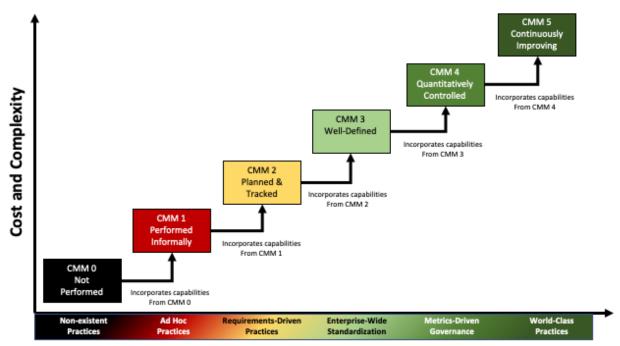
In <u>AVCMDS</u>: <u>Autonomous Vehicle Cybersecurity Manufacturer Disclosure Statement</u>, I introduced the **AVCMDS** as a way to help AV companies get a snapshot of a supplier's current capabilities. In this post we'll consider a way to establish how mature a supplier's development cybersecurity is.

### Measuring Capability

The method we've chosen is the **Capability Maturity Model** <sup>[1]</sup>. Developed in the mid-1980s for the US Department of Defense, this quantizes maturity into five (really six, when you add the true zero) levels. These are:

Level	Title	Description
0	Not Performed	No activities performed
1	Initial	Ad hoc, undocumented activities
2	Repeatable	Documented activities
3	Defined	Activities aligned to defined business processes
4	Capable	Activities managed via well-defined metrics
5	Efficient	Activity management includes process improvement

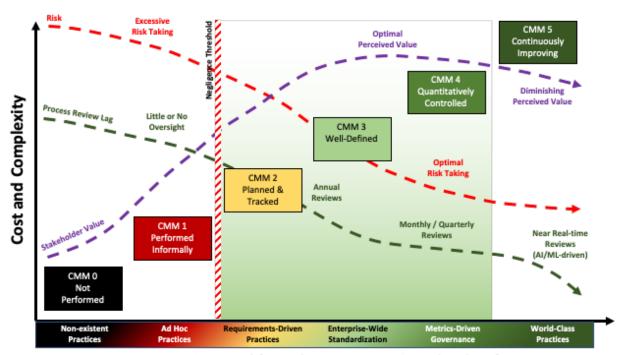
We can visualize this (and show relative cost / complexity) as follows:



Maturity Level (People, Processes & Technology)

Figure 1 - Capability Maturity Levels [5]

We can enhance this diagram with information showing the interplay between the CMM levels and risk, process review lag, and shareholder value.



Maturity Level (People, Processes & Technology)

Figure 2 - Capability Maturity Sweet Spot [5]

Here we see a "sweet spot," where perceived shareholder value is high, and both risk and process review lag are low. We can also see a negligence threshold between levels 1 and 2. This gives us an effective floor for desired maturity. At the other end of the spectrum, we can see that beyond level 4 the perceived value and risk level off. This is not to say that it is not desirable to attain level 5, but that one should recognize that there will likely be a high level of internal friction involved given the associated cost and complexity increase.

## **Covering Your Basis**

With an understanding of how we're going to quantify maturity, we turn to what we will measure. We will use the AVCDL [2] as the basis for evaluation. We use the AVCDL because it is built around the development lifecycle rather than any specific certification standard. This is because the AVCDL generalizes the cybersecurity needs of standards such as ISO 21434, ISO 26262, and UNECE WP.29 R155. So long as the AVCDL can be shown to satisfy any arbitrary certification standard, the maturity information will be transferable.

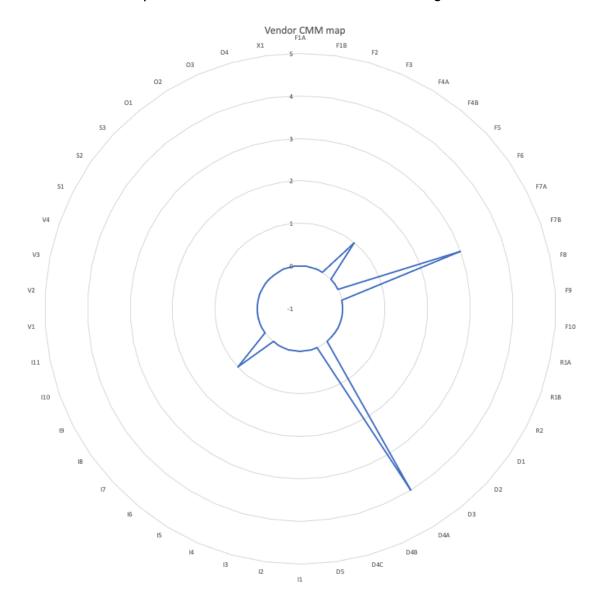
# **Know Thy Self**

The maturity information is gathered via a simple spreadsheet (AVCDL CMM) [6].

Phase Requirement	Description	Work Product	CMM Level	Notes
Foundation-1		training catalog	0 - none	
	Training	system to track training participation	0 - none	
Foundation-2	Roles and Responsibilities	roles and responsibilities document	0 - none	
Foundation-3	Toolchain Support	list of approved tools and components	0 - none	
	**	global security goals	0 - none	
Foundation-4 Foundation-5	Definition of Security Requirements	global security requirements	0 - none	
Foundation-5	Protect the Code	code protection plan	0 - none	
Foundation-6	Ensure Release Integrity	release integrity plan	0 - none	
Foundation-7	Incident December 1989	incident response plan	0 - none	
	Incident Response Plan	continuous monitoring plan	0 - none	
Foundation-8	Decommissioning Plan	decommissioning plan	0 - none	
Foundation-9	Threat Prioritization Plan	threat prioritization plan	0 - none	
Foundation-10	Deployment Plan	deployment plan	0 - none	
		Tour de set formet annuales annuales	0	
Requirements-1	Definition of Security Requirements	product-level security goals	0 - none	
Requirements-2	Requirements Gate	product-level security requirements	0 - none 0 - none	
Kequirements-2	Requirements Gate	formal gate signoff	U - none	
Design-2 Design-3 Design-4	Take Security Requirements and Risk Information into Account During Software Design	design showing security considerations	0 - none	
	Review the Software Design to Verify Compliance with Security Requirements and Risk Information	security design review report	0 - none	
	Attack Surface Reduction	attack surface analysis report	0 - none	
		threat modeling report	0 - none	
	Threat Modeling	ranked/risked threat report	0 - none	
		threat report	0 - none	
Design-5	Design Gate	formal gate signoff	0 - none	
Implementation-1	Use Approved Tools	list of tools and components used	0 - none	
Implementation-2	Configure the Compilation and Build Process to Improve Executable Security	build process documentation	0 - none	
Implementation-3	Configure the Software to Have Secure Settings by Default	secure setting document	0 - none	
Implementation-4	Reuse Existing, Well-Secured Software When Feasible Instead of Duplicating Functionality	component/version - product/version cross-reference document	0 - none	
Implementation-5	Create Source Code Adhering to Secure Coding Practice	secure development	0 - none	
Implementation-6	Deprecate Unsafe Functions			
	Deprecate Unsare Functions	currently used deprecated functions document	0 - none	
Implementation-7	Static Analysis	static analysis report	0 - none 0 - none	
Implementation-7 Implementation-8	April 1 and			
	Static Analysis	static analysis report	0 - none	
Implementation-8	Static Analysis Dynamic Program Analysis	static analysis report dynamic analysis report	0 - none 0 - none	
Implementation-8 Implementation-9	Static Analysis Dynamic Program Analysis Security Code Review	static analysis report dynamic analysis report secure code review summary	0 - none 0 - none 0 - none	
Implementation-8 Implementation-9 Implementation-10 Implementation-11	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff	0 - none 0 - none 0 - none 0 - none 0 - none	
Implementation-8 Implementation-9 Implementation-10 Implementation-11 Verification-1	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report	0 - none 0 - none 0 - none 0 - none 0 - none 0 - none	
Implementation-8 Implementation-9 Implementation-10 Implementation-11 Verification-1 Verification-2	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model	0 - none 0 - none 0 - none 0 - none 0 - none 0 - none	
Implementation-8 Implementation-9 Implementation-10 Implementation-11 Verification-1 Verification-2 Verification-3	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis	0 - none 0 - none 0 - none 0 - none 0 - none 0 - none 0 - none	
Implementation-8 Implementation-9 Implementation-10 Implementation-11 Verification-1 Verification-2 Verification-3 Verification-4	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review Verification Gate	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis formal gate signoff	0 - none 0 - none	
Implementation-8 Implementation-9 Implementation-10 Implementation-11 Verification-1 Verification-2 Verification-3 Verification-4	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review Verification Gate Final Security Review	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis formal gate signoff final security review report	0 - none	
implementation-8 implementation-19 implementation-10 implementation-11 Verification-2 Verification-3 Verification-4 Release-1 Release-2	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review Verification Gate Final Security Review Archive	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis formal gate signoff final security review report archive manifest	0 - none	
Implementation-8 Implementation-9 Implementation-10 Implementation-11 Verification-1 Verification-2 Verification-3 Verification-4	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review Verification Gate Final Security Review	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis formal gate signoff final security review report	0 - none	
implementation-8 implementation-19 implementation-10 implementation-11 Verification-2 Verification-3 Verification-4 Release-1 Release-2	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review Verification Gate Final Security Review Archive	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis formal gate signoff final security review report archive manifest	0 - none	
implementation-8 implementation-19 implementation-10 implementation-11 Verification-2 Verification-3 Verification-4 Release-1 Release-2 Release-3	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review Verification Gate Final Security Review Archive Release Gate	static analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis formal gate signoff final security review report archive manifest	0 - none	
Implementation-8 Implementation-19 Implementation-10 Implementation-11 Verification-1 Verification-2 Verification-3 Verification-4 Release-1 Release-2 Release-3 Operation-1	Static Analysis Dynamic Program Analysis Security Code Review Fuzz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review Verification Gate Final Security Review Archive Release Gate Identify and Confirm Vulnerabilities on an Ongoing Basis	static analysis report dynamic analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis formal gate signoff  final security review report archive manifest formal gate signoff	0 - none	
Implementation-8 Implementation-9 Implementation-10 Implementation-11 Verification-1 Verification-2 Verification-3 Verification-4 Release-1 Release-2 Release-3 Operation-1 Operation-2	Static Analysis Dynamic Program Analysis Security Code Review Fuz Testing Implementation Gate Penetration Testing Threat Model Review Attack Surface Analysis Review Verification Gate Final Security Review Archive Release Gate Identify and Confirm Vulnerabilities on an Ongoing Basis Assess and Prioritize the Remediation of all Vulnerabilities	static analysis report dynamic analysis report dynamic analysis report secure code review summary fuzz testing report formal gate signoff penetration testing report updated threat model updated attack surface analysis formal gate signoff  final security review report archive manifest formal gate signoff	0 - none	

This spreadsheet covers all **AVCDL** phase requirements and their associated products. Suppliers are asked to self-report their maturity for each of the phase requirement products. They may also provide notes for each.

We can then take the provided information a render a radar diagram of the answers.



## Trust, but Verify

It's really important to sanity-check the self-reported maturity values. If a supplier asserts that they are at level 1 (ad hoc activity), ask to see the products. If the supplier asserts that an activity has a maturity of level 2 (documented activity), ask to see the documentation. These are things that will come up in an audit, so suppliers should have no excuse in producing material to support their claims. Additionally, this information will be used in the creation of the cybersecurity interface agreement, so accuracy matters. An activity with a maturity level of 0 would necessitate that either the customer or a third party be used in order to ensure that the activity is properly handled.

#### How the AVCMDS and AVCDL CMM Differ

Where the AVCMDS gave us a snapshot of cybersecurity technical posture, the AVCDL CMM gives us insight into the cybersecurity process posture. It shows us supplier strengths and weaknesses with respect to supporting their cybersecurity efforts. This is critical in determining whether a supplier is capable of undertaking the activities necessary to ensure the cybersecurity of the element they would provide. It is also a major input into the cybersecurity interface agreement.

#### **Bringing Things Together**

In the next post in this series, we'll consider how to establish the division of responsibilities using a cybersecurity interface agreement. We'll also show how the AVCMDS and AVCDL CMM inform the completion of that document.

#### References

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