



Crestron International: CG-EIB Switch

This module represents one EIB group address of data type SWITCH

GENERAL INFORMATION

SIMPLWINDOWS NAME:	CG-EIB Switch V4.0.umc
CATEGORY:	Device Interface
VERSION:	V4.0
SUMMARY:	This module represents one EIB group address of data type SWITCH
GENERAL NOTES:	<p>Since the release of the CG-EIB's new firmware (4.xx) it's no longer needed to poll the CG-EIB for changes. The new firmware will handle this polling itself and report any changes directly and automatically to the Crestron processor.</p> <p>Therefore we revised the CG-EIB macro. Please see the help file for the "CG-EIB IO V4.1.umc" module or the new CG-EIB manual for detailed information.</p> <p>Together with the poll handling we changed the way of specifying which group address you want to control. In the old macro you had to specify the group address by mentioning each byte (main group, middle group, sub group high and sub group low) in their hexadecimal form. This new macro allows you to enter the group address as it is stated in the EIB software. I.e. if your group address is "12/3/255", you copy this exact sequence in the module's "Group Address" parameter. The parameter also allows 2-level group addresses.</p>
CRESTRON HARDWARE REQUIRED:	2-Series processor with com port
SETUP OF CRESTRON HARDWARE:	<p>The demo program was written for a PRO2 with TPS-15</p> <p>The CG-EIB is controlled over RS-232.</p> <p>The old CG-EIB firmware only communicated at baud rate 9600. The new firmware allows you to choose between 9600 and 38400. Crestron strongly advises to use the 38400 baud rate. To set up this baud rate, in the "Crestron KNX-Gate" software go to "setup" – "gateway setup" and pick the desired baud rate.</p>
VENDOR FIRMWARE:	V4.1
VENDOR SETUP:	CG-EIB connected to the EIB bus
CABLE DIAGRAM:	<p>The diagram illustrates the wiring between the Crestron processor and the CG-EIB module. The Crestron processor connector on the left has pins numbered 1 through 8. The CG-EIB module connector on the right also has pins numbered 1 through 8. The connections are as follows: Processor pin 1 connects to EIB pin 5; Processor pin 2 connects to EIB pin 4; Processor pin 3 connects to EIB pin 3; Processor pin 4 connects to EIB pin 2; Processor pin 5 connects to EIB pin 1; Processor pin 6 connects to EIB pin 8; Processor pin 7 connects to EIB pin 7; and Processor pin 8 connects to EIB pin 6.</p>



CONTROL:

EIB_FB	S	To be connected with the "EIB_Fb" output of the "CG-EIB IO V4.1.umc" module
On	D	Pulse to turn the group address on
Off	D	Pulse to turn the group address off

FEEDBACK:

EIB_CMD	S	To be connected with the "EIB_Cmd" input of the "CG-EIB IO V4.1.umc" module
Status	D	High when the group address is on

PARAMETERS:

Group Address	S	Specify the group address to control. For example: "1/1/15"

TESTING:

OPS USED FOR TESTING:	V3.155.1143
COMPILER USED FOR TESTING:	V2.08.26
SAMPLE PROGRAM:	CG-EIB V4.1 Demo Program
REVISION HISTORY:	CG-EIB V4.0 modules: Created for the new CG-EIB firmware 4.xx CG-EIB V4.1 modules: Fixes and improvements