```
/**
 1
 2
 3
        @copyright © 2010 - 2020, Fraunhofer-Gesellschaft zur Foerderung der
 4
      * angewandten Forschung e.V. All rights reserved.
 5
 6
      * BSD 3-Clause License
 7
      * Redistribution and use in source and binary forms, with or without
 8
      * modification, are permitted provided that the following conditions are met:
 9
      * 1. Redistributions of source code must retain the above copyright notice,
10
            this list of conditions and the following disclaimer.
11
       2. Redistributions in binary form must reproduce the above copyright
12
            notice, this list of conditions and the following disclaimer in the
13
            documentation and/or other materials provided with the distribution.
14
      * 3. Neither the name of the copyright holder nor the names of its
15
            contributors may be used to endorse or promote products derived from
16
            this software without specific prior written permission.
17
18
      * THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS "AS IS"
19
      * AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE
20
      * IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE
21
      * ARE DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT HOLDER OR CONTRIBUTORS BE
22
      * LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR
23
      * CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF
24
      * SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS
25
      * INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN
26
      * CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE)
27
      * ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE
28
      * POSSIBILITY OF SUCH DAMAGE.
29
30
      * We kindly request you to use one or more of the following phrases to refer
31
      * to foxBMS in your hardware, software, documentation or advertising
32
      * materials:
33
34
      * ″ This product uses parts of foxBMS&req; ″
35
36
      * ″ This product includes parts of foxBMS® ″
37
38
      * ″ This product is derived from foxBMS&req; ″
39
40
      */
41
     /**
42
43
      * @file
               can cfq.h
44
      * @author foxBMS Team
      * @date
              12.07.2015 (date of creation)
45
46
      * @ingroup DRIVERS CONF
      * @prefix CAN
47
48
49
      * @brief
                Headers for the configuration for the CAN module
50
      * The activation and the length of the message buffers as well as the number of
51
52
      * the messages that are received are to be configured here
```

```
53
54
    * /
55
56
    #ifndef CAN CFG H
57
    #define CAN CFG H
58
    /*----*/
59
60
    #include "general.h"
61
62
    #include "cpu_cfg.h"
63
    #include "io.h"
64
    /*========= Macros and Definitions ===========*/
65
66
67
    #define CAN 0 TRANS STANDBY CONTROL IO PIN CAN 0 TRANS STANDBY CONTROL
    #define CAN 1 TRANS STANDBY CONTROL IO PIN CAN 1 TRANS STANDBY CONTROL
68
69
    71
     * CAN BUFFER OPTIONS
     72
73
74
    * @ingroup CONFIG_CAN
75
     * CANO bus baudrate. CAN peripheral prescaler and time quantums on microcontroller
76
     * will be configured accordingly. See STM32 Reference Manual p. 1097 for more
77
78
    * \par Type:
79
    * select(4)
   * \par Default:
80
    * 1
81
    * /
82
83
84 /**
* defines the BAUD rate of the CANO
86 */
87 /* #define CANO_BAUDRATE (1000000U) */
#define CANO_BAUDRATE (500000U)
    /* #define CANO_BAUDRATE (250000U) */
    /* #define CANO BAUDRATE (125000U) */
90
91
   /**
92
93
    * @ingroup CONFIG CAN
94
     * CAN1 bus baudrate. CAN peripheral prescaler and time quantums on microcontroller
95
     * will be configured accordingly. See STM32 Reference Manual p. 1097 for more
96
97
    * \par Type:
98
    * select(4)
    * \par Default:
99
100
    * 1
101
    * /
102
103
104
    * defines the BAUD rate of the CAN1
```

```
105
106 /* #define CAN1_BAUDRATE (1000000U) */
107
     #define CAN1_BAUDRATE (500000U)
108
     /* #define CAN1_BAUDRATE (250000U) */
     /* #define CAN1_BAUDRATE (125000U) */
109
110
111
112
113
     /* CAN enabling */
114 /**
     * @ingroup CONFIG_CAN
115
     * Enables or disables CANO
116
      * \par Type:
117
118
      * int
     * \par Range:
119
120
      * x == 0 \text{ or } x == 1
121
      * \par Default:
      * 1
122
123
      */
124
      #define CAN_USE_CAN_NODE0
                                            (1U)
125
     /**
126
127
      * @ingroup CONFIG CAN
128
      * Enables or disables CAN1
129
      * \par Type:
130
      * int
131
      * \par Range:
      * x == 0 \text{ or } x == 1
132
      * \par Default:
133
134
      * 1
135
      * /
136
      #define CAN_USE_CAN_NODE1 (1U) CAN 1 is enabled.
137
138
    /**
139
      * @ingroup CONFIG_CAN
140
      * Enables or disables transmitter standby control
141
      * \par Type:
142
      * int
143
      * \par Range:
      * x == 0 \text{ or } x == 1
144
145
      * \par Default:
146
      * 1
147
148
                                      (1U)
      #define CAN_USE_STANDBY_CONTROL
149
150
      /* transmit buffer */
     /**
151
152
      * @ingroup CONFIG CAN
      * Enables or disables CANO transmit buffer
153
154
      * \par Type:
155
      * int
156
       * \par Range:
```

```
* x == 0 \text{ or } x == 1
157
158
      * \par Default:
159
     * 1
160
      * /
161
     #define CAN0_USE_TRANSMIT_BUFFER
                                         (1U)
162
163
      * @ingroup CONFIG_CAN
164
      * Defines CANO transmit buffer length
165
      * \par Type:
166
      * int
167
      * \par Range:
      * 0 < x
168
      * \par Default:
169
     * 16
170
171
     * /
172
      #define CANO_TRANSMIT_BUFFER_LENGTH (24U)
173
174
     /**
175
     * @ingroup CONFIG_CAN
176
     * Enables or disables CAN1 transmit buffer
177
      * \par Type:
178
      * int
179
      * \par Range:
180
      * x == 0 \text{ or } x == 1
181
      * \par Default:
182
      * 1
183
      * /
184
     #define CAN1_USE_TRANSMIT_BUFFER (1U)
     /**
185
186
     * @ingroup CONFIG_CAN
187
     * Defines CAN1 transmit buffer length
188
      * \par Type:
189
      * int
190
      * \par Range:
191
      * 0 < x
192
      * \par Default:
      * 16
193
      * /
194
195
     #define CAN1_TRANSMIT_BUFFER_LENGTH (16U)
196
197
     /* receive buffer */
    /**
198
199
      * @ingroup CONFIG_CAN
200
       * Enables or disables CANO receive buffer
      * \par Type:
201
202
      * int
      * \par Range:
203
204
      * x == 0 \text{ or } x == 1
205
      * \par Default:
206
      * 1
      * /
207
208
      #define CANO_USE_RECEIVE_BUFFER
                                      (1U)
```

```
209 /**
* @ingroup CONFIG_CAN
211
     * Defines CANO receive buffer length
212
      * \par Type:
213
      * int
214
     * \par Range:
215
     * 0 < x
216
      * \par Default:
217
     * 16
218
     * /
219
     #define CANO_RECEIVE_BUFFER_LENGTH
                                         (16U)
220
221
     /**
222
     * @ingroup CONFIG_CAN
223
     * Enables or disables CAN1 receive buffer
224
      * \par Type:
225
      * int
226
     * \par Range:
227
     * x == 0 \text{ or } x == 1
228
     * \par Default:
229
     * 1
230
     * /
231
     #define CAN1_USE_RECEIVE_BUFFER
                                      (1U)
232
233
     * @ingroup CONFIG_CAN
     * Defines CAN1 receive buffer length
234
235
      * \par Type:
236
      * int
     * \par Range:
237
238
      * 0 < x
239
     * \par Default:
240
     * 16
241
242
     #define CAN1 RECEIVE BUFFER LENGTH
                                          (16U)
243
244
245
     /* Number of messages that will bypass the receive buffer and will be interpreted right on reception.
      * Set the respective IDs and implement the wished functionality either in individual callback
246
      * function or in default STD_RETURN_TYPE_e CAN_BufferBypass(CAN_NodeTypeDef_e canNode, uint32_t msqID,
2.47
248
      * uint8_t* data, uint8_t DLC, uint8_t RTR) function in the can.c file. Use bypassing only for
249
       * important messages because of handling during ISR */
     /**
250
      * @ingroup CONFIG_CAN
251
252
      * Defines number of RX messages that bypass receive buffer on CANO bus
253
      * \par Type:
254
      * int
255
      * \par Range:
256
     * 0 <= x
257
      * \par Default:
258
      * 0
259
     * /
260
     #define CANO BUFFER BYPASS NUMBER OF IDs (1U)
```

```
261
262
      /**
263
      * @ingroup CONFIG_CAN
264
       * Defines number of RX messages that bypass receive buffer on CAN1 bus
265
       * \par Type:
266
      * int
267
      * \par Range:
268
      * 0 <= x
269
      * \par Default:
      * 0
271
      * /
272
      #define CAN1 BUFFER BYPASS NUMBER OF IDs (OU)
273
274
      /**
275
      * @ingroup CONFIG CAN
276
       * Defines CAN message ID to perform a software reset
277
      * \par Type:
278
       * int
279
      * \par Default:
280
      * 351
281
      * /
282
     #define CAN_ID_SOFTWARE_RESET_MSG
                                                               (0x95U)
283
      /**
284
285
      * @ingroup CONFIG_CAN
286
      * When enabled unique device ID is need as can data to perform SW reset
287
       * \par Type:
288
       * int
289
      * \par Range:
290
       * [0,1]
291
      * \par Default:
2.92
      * 0
      */
293
294
      /* #define CAN SW RESET WITH DEVICE ID (01) */
295
      #define CAN_SW_RESET_WITH_DEVICE_ID (OU)
                                                               Any device can reset foxBMS.
296
297
      typedef struct CAN_MSG_RX_TYPE {
298
          uint32 t ID; /*!< message ID */</pre>
299
          uint32_t mask; /*! < mask or 0x0000 to select list mode */.
          uint8 t DLC; /*!< data length */</pre>
                                                                        Remote transmission
                                                                                              Must be dominant (0) for data frames and recessive (1) for remote
                                                                        request (RTR) (blue)
301
          uint8 t RTR; /*!< rtr bit */</pre>
                                                                                              request frames (see Remote Frame, below)
302
          uint32_t fifo; /*! < selected CAN hardware (CAN_FILTER_FIF00 or CAN_FILTER_FIF01) */
303
          STD_RETURN_TYPE_e (*func)(uint32_t ID, uint8_t*, uint8_t, uint8_t); /*!< callback function */
      } CAN_MSG_RX_TYPE_s;
304
305
306
307
      typedef uint32 t (*can callback funcPtr) (uint32 t idx, void * value);
308
      /**
309
310
      * type definition for structure of a CAN message with its
311
       * ID,
312
       * data length code,
```

```
313
       * repetition rate (stated in number of calls of CANS mainfunction = ticks),
314
       * the initial phase.
315
      * a callback function if transfer of TX message to CAN module is successful
316
       * /
317
     typedef struct {
318
         uint32_t ID;
                                         /*!< CAN message id */</pre>
319
         uint8_t DLC;
                                         /*! < CAN message data length code */
         uint8_t DLC;
uint32_t repetition_time;
                                        /*!< CAN message cycle time */</pre>
320
         uint32 t repetition phase; /*!< CAN message startup (first send) offset */
321
322
          can_callback_funcPtr cbk_func; /*!< CAN message callback after message is sent or received */</pre>
323
     } CAN_MSG_TX_TYPE_s;
324
325
     typedef struct CanPdu {
                                     PDU stands for Protocol Data Unit (i.e. Message Format).
326
         uint8_t sdu[8];
                                     SDU stands for Service Data Unit
327
         uint32_t id;
328
         uint8 t dlc;
329
     } Can_PduType;
330
331
332
      /*======= Constant and Variable Definitions =======*/
333
      extern CAN_HandleTypeDef hcan0;
334
     extern CAN_HandleTypeDef hcan1;
335
      extern CAN MSG RX TYPE s can0 RxMsqs[];
336
      extern CAN_MSG_RX_TYPE_s can1_RxMsqs[];
337
      extern uint32_t can0_bufferBypass_RxMsqs[CAN0_BUFFER_BYPASS_NUMBER_OF_IDs];
338
      extern uint32 t can1 bufferBypass RxMsqs[CAN1 BUFFER BYPASS NUMBER OF IDs];
      extern const CAN_MSG_TX_TYPE_s can_CANO_messages_tx[];
339
340
      extern const CAN_MSG_TX_TYPE_s can_CAN1_messages_tx[];
341
342
343
     /**
344
      * array length for receiving CANO message definition
345
346
      extern const uint8 t can CANO rx length;
347
      /**
348
349
      * array length for receiving CAN1 message definition
350
      * /
351
      extern const uint8_t can_CAN1_rx_length;
352
      /**
353
354
      * array length for transmission CANO message definition
355
356
      extern const uint8 t can CANO tx length;
357
      /**
358
359
      * array length for transmission CAN1 message definition
360
361
      extern const uint8_t can_CAN1_tx_length;
362
363
      /*====== Function Prototypes =========*/
364
```