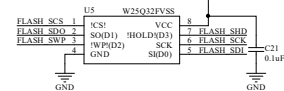


ESP32 & 0.96 Inch OLED display

### SPI FLASH



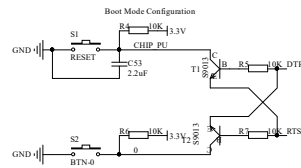
### Programming Logic

Auto-Reset

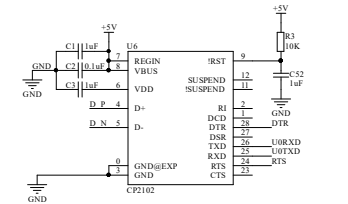
Pin	Default	Boot	Download
GP00	1	1	0
U0TXD	1	1	x
GP02	0	x	0
GP04	0	x	x
MTDO	1	x	x
GP05	1	1	x

If U0TXD, GP02, GP05 are floating, GP00 determines boot mode.

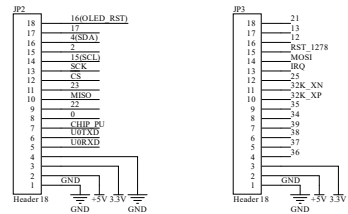
If DTR is LOW, toggling RTS from HIGH to LOW resets to run mode.  
 If RTS is HIGH, toggling DTR from LOW to HIGH resets to bootloader.



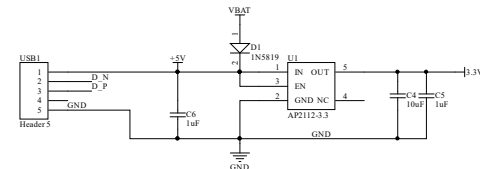
### CP2102



### Header



### 3.3V LDO



### Charging circuit

