Tabelle1

Port/Pin	Perepherie	Angeschlossene Hardware	Funktion
P1.0	TA0CLK/ACLK	LED1 – onboard	GPIO
P1.1	TA0.0	SW2 – onboard	GPIO
P1.2	TA0.1		PWM-out
P1.3	TA0.2	RGB-LED	PWM-out
P1.4	TA0.3		PWM-out
P1.5	TA0.4	Lüfter	PWM-out
P1.6	TA1CLK/CBOUT		Pulse-in
P1.7	TA1.0	Back-Channel-UART	GPIO – CTS
P2.0	TA1.1	Taster – Interrupt	GPIO
P2.1	TA1.2	SW1 – onboard	GPIO
P2.2	TA2CLK/SMCLK		GPIO
P2.3	TA2.0	Taster - Input	GPIO
P2.4	TA2.1		GPIO
P2.5	TA2.2		GPIO
P2.6	RTCCLK/DMAE0	SPI – Adresse	GPIO
P2.7	UCA0CLK/UCB0STE		GPIO
P3.0	UCB0SIMO/SDA		UCB0SIMO
P3.1	UCB0SOMI/SCL	Periphal-SPI MicroSD, Funk, etc	UCB0SOMI
P3.2	UCB0CLK/UCA0STE		UCB0CLK
P3.3	UCA0TXD/UCA0SIMO	Peripheral-UART	UCA0TXD
P3.4	UCA0RXD/UCA0SOMI	GPS, WiFi, etc	UCA0RXD
P3.5	TB0.5		GPIO
P3.6	TB0.6	Taster-LEDs	GPIO
P3.7	TB0OUTH/SVMOUT		GPIO
P4.0	PM0	NRF24l01 – CE	GPIO
P4.1	PM1	Periphal I2C	UCB1SDA
P4.2	PM2	Grove Connector	UCB1SCL
P4.3	PM3	NRF24l01 – INT	TB0CCR3
P4.4	PM4	Back-Channel-UART	UCA1TXD
P4.5	PM5		UCA1RXD
P4.7	PM7	LED2 – onboard	GPIO
P5.2	XT2IN	4MHz HF-Oszillator	HF-Clk
P5.3	XT2OUT		HF-Clk
P5.4	XIN	32kHz Uhrenquarz	LF-Clk
P5.5	XOUT		LF-Clk
P6.0	CB0/A0		GPIO – D4
P6.1	CB1/A1		GPIO – D5
P6.2	CB2/A2		GPIO – D6
P6.3	CB3/A3	LCD	GPIO – D7
P6.4	CB4/A4		GPIO – R/W
P6.5	CB5/A5		GPIO – RS
P6.6	CB6/A6		GPIO – E
P6.7	CB7/A7	Back-Channel-UART	GPIO – RTS
P7.0	CB8/A12	Analog – in	ADC12
P7.4	TB0.2	µSD – Card_detect	GPIO
P8.1		SPI – Adresse – Enable	GPIO
P8.2		LCD-LED	GPIO

Tabelle1

Port-Mapping Functions (Port 4)

Value	Name	Input-Function	Output-Function
0	PM_NONE	NONE	DVSS
1	PM_CBOUT0 PM_TB0CLK	- TB0 clock input	Comparator B Output -
2	PM_ADC12CLK PM_DMAE0	DMAE0 input	ADC12CLK
3	PM_SVMOUT TB0OUTH	TBO high impedance input	SVM output
4	PM_TB0CCR0A	TB0 CCR0 capture input A	TB0 CCR0 compare output
5	PM_TB0CCR1A	TB0 CCR1 capture input A	TB0 CCR1 compare output
6	PM_TB0CCR2A	TB0 CCR2 capture input A	TB0 CCR2 compare output
7	PM_TB0CCR3A	TB0 CCR3 capture input A	TB0 CCR3 compare output
8	PM_TB0CCR4A	TB0 CCR4 capture input A	TB0 CCR4 compare output
9	PM_TB0CCR5A	TB0 CCR5 capture input A	TB0 CCR5 compare output
10	PM_TB0CCR6A	TB0 CCR6 capture input A	TB0 CCR6 compare output
11	PM_UCA1RXD PM_UCA1SOMI	USCI_A1 UART RXD (Direction controlled by USCI – input) USCI_A1 SPI slave out master in (direction controlled by USCI)	
12	PM_UCA1TXD PM_UCA1SIMO	USCI_A1 UART TXD (Direction controlled by USCI – output) USCI_A1 SPI slave in master out (direction controlled by USCI)	
13	PM_UCA1CLK PM_UCB1STE	USCI_A1 clock input/output (direction controlled by USCI) USCI_B1 SPI slave transmit enable (direction controlled by USCI)	
14	PM_UCB1SOMI PM_UCB1SCL	USCI_B1 SPI slave out master in (direction controlled by USCI) USCI_B1 I2C clock (open drain and direction controlled by USCI)	
15	PM_UCB1SIMO PM_UCB1SDA	USCI_B1 SPI slave in master out (direction controlled by USCI) USCI_B1 I2C data (open drain and direction controlled by USCI)	
16	PM_UCB1CLK PM_UCA1STE	USCI_B1 clock input/output (direction controlled by USCI) USCI_A1 SPI slave transmit enable (direction controlled by USCI)	
17	PM_CBOUT1	NONE	Comparator B output
18	PM_MCLK	NONE	MCLK
19-30	Reserved	NONE	DVSS
31	PM_ANALOG	Open???	

auf dem Launchpad verbunden
und **nicht(!)** auf den Stiftleisten vorhanden
nicht herausgeführte Pins: 4.6, 5.0, 5.1, 5.6, 5.7, 7.1, 7.2, 7.3, 7.5, 7.6, 7.7, 8.0 , PJ.x