DaisySP

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Maps Daisy interface to STM32 HAL - I'd like to get all of this stuff tucked away somewhere inbetween the HAL, and User level So that I can start to slowly replace HAL stuff over time. Also I don't like that this throws a warning for every library file that doesn't use it... Possible solution: Move this to dsy_hal_interface.h - and the explicitly include it in the lower-level files that need it. Hardware related defines. keyboard switches shift register UART for

MIDI via TRS jacks on Field CD4051 Select Pin controls enums for controls, etc. All knobs connect to ADC1_INP10 via CD4051 mux Leaving non-cplusplus ver-

sion in place below so as not to break examples yet... Order of ADC Channels for accessing dsy_adc.h Mapping of LEDs via dsy_leddriver.h Hardware related

defines. Switches Encoder Extra Peripherals enums for controls, etc. All knobs connect to ADC1_INP10 via CD4051 mux This is a Board Specific File I don't

think it actually belongs in the library. Any new piece of hardware can just have their own board file. This will allow minor pin changes, etc. not to require changing the library in a million places. Specifies whether generic initialization will be done within the daisy_seed_init, or not. Allows for more selective init Probably should move this to a dsy_handle.h So that it can be used in the other peripheral initializations, etc. (E.g. Audio needs both SAI, and I2C for most devices.) THIS BREAKS WHEN ITS INLINED? WM8371 Codec support. SDRAM

for 32MB AS4C16M16SA (and 64MB equivalent). Thanks to whoever this awesome person is: http://main.lv/writeup/stm32f4_sdram_configuration.md The Init function is basically a copy and paste. He has references to timing, etc. for now we're configuring the RAM to run at 108MHz To use these the .sdram_data/_bss sections must be configured correctly in the LINKER SCRIPT. using BSS is advised for most things, since the DATA section must also fit in flash in order to be initialized. Determines whether chip is initialized, and activated. This is only the pins that can change on a board-to-board basis. Pins that have functions that cannot be moved to another pin will be hardcoded into the driver.

• SDNWE is the only pin that i've seen move, Fixed maximums for paral-

lel/daisy
chained use These could be expanded TODO Fix hard coding of
 $\,$

these parameters Usage: Using the dsy_switch_state(), will work with

no setup other than init. For edge, and time based functions, you'll have to call debounce() at a regular interval (i.e. 1ms) In order not to miss those events, the rising/falling edge checks should be made at the same frequency as the debounce() function.

Parameter

```
Simple parameter mapping tool that takes a 0-1 input from an hid_ctrl.
```

TODO: Move init and process to .cpp file - i was cool with them being in the h file until math.h got involved for the log stuff.

curve settings

Curves are applied to the output signal

```
PARAM_CURVE_LINEAR,
PARAM_CURVE_EXP,
PARAM_CURVE_LOG,
PARAM_CURVE_CUBE,
PARAM_CURVE_LAST,
};
```

parameter class

init

initialize a parameter using an hid_ctrl object.

hid_ctrl input - object containing the direct link to a hardware control source.

```
min - bottom of range. (when input is 0.0) max - top of range (when input is 1.0)
```

curve - the scaling curve for the input->output transformation.

```
inline void init(hid_ctrl input, float min, float max, uint8_t curve)
```

process

processes the input signal, this should be called at the sample rate of the hid_ctrl passed in.

returns a float with the specified transformation applied.

```
inline float process()
```

value

returns the current value from the parameter without processing another sample. this is useful if you need to use the value multiple times, and don't store the output of process in a local variable.

```
inline float value() { return val; }
```

PER I2C

Driver for controlling I2C devices

TODO:

- Add DMA support
- Add timing calc based on current clock source freq.
- Add discrete rx/tx functions (currently other drivers still need to call ST HAL functions).

Errata:

• 1MHZ (FastMode+) is currently only 886kHZ

Types

dsy_i2c_periph

Specifices the internal peripheral to use (these are mapped to different pins on the hardware).

```
typedef enum
{
    DSY_I2C_PERIPH_1,
    DSY_I2C_PERIPH_2,
    DSY_I2C_PERIPH_3,
    DSY_I2C_PERIPH_4,
} dsy_i2c_periph;
```

dsy_i2c_pin

List of pins associated with the peripheral. These must be set in the handle's pin_config.

```
typedef enum
{
    DSY_I2C_PIN_SCL,
    DSY_I2C_PIN_SDA,
    DSY_I2C_PIN_LAST,
} dsy_i2c_pin;
```

dsy_i2c_speed

Rate at which the clock/data will be sent/received. The device being used will have maximum speeds.

1 MHZ Mode is currently 886kHz

```
typedef enum
{
```

```
DSY_I2C_SPEED_100KHZ,
    DSY_I2C_SPEED_400KHZ,
    DSY_I2C_SPEED_1MHZ,
    DSY_I2C_SPEED_LAST,
} dsy_i2c_speed;
dsy_i2c_handle
this object will be used to initialize the I2C interface, and can be passed to dev_
drivers that require I2C.
typedef struct
    dsy_i2c_periph periph;
    dsy_gpio_pin pin_config[DSY_I2C_PIN_LAST];
    dsy_i2c_speed speed;
} dsy_i2c_handle;
Functions
dsy_i2c_init
initializes an I2C peripheral with the data given from the handle.
Requires a dsy_i2c_handle object to initialize.
void dsy_i2c_init(dsy_i2c_handle *dsy_hi2c);
dsy_i2c_hal_handle
Returns a pointer to the HAL I2C Handle, for use in device drivers.
I2C_HandleTypeDef *dsy_i2c_hal_handle(dsy_i2c_handle *dsy_hi2c);
```

Currently Sample Rates are not correctly supported. We'll get there. Sets clock

speeds, etc.