libDaisy

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Chapter 1

libdaisy

Multi-layer hardware abstraction library for Daisy Product family

On STM32H7 MCUs

Lower-levels use STM32 HAL (local copy w/ modifications in Drivers/)

Prefixes and their meanings:

- sys - System level configuration (clocks, dma, etc.)
- per - Peripheral level, internal to MCU (i2c, spi, etc.)
- dev - External device support (external flash chips, DACs, codecs, etc.)
- hid - User level interface elements (encoders, switches, audio, etc.)
- util - library level elements used within the library (not included via daisy.h)
- daisy - core API files (specific boards, platforms have extended user APIs that configure libdaisy more below).

Also included is a core/ folder containing:

- a generic Makefile that can be included in a project Makefile to simplify getting started
- a linker script for defining the sections of memory used by the firmware
- core files for starting the hardware (system_stm32h7xx.c, startup_stm32h750xx.s, etc.)

1.1 Using libdaisy

Due to the amount of hardware configuration and flexibility of the daisy platform, (in the present, and the future), a user can use libdaisy to define their own custom hardware, or include one of our supported board files to jumpstart the creativity, and hack on an existing piece of hardware.

If you are getting started, and have one of the Daisy Family Products, you can skip ahead to that section below.

1.1.1 daisy.h

The base-level include file. This is all you need to include to create your own custom hardware that uses libdaisy.

daisy_seed.h is an example of a board level file that utilizes libdaisy to define some hardware, and provide flexible access.

1.1.2 daisy_seed.h

The SOM-level include file. This can be used with any boards that use the Daisy Seed hardware.

Additional configuration files, with more specific hardware access are provided below for our supported hardware platforms.

1.1.3 daisy_platform.h

Several other pairs of files exist in the repo for each of the supported hardware platforms that work with Daisy Seed.

These are:

- daisy_field
- daisy_patch
- daisy_petal
- daisy_pod

With these files a number of additional initialization, and configuration is done by the library.

This allows a user to jump right into their new product with a simple api to do things without having a full understanding of what's going on under the hood.

With this flexible approach to the hardware configuration, we hope to promote a lot of fantastic hardware along with code to go with it.

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3.1 Class List

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Primarily designed for ADC input controls such as
potentiometers, and control voltage.
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Class that handles initializing all of the hardware specific to the Daisy Patch Board.
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Generic Class for handling Quadrature Encoders
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Configures Audio Device and provides callback for signal processing.
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Chapter 5

Module Documentation

5.1 USBD_CDC_IF

Usb VCP device module.

Modules

- USBD_CDC_IF_Exported_Defines

  Defines.
- USBD_CDC_IF_Exported_Types

  Types.
- USBD_CDC_IF_Exported_Macros

  Aliases.
- USBD_CDC_IF_Exported_Variables

  Public variables.
- USBD_CDC_IF_Exported_FunctionsPrototype

  Public functions declaration.

5.1.1 Detailed Description

Usb VCP device module.

5.2 USBD_CDC_IF_Exported_Defines

Defines.

Defines.

5.3 USBD_CDC_IF_Exported_Types

Types.

Typedefs

- typedef void(∗ CDC_ReceiveCallback) (uint8_t ∗buf, uint32_t ∗size)

5.3.1 Detailed Description

Types.

5.3.2 Typedef Documentation

5.3.2.1 CDC_ReceiveCallback

```
typedef void(* CDC_ReceiveCallback) (uint8_t *buf, uint32_t *size)
```

Parameters

buf	buffer
size	buffer size

5.4 USBD_CDC_IF_Exported_Macros

Aliases.

Aliases.

5.5 USBD_CDC_IF_Exported_Variables

Public variables.

Variables

- USBD_CDC_ItfTypeDef USBD_Interface_fops_FS
- USBD_CDC_ItfTypeDef USBD_Interface_fops_HS

5.5.1 Detailed Description

Public variables.

5.5.2 Variable Documentation

5.5.2.1 USBD_Interface_fops_FS

```
USBD_CDC_ItfTypeDef USBD_Interface_fops_FS
```

CDC Interface callback.

5.5.2.2 USBD_Interface_fops_HS

```
USBD_CDC_ItfTypeDef USBD_Interface_fops_HS
```

CDC Interface callback.

5.6 USBD_CDC_IF_Exported_FunctionsPrototype

Public functions declaration.

Functions

- void CDC_Set_Rx_Callback_FS (CDC_ReceiveCallback cb)
- uint8_t CDC_Transmit_FS (uint8_t *Buf, uint16_t Len)
- uint8_t CDC_Transmit_HS (uint8_t *Buf, uint16_t Len)

5.6.1 Detailed Description

Public functions declaration.

5.6.2 Function Documentation

5.6.2.1 CDC_Set_Rx_Callback_FS()

```
void CDC_Set_Rx_Callback_FS (
            CDC_ReceiveCallback cb )
```

&

5.6.2.2 CDC_Transmit_FS()

```
uint8_t CDC_Transmit_FS (
            uint8_t * Buf,
            uint16_t Len )
```

&

5.6.2.3 CDC_Transmit_HS()

```
uint8_t CDC_Transmit_HS (
            uint8_t * Buf,
            uint16_t Len )
```

&

5.7 USBD_CONF

Configuration file for Usb otg low level driver.

Modules

- USBD_CONF_Exported_Variables

  Public variables.
- USBD_CONF_Exported_Defines

  Defines for configuration of the Usb device.
- USBD_CONF_Exported_Macros

  Aliases.
- USBD_CONF_Exported_Types

  Types.
- USBD_CONF_Exported_FunctionsPrototype

  Declaration of public functions for Usb device.

5.7.1 Detailed Description

Configuration file for Usb otg low level driver.

5.8 USBD_CONF_Exported_Variables

Public variables.

Public variables.

5.9 USBD_CONF_Exported_Defines

Defines for configuration of the Usb device.

Macros

- #define USBD_MAX_NUM_INTERFACES 1U
- #define USBD_MAX_NUM_CONFIGURATION 1U
- #define USBD_MAX_STR_DESC_SIZ 512U
- #define USBD_SUPPORT_USER_STRING 0U
- #define USBD_DEBUG_LEVEL 3U
- #define USBD_LPM_ENABLED 0U
- #define USBD_SELF_POWERED 1U
- #define DEVICE_FS 0
- #define DEVICE_HS 1

5.9.1 Detailed Description

Defines for configuration of the Usb device.

5.9.2 Macro Definition Documentation

5.9.2.1 DEVICE_FS

```
#define DEVICE_FS 0
```

FS and HS identification

5.9.2.2 DEVICE_HS

```
#define DEVICE_HS 1
```

&

5.9.2.3 USBD_DEBUG_LEVEL

```
#define USBD_DEBUG_LEVEL 3U
```

&

5.9.2.4 USBD_LPM_ENABLED

```
#define USBD_LPM_ENABLED 0U
```

&

5.9.2.5 USBD_MAX_NUM_CONFIGURATION

```
#define USBD_MAX_NUM_CONFIGURATION 1U
```

&

5.9.2.6 USBD_MAX_NUM_INTERFACES

```
#define USBD_MAX_NUM_INTERFACES 1U
```

&

5.9.2.7 USBD_MAX_STR_DESC_SIZ

```
#define USBD_MAX_STR_DESC_SIZ 512U
```

&

5.9.2.8 USBD_SELF_POWERED

```
#define USBD_SELF_POWERED 1U
```

&

5.9.2.9 USBD_SUPPORT_USER_STRING

```
#define USBD_SUPPORT_USER_STRING 0U
```

&

5.10 USBD_CONF_Exported_Macros

Aliases.

Macros

- #define USBD_malloc malloc
- #define USBD_free free
- #define USBD_memset memset
- #define USBD_memcpy memcpy
- #define USBD_Delay HAL_Delay
- #define USBD_UsrLog(...)
- #define USBD_ErrLog(...)
- #define USBD_DbgLog(...)

5.10.1 Detailed Description

Aliases.

5.10.2 Macro Definition Documentation

5.10.2.1 USBD_DbgLog

```
#define USBD_DbgLog(
               ...  )
```

Value:

```
printf("DEBUG : ");  \
    printf(__VA_ARGS__); \
    printf("\n");
```

&

5.10.2.2 USBD_Delay

```
#define USBD_Delay HAL_Delay
```

Alias for delay.

5.10.2.3 USBD_ErrLog

```
#define USBD_ErrLog(
                ...  )
```

Value:

```
printf("ERROR: ");   \
    printf(__VA_ARGS__); \
    printf("\n");
```

&

5.10.2.4 USBD_free

```
#define USBD_free free
```

Alias for memory release.

5.10.2.5 USBD_malloc

```
#define USBD_malloc malloc
```

Alias for memory allocation.

5.10.2.6 USBD_memcpy

```
#define USBD_memcpy memcpy
```

Alias for memory copy.

5.10.2.7 USBD_memset

```
#define USBD_memset memset
```

Alias for memory set.

5.10.2.8 USBD_UsrLog

```
#define USBD_UsrLog(
                ...  )
```

Value:

```
printf(__VA_ARGS__); \
    printf("\n");
```

&

5.11 USBD_CONF_Exported_Types

Types.

Types.

5.12 USBD_CONF_Exported_FunctionsPrototype

Declaration of public functions for Usb device.

Declaration of public functions for Usb device.

5.13 USBD_DESC

Usb device descriptors module.

Modules

- USBD_DESC_Exported_Constants

  Constants.
- USBD_DESC_Exported_Defines

  Defines.
- USBD_DESC_Exported_TypesDefinitions

  Types.
- USBD_DESC_Exported_Macros

  Aliases.
- USBD_DESC_Exported_Variables

  Public variables.
- USBD_DESC_Exported_FunctionsPrototype

  Public functions declaration.

5.13.1 Detailed Description

Usb device descriptors module.

5.14 USBD_DESC_Exported_Constants

Constants.

Macros

- #define DEVICE_ID1 (UID_BASE)
- #define DEVICE_ID2 (UID_BASE + 0x4)
- #define DEVICE_ID3 (UID_BASE + 0x8)
- #define USB_SIZ_STRING_SERIAL 0x1A

5.14.1 Detailed Description

Constants.

5.14.2 Macro Definition Documentation

5.14.2.1 DEVICE_ID1

```
#define DEVICE_ID1 (UID_BASE)
```

&

5.14.2.2 DEVICE_ID2

```
#define DEVICE_ID2 (UID_BASE + 0x4)
```

&

5.14.2.3 DEVICE_ID3

```
#define DEVICE_ID3 (UID_BASE + 0x8)
```

&

5.14.2.4 USB_SIZ_STRING_SERIAL

```
#define USB_SIZ_STRING_SERIAL 0x1A
```

&

5.15 USBD_DESC_Exported_Defines

Defines.

Defines.

5.16 USBD_DESC_Exported_TypesDefinitions

Types.

Types.

5.17 USBD_DESC_Exported_Macros

Aliases.

Aliases.

5.18 USBD_DESC_Exported_Variables

Public variables.

Variables

- USBD_DescriptorsTypeDef HS_Desc
- USBD_DescriptorsTypeDef FS_Desc

5.18.1 Detailed Description

Public variables.

5.18.2 Variable Documentation

5.18.2.1 FS_Desc

```
USBD_DescriptorsTypeDef FS_Desc
```

Descriptor for the Usb device.

5.18.2.2 HS_Desc

```
USBD_DescriptorsTypeDef HS_Desc
```

Descriptor for the Usb device.

5.19 USBD_DESC_Exported_FunctionsPrototype

Public functions declaration.

Public functions declaration.

5.20 STM32_USB_OTG_DEVICE_LIBRARY

For Usb device.

Modules

- USBD_CDC_IF

  Usb VCP device module.
- USBD_DESC

  Usb device descriptors module.

5.20.1 Detailed Description

For Usb device.

< Define to prevent recursive inclusion ----------

5.21 USBD_OTG_DRIVER

Modules

- USBD_CONF

  Configuration file for Usb otg low level driver.

5.21.1 Detailed Description

Chapter 6

Class Documentation

6.1 daisy::AdcChannelConfig Struct Reference

`#include <per_adc.h>`

Public Types

- enum MuxPin { MUX_SEL_0, MUX_SEL_1, MUX_SEL_2, MUX_SEL_LAST }

Public Member Functions

- void InitSingle (dsy_gpio_pin pin)
- void InitMux (dsy_gpio_pin adc_pin, dsy_gpio_pin mux_0, dsy_gpio_pin mux_1, dsy_gpio_pin mux_2, size_t channels)

Public Attributes

- dsy_gpio pin_
- dsy_gpio mux_pin_ [MUX_SEL_LAST]
- uint8_t mux_channels_

6.1.1 Detailed Description

Configuration Structure for a given channel

6.1.2 Member Enumeration Documentation

6.1.2.1 MuxPin

`enum daisy::AdcChannelConfig::MuxPin`

Which pin to use for multiplexing

Enumerator

MUX_SEL_0	&
MUX_SEL_1	&
MUX_SEL_2	&
MUX_SEL_LAST	&

6.1.3 Member Function Documentation

6.1.3.1 InitMux()

```
void daisy::AdcChannelConfig::InitMux (
            dsy_gpio_pin adc_pin,
            dsy_gpio_pin mux_0,
            dsy_gpio_pin mux_1,
            dsy_gpio_pin mux_2,
            size_t channels )
```

Initializes a single ADC pin as a Multiplexed ADC. Requires a CD4051 Multiplexor connected to the pin Internal Callbacks handle the pin addressing.

Parameters

channels	must be 1-8
mux_0	First mux pin
mux_1	Second mux pin
mux_2	Third mux pin
adc_pin	&

6.1.3.2 InitSingle()

```
void daisy::AdcChannelConfig::InitSingle (
            dsy_gpio_pin pin )
```

Initializes a single ADC pin as an ADC.

Parameters

pin	Pin to init.

6.1.4 Member Data Documentation

6.1.4.1 mux_channels_

```
uint8_t daisy::AdcChannelConfig::mux_channels_
```

&

6.1.4.2 mux_pin_

```
dsy_gpio daisy::AdcChannelConfig::mux_pin_[MUX_SEL_LAST]
```

&

6.1.4.3 pin_

```
dsy_gpio daisy::AdcChannelConfig::pin_
```

&

The documentation for this struct was generated from the following file:

- src/per_adc.h

6.2 daisy::AdcHandle Class Reference

```
#include <per_adc.h>
```

Public Types

- enum OverSampling {
  OVS_NONE, OVS_4, OVS_8, OVS_16,
  OVS_32, OVS_64, OVS_128, OVS_256,
  OVS_512, OVS_1024, OVS_LAST }

Public Member Functions

- void Init (AdcChannelConfig *cfg, size_t num_channels, OverSampling ovs=OVS_32)
- void Start ()
- void Stop ()
- uint16_t Get (uint8_t chn)
- uint16_t * GetPtr (uint8_t chn)
- float GetFloat (uint8_t chn)
- uint16_t GetMux (uint8_t chn, uint8_t idx)
- uint16_t * GetMuxPtr (uint8_t chn, uint8_t idx)
- float GetMuxFloat (uint8_t chn, uint8_t idx)

6.2.1 Detailed Description

Handler for analog to digital conversion

6.2.2 Member Enumeration Documentation

6.2.2.1 OverSampling

```
enum daisy::AdcHandle::OverSampling
```

Supported oversampling amounts

Enumerator

OVS_NONE	&
OVS_4	&
OVS_8	&
OVS_16	&
OVS_32	&
OVS_64	&
OVS_128	&
OVS_256	&
OVS_512	&
OVS_1024	&
OVS_LAST	&

6.2.3 Member Function Documentation

6.2.3.1 Get()

```
uint16_t daisy::AdcHandle::Get (
            uint8_t chn )
```

Single channel getter

Parameters

chn	channel to get

Returns

Converted value

6.2.3.2 GetFloat()

```
float daisy::AdcHandle::GetFloat (
            uint8_t chn )
```

Get floating point from single channel

Parameters

chn	Channel to get from

Returns

Floating point converted value

6.2.3.3 GetMux()

```
uint16_t daisy::AdcHandle::GetMux (
            uint8_t chn,
            uint8_t idx )
```

Getters for multiplexed inputs on a single channel (up to 8 per ADC input).

Parameters

chn	Channel to get from
idx	&

Returns

data

6.2.3.4 GetMuxFloat()

```
float daisy::AdcHandle::GetMuxFloat (
            uint8_t chn,
            uint8_t idx )
```

Getters for multiplexed inputs on a single channel (up to 8 per ADC input).

Parameters

chn	Channel to get from
idx	&

Returns

Floating point data

6.2.3.5 GetMuxPtr()

```
uint16_t* daisy::AdcHandle::GetMuxPtr (
            uint8_t chn,
            uint8_t idx )
```

Getters for multiplexed inputs on a single channel. (Max 8 per chan)

Parameters

chn	Channel to get from
idx	&

Returns

Pointer to data

6.2.3.6 GetPtr()

```
uint16_t* daisy::AdcHandle::GetPtr (
            uint8_t chn )
```

Get pointer to a value from a single channel

Parameters

chn	

Returns

Pointer to converted value

6.2.3.7 Init()

```
void daisy::AdcHandle::Init (
            AdcChannelConfig * cfg,
            size_t num_channels,
            OverSampling ovs = OVS_32 )
```

Initializes the ADC with the pins passed in.

Parameters

∗cfg	an array of AdcChannelConfig of the desired channel
num_channels	number of ADC channels to initialize
ovs	Oversampling amount - Defaults to OVS_32

6.2.3.8 Start()

```
void daisy::AdcHandle::Start ( )
```

Starts reading from the ADC

6.2.3.9 Stop()

```
void daisy::AdcHandle::Stop ( )
```

Stops reading from the ADC

The documentation for this class was generated from the following file:

- src/per_adc.h

6.3 daisy::AnalogControl Class Reference

Hardware Interface for control inputs
Primarily designed for ADC input controls such as
potentiometers, and control voltage.
.

```
#include <hid_ctrl.h>
```

Public Member Functions

- AnalogControl ()
- ∼AnalogControl ()
- void Init (uint16_t ∗adcptr, float sr, bool flip=false, bool invert=false, float slew_seconds=0.002f)
- void InitBipolarCv (uint16_t ∗adcptr, float sr)
- float Process ()
- float Value () const

6.3.1 Detailed Description

Hardware Interface for control inputs
Primarily designed for ADC input controls such as potentiometers, and control voltage.
.

Author

Stephen Hensley

Date

November 2019

6.3.2 Constructor & Destructor Documentation

6.3.2.1 AnalogControl()

```
daisy::AnalogControl::AnalogControl ( )  [inline]
```

Constructor

6.3.2.2 ~AnalogControl()

```
daisy::AnalogControl::~AnalogControl ( )  [inline]
```

destructor

6.3.3 Member Function Documentation

6.3.3.1 Init()

```
void daisy::AnalogControl::Init (
            uint16_t * adcptr,
            float sr,
            bool flip = false,
            bool invert = false,
            float slew_seconds = 0.002f )
```

Initializes the control

Parameters

∗adcptr	is a pointer to the raw adc read value – This can be acquired with dsy_adc_get_rawptr(), or dsy_adc_get_mux_rawptr()
sr	is the samplerate in Hz that the Process function will be called at.
flip	determines whether the input is flipped (i.e. 1.f - input) or not before being processed.1
invert	determines whether the input is inverted (i.e. -1.f ∗ input) or note before being processed.
slew_seconds	is the slew time in seconds that it takes for the control to change to a new value.

6.3.3.2 InitBipolarCv()

```
void daisy::AnalogControl::InitBipolarCv (
            uint16_t * adcptr,
            float sr )
```

This Initializes the AnalogControl for a -5V to 5V inverted input All of the Init details are the same otherwise

Parameters

∗adcptr	Pointer to analog digital converter
sr	Audio engine sample rate

6.3.3.3 Process()

```
float daisy::AnalogControl::Process ( )
```

Filters, and transforms a raw ADC read into a normalized range. this should be called at the rate of specified by samplerate at Init time. Default Initializations will return 0.0 -> 1.0 Bi-polar CV inputs will return -1.0 -> 1.0

6.3.3.4 Value()

```
float daisy::AnalogControl::Value ( ) const  [inline]
```

Returns the current stored value, without reprocessing

The documentation for this class was generated from the following file:

- src/hid_ctrl.h

6.4 codec_frame_t Struct Reference

```
#include <dev_codec_wm8731_frame.h>
```

Public Attributes

- short l
- short r

6.4.1 Detailed Description

&

6.4.2 Member Data Documentation

6.4.2.1 l

```
short codec_frame_t::l
```

&

6.4.2.2 r

```
short codec_frame_t::r
```

&

The documentation for this struct was generated from the following file:

- src/dev_codec_wm8731_frame.h

6.5 color Struct Reference

```
#include <dev_leddriver.h>
```

Public Attributes

- uint16_t red
- uint16_t green
- uint16_t blue

6.5.1 Detailed Description

Simple color struct Different from util_color only in type (0-4095 vs 0-1) This could easily be migrated to work with those instead.

6.5.2 Member Data Documentation

6.5.2.1 blue

```
uint16_t color::blue
```

&

6.5.2.2 green

```
uint16_t color::green
```

&

6.5.2.3 red

```
uint16_t color::red
```

&

The documentation for this struct was generated from the following file:

- src/dev_leddriver.h

6.6 daisy::Color Class Reference

```
#include <util_color.h>
```

Public Types

- enum PresetColor {
  RED, GREEN, BLUE, WHITE,
  PURPLE, CYAN, GOLD, OFF,
  LAST }

Public Member Functions

- void Init (PresetColor c)
- void Init (float red, float green, float blue)
- float Red () const
- float Green () const
- float Blue () const

6.6.1 Detailed Description

Class for handling simple colors

6.6.2 Member Enumeration Documentation

6.6.2.1 PresetColor

```
enum daisy::Color::PresetColor
```

List of colors that have a preset RGB value

Enumerator

RED	&
GREEN	&
BLUE	&
WHITE	&
PURPLE	&
CYAN	&
GOLD	&
OFF	&
LAST	&

6.6.3 Member Function Documentation

6.6.3.1 Blue()

```
float daisy::Color::Blue ( ) const  [inline]
```

Returns the 0-1 value for Blue

6.6.3.2 Green()

```
float daisy::Color::Green ( ) const  [inline]
```

Returns the 0-1 value for Green

6.6.3.3 Init() [1/2]

```
void daisy::Color::Init (
            PresetColor c )
```

Initializes the Color with a given preset.

Parameters

c	Color to init to

6.6.3.4 Init() [2/2]

```
void daisy::Color::Init (
            float red,
            float green,
            float blue )
```

Initializes the Color with a specific RGB value red, green, and blue should be floats between 0 and 1

Parameters

red	Red value
green	Green value
blue	Blue value

6.6.3.5 Red()

```
float daisy::Color::Red ( ) const  [inline]
```

Returns the 0-1 value for Red

The documentation for this class was generated from the following file:

- src/util_color.h

6.7 daisy::ControlChangeEvent Struct Reference

```
#include <hid_midi.h>
```

Public Attributes

- int channel
- uint8_t control_number
- uint8_t value

6.7.1 Detailed Description

Struct containing control number, and value for a given channel. Can be made from MidiEvent

6.7.2 Member Data Documentation

6.7.2.1 channel

```
int daisy::ControlChangeEvent::channel
```

&

6.7.2.2 control_number

```
uint8_t daisy::ControlChangeEvent::control_number
```

&

6.7.2.3 value

```
uint8_t daisy::ControlChangeEvent::value
```

&

The documentation for this struct was generated from the following file:

- src/hid_midi.h

6.8 daisy::daisy_field Struct Reference

```
#include <daisy_field.h>
```

Public Attributes

- daisy::DaisySeed seed
- daisy::Switch switches [SW_LAST]
- dsy_gpio gate_in
- dsy_gpio gate_out
- dsy_sr_4021_handle keyboard_sr
- AnalogControl knobs [KNOB_LAST]
- AnalogControl cvs [CV_LAST]

6.8.1 Detailed Description

Struct containing hardware defines and daisy seed

6.8.2 Member Data Documentation

6.8.2.1 cvs

```
AnalogControl daisy::daisy_field::cvs[CV_LAST]
```

Array of cv ins

6.8.2.2 gate_in

```
dsy_gpio daisy::daisy_field::gate_in
```

Gate input.

6.8.2.3 gate_out

```
dsy_gpio daisy::daisy_field::gate_out
```

Gate output

6.8.2.4 keyboard_sr

```
dsy_sr_4021_handle daisy::daisy_field::keyboard_sr
```

Keyboard shift register

6.8.2.5 knobs

```
AnalogControl daisy::daisy_field::knobs[KNOB_LAST]
```

Array of hardware knobs

6.8.2.6 seed

```
daisy::DaisySeed daisy::daisy_field::seed
```

Daisy seed

6.8.2.7 switches

```
daisy::Switch daisy::daisy_field::switches[SW_LAST]
```

Array of hardware switches

The documentation for this struct was generated from the following file:

- src/daisy_field.h

6.9 daisy::DaisyPatch Class Reference

Class that handles initializing all of the hardware specific to the Daisy Patch Board.
Helper funtions are also in place to provide easy access to built-in controls and peripherals.

`#include <daisy_patch.h>`

Public Types

- enum Ctrl {
  CTRL_1, **CTRL_2**, **CTRL_3**, **CTRL_4**,
  CTRL_LAST }
- enum GateInput { **GATE_IN_1**, **GATE_IN_2**, GATE_IN_LAST }

Public Member Functions

- DaisyPatch ()
- ~DaisyPatch ()
- void Init ()
- void DelayMs (size_t del)
- void SetAudioBlockSize (size_t size)
- void StartAudio (dsy_audio_mc_callback cb)
- void ChangeAudioCallback (dsy_audio_callback cb)
- void StartAdc ()
- float AudioSampleRate ()
- size_t AudioBlockSize ()
- float AudioCallbackRate ()
- void UpdateAnalogControls ()
- float GetCtrlValue (Ctrl k)
- void DebounceControls ()
- void DisplayControls (bool invert=true)

Public Attributes

- DaisySeed seed
- Encoder encoder
- AnalogControl controls [CTRL_LAST]
- GateIn gate_input [GATE_IN_LAST]
- MidiHandler midi
- OledDisplay display
- dsy_gpio gate_output

6.9.1 Detailed Description

Class that handles initializing all of the hardware specific to the Daisy Patch Board.
Helper funtions are also in place to provide easy access to built-in controls and peripherals.

Author

Stephen Hensley

Date

November 2019

6.9.2 Member Enumeration Documentation

6.9.2.1 Ctrl

`enum daisy::DaisyPatch::Ctrl`

Enum of Ctrls to represent the four CV/Knob combos on the Patch

6.9.2.2 GateInput

`enum daisy::DaisyPatch::GateInput`

Daisy patch gate inputs

Enumerator

GATE_IN_LAST	<

6.9.3 Constructor & Destructor Documentation

6.9.3.1 DaisyPatch()

`daisy::DaisyPatch::DaisyPatch ( ) [inline]`

Constructor

6.9.3.2 ~DaisyPatch()

`daisy::DaisyPatch::~DaisyPatch ( ) [inline]`

Destructor

6.9.4 Member Function Documentation

6.9.4.1 AudioBlockSize()

`size_t daisy::DaisyPatch::AudioBlockSize ( )`

Get block size

6.9.4.2 AudioCallbackRate()

```
float daisy::DaisyPatch::AudioCallbackRate ( )
```

Get callback rate

6.9.4.3 AudioSampleRate()

```
float daisy::DaisyPatch::AudioSampleRate ( )
```

Get sample rate

6.9.4.4 ChangeAudioCallback()

```
void daisy::DaisyPatch::ChangeAudioCallback (
            dsy_audio_callback cb )
```

Change to a different callback function.

Parameters

cb	New callback function.

6.9.4.5 DebounceControls()

```
void daisy::DaisyPatch::DebounceControls ( )
```

Debounce analog controls. Call at same rate as reading controls.

6.9.4.6 DelayMs()

```
void daisy::DaisyPatch::DelayMs (
            size_t del )
```

Wait some ms before going on.

Parameters

del	Delay time in ms.

6.9.4.7 DisplayControls()

```
void daisy::DaisyPatch::DisplayControls (
```

```
            bool invert = true )
```

Control the display

6.9.4.8 GetCtrlValue()

```
float daisy::DaisyPatch::GetCtrlValue (
            Ctrl k )
```

Get value for a partiular control

Parameters

k	Which control to get

6.9.4.9 Init()

```
void daisy::DaisyPatch::Init ( )
```

Initializes the daisy seed, and patch hardware.

6.9.4.10 SetAudioBlockSize()

```
void daisy::DaisyPatch::SetAudioBlockSize (
            size_t size )
```

Audio Block size defaults to 48. Change it using this function before StartingAudio

Parameters

size	Audio block size.

6.9.4.11 StartAdc()

```
void daisy::DaisyPatch::StartAdc ( )
```

Start analog to digital conversion.

6.9.4.12 StartAudio()

```
void daisy::DaisyPatch::StartAudio (
            dsy_audio_mc_callback cb )
```

Start audio output.

Parameters

cb	Audio callback function

6.9.4.13 UpdateAnalogControls()

```
void daisy::DaisyPatch::UpdateAnalogControls ( )
```

Call at same rate as reading controls for good reads.

6.9.5 Member Data Documentation

6.9.5.1 controls

```
AnalogControl daisy::DaisyPatch::controls[CTRL_LAST]
```

Array of controls

6.9.5.2 display

```
OledDisplay daisy::DaisyPatch::display
```

&

6.9.5.3 encoder

```
Encoder daisy::DaisyPatch::encoder
```

Encoder object

6.9.5.4 gate_input

```
GateIn daisy::DaisyPatch::gate_input[GATE_IN_LAST]
```

Gate inputs

6.9.5.5 gate_output

```
dsy_gpio daisy::DaisyPatch::gate_output
```

&

6.9.5.6 midi

`MidiHandler daisy::DaisyPatch::midi`

Handles midi

6.9.5.7 seed

`DaisySeed daisy::DaisyPatch::seed`

Seed object

The documentation for this class was generated from the following file:

- src/daisy_patch.h

6.10 daisy::DaisyPetal Class Reference

Helpers and hardware definitions for daisy petal.

`#include <daisy_petal.h>`

Public Types

- enum Sw {
  SW_1, SW_2, SW_3, SW_4,
  SW_5, SW_6, SW_7, SW_LAST }
- enum Knob {
  KNOB_1, KNOB_2, KNOB_3, KNOB_4,
  KNOB_5, KNOB_6, KNOB_LAST }
- enum RingLed {
  RING_LED_1, RING_LED_2, RING_LED_3, RING_LED_4,
  RING_LED_5, RING_LED_6, RING_LED_7, RING_LED_8,
  RING_LED_LAST }
- enum FootswitchLed {
  FOOTSWITCH_LED_1, FOOTSWITCH_LED_2, FOOTSWITCH_LED_3, FOOTSWITCH_LED_4,
  FOOTSWITCH_LED_LAST }

Public Member Functions

- DaisyPetal ()
- ~DaisyPetal ()
- void Init ()
- void DelayMs (size_t del)
- void SetAudioBlockSize (size_t size)
- void StartAudio (dsy_audio_callback cb)
- void ChangeAudioCallback (dsy_audio_callback cb)
- void StartAdc ()
- float AudioSampleRate ()
- size_t AudioBlockSize ()
- float AudioCallbackRate ()
- void UpdateAnalogControls ()
- float GetKnobValue (Knob k)
- float GetExpression ()
- void DebounceControls ()
- void ClearLeds ()
- void UpdateLeds ()
- void SetRingLed (RingLed idx, float r, float g, float b)
- void SetFootswitchLed (FootswitchLed idx, float bright)

Public Attributes

- DaisySeed seed
- Encoder encoder
- AnalogControl knob [KNOB_LAST]
- AnalogControl expression
- Switch switches [SW_LAST]
- RgbLed ring_led [8]
- Led footswitch_led [4]

6.10.1 Detailed Description

Helpers and hardware definitions for daisy petal.

6.10.2 Member Enumeration Documentation

6.10.2.1 FootswitchLed

`enum daisy::DaisyPetal::FootswitchLed`

footswitch leds

Enumerator

FOOTSWITCH_LED_1	&
FOOTSWITCH_LED_2	&
FOOTSWITCH_LED_3	&
FOOTSWITCH_LED_4	&
FOOTSWITCH_LED_LAST	&

6.10.2.2 Knob

`enum daisy::DaisyPetal::Knob`

Knobs

Enumerator

KNOB_1	&
KNOB_2	&
KNOB_3	&
KNOB_4	&
KNOB_5	&
KNOB_6	&
KNOB_LAST	&

6.10.2.3 RingLed

`enum daisy::DaisyPetal::RingLed`

Leds in ringled

Enumerator

RING_LED_1	&
RING_LED_2	&
RING_LED_3	&
RING_LED_4	&
RING_LED_5	&
RING_LED_6	&
RING_LED_7	&
RING_LED_8	&
RING_LED_LAST	&

6.10.2.4 Sw

`enum daisy::DaisyPetal::Sw`

Switches

Enumerator

SW_1	Footswitch
SW_2	Footswitch
SW_3	Footswitch
SW_4	Footswitch
SW_5	Toggle
SW_6	Toggle
SW_7	Toggle
SW_LAST	Last enum item

6.10.3 Constructor & Destructor Documentation

6.10.3.1 DaisyPetal()

`daisy::DaisyPetal::DaisyPetal ( ) [inline]`

Constructor

6.10.3.2 ~DaisyPetal()

```
daisy::DaisyPetal::~DaisyPetal ( )  [inline]
```

Destructor

6.10.4 Member Function Documentation

6.10.4.1 AudioBlockSize()

```
size_t daisy::DaisyPetal::AudioBlockSize ( )
```

Get audio block size

6.10.4.2 AudioCallbackRate()

```
float daisy::DaisyPetal::AudioCallbackRate ( )
```

Get callback rate

6.10.4.3 AudioSampleRate()

```
float daisy::DaisyPetal::AudioSampleRate ( )
```

Device audio sample rate.

6.10.4.4 ChangeAudioCallback()

```
void daisy::DaisyPetal::ChangeAudioCallback (
            dsy_audio_callback cb )
```

Change callback function

Parameters

cb	New callback function.

6.10.4.5 ClearLeds()

```
void daisy::DaisyPetal::ClearLeds ( )
```

Turn all leds off

6.10.4.6 DebounceControls()

```
void daisy::DaisyPetal::DebounceControls ( )
```

Debounce inputs.

6.10.4.7 DelayMs()

```
void daisy::DaisyPetal::DelayMs (
            size_t del )
```

Wait before moving on.

Parameters

del	Delay time in ms.

6.10.4.8 GetExpression()

```
float daisy::DaisyPetal::GetExpression ( )
```

&

6.10.4.9 GetKnobValue()

```
float daisy::DaisyPetal::GetKnobValue (
            Knob k )
```

Get value per knob.

Parameters

k	Which knob to get

Returns

Floating point knob position.

6.10.4.10 Init()

```
void daisy::DaisyPetal::Init ( )
```

Initialize daisy petal

6.10.4.11 SetAudioBlockSize()

```
void daisy::DaisyPetal::SetAudioBlockSize (
            size_t size )
```

Set size of audio blocks.

Parameters

size	Audio block size

6.10.4.12 SetFootswitchLed()

```
void daisy::DaisyPetal::SetFootswitchLed (
            FootswitchLed idx,
            float bright )
```

Set footswitch LED

Parameters

idx	Led Index
bright	Brightness

6.10.4.13 SetRingLed()

```
void daisy::DaisyPetal::SetRingLed (
            RingLed idx,
            float r,
            float g,
            float b )
```

Set ring LED colors

Parameters

idx	Index to set
r	Red value
g	Green value
b	Blue value

6.10.4.14 StartAdc()

```
void daisy::DaisyPetal::StartAdc ( )
```

Start analog to digital conversion.

6.10.4.15 StartAudio()

```
void daisy::DaisyPetal::StartAudio (
            dsy_audio_callback cb )
```

Start audio callback

Parameters

cb	Callback function.

6.10.4.16 UpdateAnalogControls()

```
void daisy::DaisyPetal::UpdateAnalogControls ( )
```

Call at the same frequency as controls are read for stable readings.

6.10.4.17 UpdateLeds()

```
void daisy::DaisyPetal::UpdateLeds ( )
```

Update Leds to values you had set.

6.10.5 Member Data Documentation

6.10.5.1 encoder

```
Encoder daisy::DaisyPetal::encoder
```

&

6.10.5.2 expression

```
AnalogControl daisy::DaisyPetal::expression
```

&

6.10.5.3 footswitch_led

```
Led daisy::DaisyPetal::footswitch_led[4]
```

&

6.10.5.4 knob

```
AnalogControl daisy::DaisyPetal::knob[KNOB_LAST]
```

&

6.10.5.5 ring_led

```
RgbLed daisy::DaisyPetal::ring_led[8]
```

&

6.10.5.6 seed

```
DaisySeed daisy::DaisyPetal::seed
```

&

6.10.5.7 switches

```
Switch daisy::DaisyPetal::switches[SW_LAST]
```

< &

The documentation for this class was generated from the following file:

- src/daisy_petal.h

6.11 daisy::DaisyPod Class Reference

Class that handles initializing all of the hardware specific to the Daisy Patch Board.
Helper funtions are also in place to provide easy access to built-in controls and peripherals.

```
#include <daisy_pod.h>
```

Public Types

- enum Sw { **BUTTON_1**, BUTTON_2, BUTTON_LAST }
- enum Knob { **KNOB_1**, KNOB_2, KNOB_LAST }

Public Member Functions

- void Init ()
- void DelayMs (size_t del)
- void SetAudioBlockSize (size_t size)
- void StartAudio (dsy_audio_callback cb)
- void ChangeAudioCallback (dsy_audio_callback cb)
- void StartAdc ()
- float AudioSampleRate ()
- size_t AudioBlockSize ()
- float AudioCallbackRate ()
- void UpdateAnalogControls ()
- float GetKnobValue (Knob k)
- void DebounceControls ()
- void ClearLeds ()
- void UpdateLeds ()

Public Attributes

- DaisySeed seed
- Encoder encoder
- AnalogControl knob1
- AnalogControl knob2
- AnalogControl * knobs [KNOB_LAST]
- Switch button1
- Switch button2
- Switch * buttons [BUTTON_LAST]
- RgbLed led1
- RgbLed led2

6.11.1 Detailed Description

Class that handles initializing all of the hardware specific to the Daisy Patch Board.
Helper funtions are also in place to provide easy access to built-in controls and peripherals.

Author

Stephen Hensley

Date

November 2019

6.11.2 Member Enumeration Documentation

6.11.2.1 Knob

`enum daisy::DaisyPod::Knob`

Knobs

Enumerator

KNOB_2	&
KNOB_LAST	&

6.11.2.2 Sw

```
enum daisy::DaisyPod::Sw
```

Switches

Enumerator

BUTTON_2	&
BUTTON_LAST	&

6.11.3 Member Function Documentation

6.11.3.1 AudioBlockSize()

```
size_t daisy::DaisyPod::AudioBlockSize ( )
```

Get block size

6.11.3.2 AudioCallbackRate()

```
float daisy::DaisyPod::AudioCallbackRate ( )
```

Get callback rate

6.11.3.3 AudioSampleRate()

```
float daisy::DaisyPod::AudioSampleRate ( )
```

Get sample rate

6.11.3.4 ChangeAudioCallback()

```
void daisy::DaisyPod::ChangeAudioCallback (
            dsy_audio_callback cb )
```

Switch callback functions

Parameters

cb	New callback function.

6.11.3.5 ClearLeds()

```
void daisy::DaisyPod::ClearLeds ( )
```

Reset Leds

6.11.3.6 DebounceControls()

```
void daisy::DaisyPod::DebounceControls ( )
```

&

6.11.3.7 DelayMs()

```
void daisy::DaisyPod::DelayMs (
            size_t del )
```

Wait for a bit

Parameters

del	Time to wait in ms.

6.11.3.8 GetKnobValue()

```
float daisy::DaisyPod::GetKnobValue (
            Knob k )
```

&

6.11.3.9 Init()

```
void daisy::DaisyPod::Init ( )
```

Init related stuff.

6.11.3.10 SetAudioBlockSize()

```
void daisy::DaisyPod::SetAudioBlockSize (
            size_t size )
```

Audio Block size defaults to 48. Change it using this function before StartingAudio.

Parameters

size	Block size to set.

6.11.3.11 StartAdc()

```
void daisy::DaisyPod::StartAdc ( )
```

Start analog to digital conversion.

6.11.3.12 StartAudio()

```
void daisy::DaisyPod::StartAudio (
            dsy_audio_callback cb )
```

Start audio callback

Parameters

cb	Callback function.

6.11.3.13 UpdateAnalogControls()

```
void daisy::DaisyPod::UpdateAnalogControls ( )
```

Call at same rate as analog reads for smooth reading.

6.11.3.14 UpdateLeds()

```
void daisy::DaisyPod::UpdateLeds ( )
```

Update Leds to set colors

6.11.4 Member Data Documentation

6.11.4.1 button1

```
Switch daisy::DaisyPod::button1
```

&

6.11.4.2 button2

```
Switch daisy::DaisyPod::button2
```

&

6.11.4.3 buttons

```
Switch * daisy::DaisyPod::buttons[BUTTON_LAST]
```

&

6.11.4.4 encoder

```
Encoder daisy::DaisyPod::encoder
```

&

6.11.4.5 knob1

```
AnalogControl daisy::DaisyPod::knob1
```

&

6.11.4.6 knob2

```
AnalogControl daisy::DaisyPod::knob2
```

&

6.11.4.7 knobs

```
AnalogControl * daisy::DaisyPod::knobs[KNOB_LAST]
```

&

6.11.4.8 led1

```
RgbLed daisy::DaisyPod::led1
```

&

6.11.4.9 led2

```
RgbLed daisy::DaisyPod::led2
```

&

6.11.4.10 seed

`DaisySeed daisy::DaisyPod::seed`

Public Members #

The documentation for this class was generated from the following file:

- src/daisy_pod.h

6.12 daisy::DaisySeed Class Reference

This is the higher-level interface for the Daisy board.
All basic peripheral configuration/initialization is setup here.

`#include <daisy_seed.h>`

Public Member Functions

- void Configure ()
- void Init ()
- dsy_gpio_pin GetPin (uint8_t pin_idx)
- void StartAudio (dsy_audio_callback cb)
- void SetLed (bool state)
- void SetTestPoint (bool state)
- float AudioSampleRate ()
- void SetAudioBlockSize (size_t blocksize)

Public Attributes

- dsy_sdram_handle sdram_handle
- dsy_qspi_handle qspi_handle
- dsy_audio_handle audio_handle
- dsy_sai_handle sai_handle
- dsy_i2c_handle i2c1_handle
- dsy_i2c_handle i2c2_handle
- AdcHandle adc
- dsy_dac_handle dac_handle
- UsbHandle usb_handle

6.12.1 Detailed Description

This is the higher-level interface for the Daisy board.
All basic peripheral configuration/initialization is setup here.

6.12.2 Member Function Documentation

6.12.2.1 AudioSampleRate()

```
float daisy::DaisySeed::AudioSampleRate ( )
```

Returns the audio sample rate in Hz as a floating point number.

6.12.2.2 Configure()

```
void daisy::DaisySeed::Configure ( )
```

Configures the settings for all internal peripherals, but does not initialize them. This allows for modification of the configuration handles prior to initialization.&

6.12.2.3 GetPin()

```
dsy_gpio_pin daisy::DaisySeed::GetPin (
            uint8_t pin_idx )
```

Returns the gpio_pin corresponding to the index 0-31. For the given GPIO on the Daisy Seed (labeled 1-32 in docs).

6.12.2.4 Init()

```
void daisy::DaisySeed::Init ( )
```

Initializes the Daisy Seed and the following peripherals: SDRAM, QSPI, 24-bit 48kHz Audio via AK4556, Internal USB, as well as the built-in LED and Testpoint.

ADCs, DACs, and other special peripherals (such as I2C, SPI, etc.) can be initialized using their specific initializers within libdaisy for a specific application.

6.12.2.5 SetAudioBlockSize()

```
void daisy::DaisySeed::SetAudioBlockSize (
            size_t blocksize )
```

Sets the number of samples processed per channel by the audio callback.

6.12.2.6 SetLed()

```
void daisy::DaisySeed::SetLed (
            bool state )
```

Sets the state of the built in LED

6.12.2.7 SetTestPoint()

```
void daisy::DaisySeed::SetTestPoint (
            bool state )
```

Sets the state of the test point near pin 10

6.12.2.8 StartAudio()

```
void daisy::DaisySeed::StartAudio (
            dsy_audio_callback cb )
```

Begins the audio for the seeds builtin audio. the specified callback will get called whenever new data is ready to be prepared.

6.12.3 Member Data Documentation

6.12.3.1 adc

```
AdcHandle daisy::DaisySeed::adc
```

&

6.12.3.2 audio_handle

```
dsy_audio_handle daisy::DaisySeed::audio_handle
```

&

6.12.3.3 dac_handle

```
dsy_dac_handle daisy::DaisySeed::dac_handle
```

&

6.12.3.4 i2c1_handle

```
dsy_i2c_handle daisy::DaisySeed::i2c1_handle
```

&

6.12.3.5 i2c2_handle

```
dsy_i2c_handle daisy::DaisySeed::i2c2_handle
```

&

6.12.3.6 qspi_handle

```
dsy_qspi_handle daisy::DaisySeed::qspi_handle
```

&

6.12.3.7 sai_handle

```
dsy_sai_handle daisy::DaisySeed::sai_handle
```

&

6.12.3.8 sdram_handle

```
dsy_sdram_handle daisy::DaisySeed::sdram_handle
```

&

6.12.3.9 usb_handle

```
UsbHandle daisy::DaisySeed::usb_handle
```

&

The documentation for this class was generated from the following file:

- src/daisy_seed.h

6.13 dsy_audio_handle Struct Reference

```
#include <hid_audio.h>
```

Public Attributes

- size_t block_size
- dsy_sai_handle * sai
- dsy_i2c_handle * dev0_i2c
- dsy_i2c_handle * dev1_i2c

6.13.1 Detailed Description

Simple config struct that holds peripheral drivers.

6.13.2 Member Data Documentation

6.13.2.1 block_size

```
size_t dsy_audio_handle::block_size
```

&

6.13.2.2 dev0_i2c

```
dsy_i2c_handle* dsy_audio_handle::dev0_i2c
```

&

6.13.2.3 dev1_i2c

```
dsy_i2c_handle* dsy_audio_handle::dev1_i2c
```

&

6.13.2.4 sai

```
dsy_sai_handle* dsy_audio_handle::sai
```

&

The documentation for this struct was generated from the following file:

- src/hid_audio.h

6.14 dsy_dac_handle Struct Reference

```
#include <per_dac.h>
```

Public Attributes

- dsy_dac_mode mode
- dsy_dac_bitdepth bitdepth
- dsy_gpio_pin pin_config [DSY_DAC_CHN_LAST]

6.14.1 Detailed Description

Configuration structure for DAC initialization and settings. pin_config must be filled out. However, the DACs are pretty consistently on pins PA4, and PA5 across all STM32 MCUs that I've used.

6.14.2 Member Data Documentation

6.14.2.1 bitdepth

```
dsy_dac_bitdepth dsy_dac_handle::bitdepth
```

&

6.14.2.2 mode

```
dsy_dac_mode dsy_dac_handle::mode
```

&

6.14.2.3 pin_config

```
dsy_gpio_pin dsy_dac_handle::pin_config[DSY_DAC_CHN_LAST]
```

&

The documentation for this struct was generated from the following file:

- src/per_dac.h

6.15 dsy_gpio Struct Reference

```
#include <per_gpio.h>
```

Public Attributes

- dsy_gpio_pin pin
- dsy_gpio_mode mode
- dsy_gpio_pull pull

6.15.1 Detailed Description

Struct for holding the pin, and configuration

6.15.2 Member Data Documentation

6.15.2.1 mode

dsy_gpio_mode dsy_gpio::mode

&

6.15.2.2 pin

dsy_gpio_pin dsy_gpio::pin

&

6.15.2.3 pull

dsy_gpio_pull dsy_gpio::pull

&

The documentation for this struct was generated from the following file:

- src/per_gpio.h

6.16 dsy_gpio_pin Struct Reference

```
#include <daisy_core.h>
```

Public Attributes

- dsy_gpio_port port
- uint8_t pin

6.16.1 Detailed Description

Hardware define pins

6.16.2 Member Data Documentation

6.16.2.1 pin

```
uint8_t dsy_gpio_pin::pin
```

number 0-15

6.16.2.2 port

```
dsy_gpio_port dsy_gpio_pin::port
```

&

The documentation for this struct was generated from the following file:

- src/daisy_core.h

6.17 dsy_i2c_handle Struct Reference

```
#include <per_i2c.h>
```

Public Attributes

- dsy_i2c_periph periph
- dsy_gpio_pin pin_config [DSY_I2C_PIN_LAST]
- dsy_i2c_speed speed

6.17.1 Detailed Description

this object will be used to initialize the I2C interface, and can be passed to dev_ drivers that require I2C.

6.17.2 Member Data Documentation

6.17.2.1 periph

```
dsy_i2c_periph dsy_i2c_handle::periph
```

&

6.17.2.2 pin_config

```
dsy_gpio_pin dsy_i2c_handle::pin_config[DSY_I2C_PIN_LAST]
```

&

6.17.2.3 speed

```
dsy_i2c_speed dsy_i2c_handle::speed
```

&

The documentation for this struct was generated from the following file:

- src/per_i2c.h

6.18 dsy_qspi_handle Struct Reference

```
#include <per_qspi.h>
```

Public Attributes

- dsy_qspi_mode mode
- dsy_qspi_device device
- dsy_gpio_pin pin_config [DSY_QSPI_PIN_LAST]

6.18.1 Detailed Description

Configuration structure for interfacing with QSPI Driver

6.18.2 Member Data Documentation

6.18.2.1 device

```
dsy_qspi_device dsy_qspi_handle::device
```

&

6.18.2.2 mode

```
dsy_qspi_mode dsy_qspi_handle::mode
```

&

6.18.2.3 pin_config

```
dsy_gpio_pin dsy_qspi_handle::pin_config[DSY_QSPI_PIN_LAST]
```

&

The documentation for this struct was generated from the following file:

- src/per_qspi.h

6.19 dsy_sai_handle Struct Reference

```
#include <per_sai.h>
```

Public Attributes

- dsy_audio_sai init
- dsy_audio_samplerate samplerate [DSY_SAI_LAST]
- dsy_audio_bitdepth bitdepth [DSY_SAI_LAST]
- dsy_audio_dir a_direction [DSY_SAI_LAST]
- dsy_audio_dir b_direction [DSY_SAI_LAST]
- dsy_audio_sync sync_config [DSY_SAI_LAST]
- dsy_audio_device device [DSY_SAI_LAST]
- dsy_gpio_pin sai1_pin_config [DSY_SAI_PIN_LAST]
- dsy_gpio_pin sai2_pin_config [DSY_SAI_PIN_LAST]

6.19.1 Detailed Description

Configuration structure for SAI contains all above settings, and passes them to internal structure for hardware initialization.

6.19.2 Member Data Documentation

6.19.2.1 a_direction

```
dsy_audio_dir dsy_sai_handle::a_direction[DSY_SAI_LAST]
```

&

6.19.2.2 b_direction

```
dsy_audio_dir dsy_sai_handle::b_direction[DSY_SAI_LAST]
```

&

6.19.2.3 bitdepth

```
dsy_audio_bitdepth dsy_sai_handle::bitdepth[DSY_SAI_LAST]
```

&

6.19.2.4 device

```
dsy_audio_device dsy_sai_handle::device[DSY_SAI_LAST]
```

&

6.19.2.5 init

```
dsy_audio_sai dsy_sai_handle::init
```

&

6.19.2.6 sai1_pin_config

```
dsy_gpio_pin dsy_sai_handle::sai1_pin_config[DSY_SAI_PIN_LAST]
```

&

6.19.2.7 sai2_pin_config

```
dsy_gpio_pin dsy_sai_handle::sai2_pin_config[DSY_SAI_PIN_LAST]
```

&

6.19.2.8 samplerate

```
dsy_audio_samplerate dsy_sai_handle::samplerate[DSY_SAI_LAST]
```

&

6.19.2.9 sync_config

```
dsy_audio_sync dsy_sai_handle::sync_config[DSY_SAI_LAST]
```

&

The documentation for this struct was generated from the following file:

- src/per_sai.h

6.20 DSY_SD_CardInfoTypeDef Struct Reference

`#include <util_bsp_sd_diskio.h>`

Public Attributes

- uint32_t CardType
- uint32_t CardVersion
- uint32_t Class
- uint32_t RelCardAdd
- uint32_t BlockNbr
- uint32_t BlockSize
- uint32_t LogBlockNbr
- uint32_t LogBlockSize
- uint32_t CardSpeed

6.20.1 Detailed Description

Functions for handling DiskIO via SDMMC These are usually configured through the FatFS driver/interface, and won't need to be accessed directly often.

6.20.2 Member Data Documentation

6.20.2.1 BlockNbr

`uint32_t DSY_SD_CardInfoTypeDef::BlockNbr`

Specifies the Card Capacity in blocks

6.20.2.2 BlockSize

`uint32_t DSY_SD_CardInfoTypeDef::BlockSize`

Specifies one block size in bytes

6.20.2.3 CardSpeed

`uint32_t DSY_SD_CardInfoTypeDef::CardSpeed`

Specifies the card Speed

6.20.2.4 CardType

```
uint32_t DSY_SD_CardInfoTypeDef::CardType
```

Specifies the card Type

6.20.2.5 CardVersion

```
uint32_t DSY_SD_CardInfoTypeDef::CardVersion
```

Specifies the card version

6.20.2.6 Class

```
uint32_t DSY_SD_CardInfoTypeDef::Class
```

Specifies the class of the card class

6.20.2.7 LogBlockNbr

```
uint32_t DSY_SD_CardInfoTypeDef::LogBlockNbr
```

Specifies the Card logical Capacity in blocks

6.20.2.8 LogBlockSize

```
uint32_t DSY_SD_CardInfoTypeDef::LogBlockSize
```

Specifies logical block size in bytes

6.20.2.9 RelCardAdd

```
uint32_t DSY_SD_CardInfoTypeDef::RelCardAdd
```

Specifies the Relative Card Address

The documentation for this struct was generated from the following file:

- src/util_bsp_sd_diskio.h

6.21 dsy_sdram_handle Struct Reference

```
#include <dev_sdram.h>
```

Public Attributes

- dsy_sdram_state state
- dsy_gpio_pin pin_config [DSY_SDRAM_PIN_LAST]

6.21.1 Detailed Description

Configuration struct for passing to initialization

6.21.2 Member Data Documentation

6.21.2.1 pin_config

```
dsy_gpio_pin dsy_sdram_handle::pin_config[DSY_SDRAM_PIN_LAST]
```

&

6.21.2.2 state

```
dsy_sdram_state dsy_sdram_handle::state
```

&

The documentation for this struct was generated from the following file:

- src/dev_sdram.h

6.22 dsy_sr_4021_handle Struct Reference

```
#include <dev_sr_4021.h>
```

Public Attributes

- dsy_gpio_pin pin_config [DSY_SR_4021_PIN_LAST]
- uint8_t num_parallel
- uint8_t num_daisychained
- dsy_gpio cs
- dsy_gpio clk
- dsy_gpio data [2]
- uint8_t states [8 ∗1 ∗2]

6.22.1 Detailed Description

configuration strucutre for 4021 pin config is used to initialize the dsy_gpio num_parallel is the number of devices connected that share the same clk/cs, etc. but have independent data num_daisychained is the number of devices in a daisy-chain configuration

6.22.2 Member Data Documentation

6.22.2.1 clk

```
dsy_gpio dsy_sr_4021_handle::clk
```

clk pin

6.22.2.2 cs

```
dsy_gpio dsy_sr_4021_handle::cs
```

cs pin

6.22.2.3 data

```
dsy_gpio dsy_sr_4021_handle::data[2]
```

array of data pins

6.22.2.4 num_daisychained

```
uint8_t dsy_sr_4021_handle::num_daisychained
```

Number of devices daisy chained

6.22.2.5 num_parallel

```
uint8_t dsy_sr_4021_handle::num_parallel
```

number of devices connected

6.22.2.6 pin_config

```
dsy_gpio_pin dsy_sr_4021_handle::pin_config[DSY_SR_4021_PIN_LAST]
```

used to initialize the dsy_gpio

6.22.2.7 states

```
uint8_t dsy_sr_4021_handle::states[8 * 1 * 2]
```

array of states

The documentation for this struct was generated from the following file:

- src/dev_sr_4021.h

6.23 daisy::Encoder Class Reference

Generic Class for handling Quadrature Encoders
Inspired/influenced by Mutable Instruments (pichenettes) Encoder classes.

```
#include <hid_encoder.h>
```

Public Member Functions

- void Init (dsy_gpio_pin a, dsy_gpio_pin b, dsy_gpio_pin click, float update_rate)
- void Debounce ()
- int32_t Increment () const
- bool RisingEdge () const
- bool FallingEdge () const
- bool Pressed () const
- float TimeHeldMs () const

6.23.1 Detailed Description

Generic Class for handling Quadrature Encoders
Inspired/influenced by Mutable Instruments (pichenettes) Encoder classes.

Author

Stephen Hensley

Date

December 2019

6.23.2 Member Function Documentation

6.23.2.1 Debounce()

```
void daisy::Encoder::Debounce ( )
```

Called at update_rate to debounce and handle timing for the switch. In order for events not to be missed, its important that the Edge/Pressed checks be made at the same rate as the debounce function is being called.

6.23.2.2 FallingEdge()

```
bool daisy::Encoder::FallingEdge ( ) const  [inline]
```

Returns true if the encoder was just released.

6.23.2.3 Increment()

```
int32_t daisy::Encoder::Increment ( ) const  [inline]
```

Returns +1 if the encoder was turned clockwise, -1 if it was turned counter-clockwise, or 0 if it was not just turned.

6.23.2.4 Init()

```
void daisy::Encoder::Init (
            dsy_gpio_pin a,
            dsy_gpio_pin b,
            dsy_gpio_pin click,
            float update_rate )
```

Initializes the encoder with the specified hardware pins. Update rate should be the rate at which Debounce() gets called in Hertz.

6.23.2.5 Pressed()

```
bool daisy::Encoder::Pressed ( ) const  [inline]
```

Returns true while the encoder is held down.

6.23.2.6 RisingEdge()

```
bool daisy::Encoder::RisingEdge ( ) const  [inline]
```

Returns true if the encoder was just pressed.

6.23.2.7 TimeHeldMs()

```
float daisy::Encoder::TimeHeldMs ( ) const  [inline]
```

Returns the time in milliseconds that the encoder has been held down.

The documentation for this class was generated from the following file:

- src/hid_encoder.h

6.24 FontDef Struct Reference

`#include <util_oled_fonts.h>`

Public Attributes

- const uint8_t FontWidth
- uint8_t FontHeight
- const uint16_t * data

6.24.1 Detailed Description

Utility for displaying fonts on OLED displays
Migrated to work with libdaisy from stm32-ssd1306

Author

afiskon on github.Font struct

6.24.2 Member Data Documentation

6.24.2.1 data

`const uint16_t* FontDef::data`

Pointer to data font data array

6.24.2.2 FontHeight

`uint8_t FontDef::FontHeight`

Font height in pixels

6.24.2.3 FontWidth

`const uint8_t FontDef::FontWidth`

Font width in pixels

The documentation for this struct was generated from the following file:

- src/util_oled_fonts.h

6.25 daisy::GateIn Class Reference

Generic Class for handling gate inputs through GPIO.

`#include <hid_gatein.h>`

Public Member Functions

- GateIn ()
- ~GateIn ()
- void Init (dsy_gpio_pin *pin_cfg)
- bool Trig ()

6.25.1 Detailed Description

Generic Class for handling gate inputs through GPIO.

Author

Stephen Hensley

Date

March 2020

6.25.2 Constructor & Destructor Documentation

6.25.2.1 GateIn()

`daisy::GateIn::GateIn ( )  [inline]`

GateIn Constructor

6.25.2.2 ~GateIn()

`daisy::GateIn::~GateIn ( )  [inline]`

GateIn~ Destructor

6.25.3 Member Function Documentation

6.25.3.1 Init()

```
void daisy::GateIn::Init (
            dsy_gpio_pin * pin_cfg )
```

Init Initializes the gate input with specified hardware pin

6.25.3.2 Trig()

```
bool daisy::GateIn::Trig ( )
```

Trig Checks current state of gate input.

Returns

FALSE if pin is low, and TRUE if high

The documentation for this class was generated from the following file:

- src/hid_gatein.h

6.26 daisy::Led Class Reference

LED Class providing simple Software PWM ability, etc
Eventually this will work with hardware PWM, and external LED Driver devices as well.

```
#include <hid_led.h>
```

Public Member Functions

- void Init (dsy_gpio_pin pin, bool invert, float samplerate=1000.0f)
- void Set (float val)
- void Update ()

6.26.1 Detailed Description

LED Class providing simple Software PWM ability, etc
Eventually this will work with hardware PWM, and external LED Driver devices as well.

Author

shensley

Date

March 2020

6.26.2 Member Function Documentation

6.26.2.1 Init()

```
void daisy::Led::Init (
            dsy_gpio_pin pin,
            bool invert,
            float samplerate = 1000.0f )
```

Initializes an LED using the specified hardware pin.

Parameters

pin	chooses LED pin
invert	will set whether to internally invert the brightness due to hardware config.
samplerate	sets the rate at which 'Update()' will be called (used for software PWM)

6.26.2.2 Set()

```
void daisy::Led::Set (
             float val )
```

Sets the brightness of the Led.

Parameters

val	will be cubed for gamma correction, and then quantized to 8-bit values for Software PWM 8-bit is for more flexible update rate options, as 12-bit or more would require faster update rates.

6.26.2.3 Update()

```
void daisy::Led::Update ( )
```

This processes the pwm of the LED sets the hardware accordingly.

The documentation for this class was generated from the following file:

- src/hid_led.h

6.27 daisy::MidiEvent Struct Reference

```
#include <hid_midi.h>
```

Public Member Functions

- NoteOnEvent AsNoteOn ()
- ControlChangeEvent AsControlChange ()

Public Attributes

- MidiMessageType type
- int channel
- uint8_t data [2]

6.27.1 Detailed Description

Simple MidiEvent with message type, channel, and data[2] members.

6.27.2 Member Function Documentation

6.27.2.1 AsControlChange()

```
ControlChangeEvent daisy::MidiEvent::AsControlChange ( )  [inline]
```

Returns the data within the MidiEvent as a NoteOnEvent struct.

6.27.2.2 AsNoteOn()

```
NoteOnEvent daisy::MidiEvent::AsNoteOn ( )  [inline]
```

Returns the data within the MidiEvent as a NoteOnEvent struct

6.27.3 Member Data Documentation

6.27.3.1 channel

```
int daisy::MidiEvent::channel
```

&

6.27.3.2 data

```
uint8_t daisy::MidiEvent::data[2]
```

&

6.27.3.3 type

```
MidiMessageType daisy::MidiEvent::type
```

&

The documentation for this struct was generated from the following file:

- src/hid_midi.h

6.28 daisy::MidiHandler Class Reference

Simple MIDI Handler
Parses bytes from an input into valid MidiEvents.
The MidiEvents fill a FIFO queue that the user can pop messages from.

`#include <hid_midi.h>`

Public Types

- enum MidiInputMode { INPUT_MODE_NONE = 0x00, INPUT_MODE_UART1 = 0x01, INPUT_MODE_USB_INT = 0x02, INPUT_MODE_USB_EXT = 0x04 }
- enum MidiOutputMode { OUTPUT_MODE_NONE = 0x00, OUTPUT_MODE_UART1 = 0x01, OUTPUT_MODE_USB_INT = 0x02, OUTPUT_MODE_USB_EXT = 0x04 }

Public Member Functions

- void Init (MidiInputMode in_mode, MidiOutputMode out_mode)
- void StartReceive ()
- void Listen ()
- void Parse (uint8_t byte)
- bool HasEvents () const
- MidiEvent PopEvent ()

6.28.1 Detailed Description

Simple MIDI Handler
Parses bytes from an input into valid MidiEvents.
The MidiEvents fill a FIFO queue that the user can pop messages from.

Author

shensley

Date

March 2020

6.28.2 Member Enumeration Documentation

6.28.2.1 MidiInputMode

`enum daisy::MidiHandler::MidiInputMode`

Input and Output can be configured separately Multiple Input modes can be selected by OR'ing the values.

Enumerator

INPUT_MODE_NONE	&
INPUT_MODE_UART1	&
INPUT_MODE_USB_INT	&
INPUT_MODE_USB_EXT	&

6.28.2.2 MidiOutputMode

```
enum daisy::MidiHandler::MidiOutputMode
```

Output mode

Enumerator

OUTPUT_MODE_NONE	&
OUTPUT_MODE_UART1	&
OUTPUT_MODE_USB_INT	&
OUTPUT_MODE_USB_EXT	&

6.28.3 Member Function Documentation

6.28.3.1 HasEvents()

```
bool daisy::MidiHandler::HasEvents ( ) const  [inline]
```

Checks if there are unhandled messages in the queue

Returns

True if there are events to be handled, else false.

6.28.3.2 Init()

```
void daisy::MidiHandler::Init (
            MidiInputMode in_mode,
            MidiOutputMode out_mode )
```

Initializes the MidiHandler

Parameters

in_mode	Input mode
out_mode	Output mode

6.28.3.3 Listen()

```
void daisy::MidiHandler::Listen ( )
```

Start listening

6.28.3.4 Parse()

```
void daisy::MidiHandler::Parse (
            uint8_t byte )
```

Feed in bytes to state machine from a queue. Populates internal FIFO queue with MIDI Messages For example with uart: midi.Parse(uart.PopRx());

Parameters

byte	&

6.28.3.5 PopEvent()

```
MidiEvent daisy::MidiHandler::PopEvent ( )  [inline]
```

Pops the oldest unhandled MidiEvent from the internal queue

Returns

The event to be handled

6.28.3.6 StartReceive()

```
void daisy::MidiHandler::StartReceive ( )
```

Starts listening on the selected input mode(s). MidiEvent Queue will begin to fill, and can be checked with

The documentation for this class was generated from the following file:

- src/hid_midi.h

6.29 daisy::NoteOnEvent Struct Reference

`#include <hid_midi.h>`

Public Attributes

- int channel
- uint8_t note
- uint8_t velocity

6.29.1 Detailed Description

Struct containing note, and velocity data for a given channel. Can be made from MidiEvent

6.29.2 Member Data Documentation

6.29.2.1 channel

`int daisy::NoteOnEvent::channel`

&

6.29.2.2 note

`uint8_t daisy::NoteOnEvent::note`

&

6.29.2.3 velocity

`uint8_t daisy::NoteOnEvent::velocity`

&

The documentation for this struct was generated from the following file:

- src/hid_midi.h

6.30 daisy::OledDisplay Class Reference

`#include <hid_oled_display.h>`

Public Types

- enum Pins { DATA_COMMAND, RESET, NUM_PINS }

Public Member Functions

- void Init (dsy_gpio_pin *pin_cfg)
- void Fill (bool on)
- void DrawPixel (uint8_t x, uint8_t y, bool on)
- char WriteChar (char ch, FontDef font, bool on)
- char WriteString (char *str, FontDef font, bool on)
- void SetCursor (uint8_t x, uint8_t y)
- void Update ()

6.30.1 Detailed Description

Human Interface Driver for using an OLED Display (SSD1309) For all `bool on` arguments: true is on, false is off. Credit to Aleksander Alekseev (github.com/afiskon/stm32-ssd1306) on github for a great starting point. adapted for SSD1309 and H7 by shensley, 2020

6.30.2 Member Enumeration Documentation

6.30.2.1 Pins

```
enum daisy::OledDisplay::Pins
```

GPIO Pins that need to be used independent of peripheral used.

Enumerator

DATA_COMMAND	Data command pin.
RESET	Reset pin
NUM_PINS	Num pins

6.30.3 Member Function Documentation

6.30.3.1 DrawPixel()

```
void daisy::OledDisplay::DrawPixel (
            uint8_t x,
```

```
            uint8_t y,
            bool on )
```

Sets the pixel at the specified coordinate to be on/off.

Parameters

x	x Coordinate
y	y coordinate
on	on or off

6.30.3.2 Fill()

```
void daisy::OledDisplay::Fill (
            bool on )
```

Fills the entire display with either on/off.

Parameters

on	Sets on or off.

6.30.3.3 Init()

```
void daisy::OledDisplay::Init (
            dsy_gpio_pin * pin_cfg )
```

Takes an argument for the pin cfg

Parameters

pin_cfg	should be a pointer to an array of OledDisplay::NUM_PINS dsy_gpio_pins

6.30.3.4 SetCursor()

```
void daisy::OledDisplay::SetCursor (
            uint8_t x,
            uint8_t y )
```

Moves the 'Cursor' position used for WriteChar, and WriteStr to the specified coordinate.

Parameters

x	x pos
y	y pos

6.30.3.5 Update()

```
void daisy::OledDisplay::Update ( )
```

Writes the current display buffer to the OLED device using SPI or I2C depending on how the object was initialized.

6.30.3.6 WriteChar()

```
char daisy::OledDisplay::WriteChar (
            char ch,
            FontDef font,
            bool on )
```

Writes the character with the specific FontDef to the display buffer at the current Cursor position.

Parameters

ch	character to be written
font	font to be written in
on	on or off

Returns

&

6.30.3.7 WriteString()

```
char daisy::OledDisplay::WriteString (
            char * str,
            FontDef font,
            bool on )
```

Similar to WriteChar, except it will handle an entire String. Wrapping does not happen automatically, so the width of the string must be kept within the dimensions of the screen.

Parameters

str	string to be written
font	font to use
on	on or off

Returns

&

The documentation for this class was generated from the following file:

- src/hid_oled_display.h

6.31 daisy::Parameter Class Reference

`#include <hid_parameter.h>`

Public Types

- enum Curve {
  LINEAR, EXPONENTIAL, LOGARITHMIC, CUBE,
  LAST }

Public Member Functions

- Parameter ()
- ~Parameter ()
- void Init (AnalogControl input, float min, float max, Curve curve)
- float Process ()
- float Value ()

6.31.1 Detailed Description

Simple parameter mapping tool that takes a 0-1 input from an hid_ctrl.

6.31.2 Member Enumeration Documentation

6.31.2.1 Curve

`enum daisy::Parameter::Curve`

Curves are applied to the output signal

Enumerator

LINEAR	Linear curve
EXPONENTIAL	Exponential curve
LOGARITHMIC	Logarithmic curve
CUBE	Cubic curve
LAST	Final enum element.

6.31.3 Constructor & Destructor Documentation

6.31.3.1 Parameter()

```
daisy::Parameter::Parameter ( )  [inline]
```

Constructor

6.31.3.2 ~Parameter()

```
daisy::Parameter::~Parameter ( )  [inline]
```

Destructor

6.31.4 Member Function Documentation

6.31.4.1 Init()

```
void daisy::Parameter::Init (
            AnalogControl input,
            float min,
            float max,
            Curve curve )
```

initialize a parameter using an hid_ctrl object.

Parameters

input	- object containing the direct link to a hardware control source.
min	- bottom of range. (when input is 0.0)
max	- top of range (when input is 1.0)
curve	- the scaling curve for the input->output transformation.

6.31.4.2 Process()

```
float daisy::Parameter::Process ( )
```

processes the input signal, this should be called at the samplerate of the hid_ctrl passed in.

Returns

a float with the specified transformation applied.

6.31.4.3 Value()

```
float daisy::Parameter::Value ( )  [inline]
```

Returns

the current value from the parameter without processing another sample. this is useful if you need to use the value multiple times, and don't store the output of process in a local variable.

The documentation for this class was generated from the following file:

- src/hid_parameter.h

6.32 daisy::RgbLed Class Reference

```
#include <hid_rgb_led.h>
```

Public Member Functions

- void Init (dsy_gpio_pin red, dsy_gpio_pin green, dsy_gpio_pin blue, bool invert)
- void Set (float r, float g, float b)
- void SetColor (Color c)
- void Update ()

6.32.1 Detailed Description

3x LEDs configured as an RGB for ease of use.

6.32.2 Member Function Documentation

6.32.2.1 Init()

```
void daisy::RgbLed::Init (
            dsy_gpio_pin red,
            dsy_gpio_pin green,
            dsy_gpio_pin blue,
            bool invert )
```

Initializes 3x GPIO Pins as red, green, and blue elements of an RGB LED

Parameters

red	Red element
green	Green element
blue	Blue element
invert	Flips led polarity

6.32.2.2 Set()

```
void daisy::RgbLed::Set (
            float r,
            float g,
            float b )
```

Sets each element of the LED with a floating point number 0-1

Parameters

r	Red element
g	Green element
b	Blue element

6.32.2.3 SetColor()

```
void daisy::RgbLed::SetColor (
            Color c )
```

Sets the RGB using a Color object.

Parameters

c	Color object to set.

6.32.2.4 Update()

```
void daisy::RgbLed::Update ( )
```

Updates the PWM of the LED based on the current values. Should be called at a regular interval. (i.e. 1kHz/1ms)

The documentation for this class was generated from the following file:

- src/hid_rgb_led.h

6.33 daisy::RingBuffer< T, size > Class Template Reference

`#include <util_ringbuffer.h>`

Public Member Functions

- void Init ()
- size_t capacity () const
- size_t writable () const
- size_t readable () const
- void Write (T v)
- void Overwrite (T v)
- T Read ()
- T ImmediateRead ()
- void Flush ()
- void Swallow (size_t n)
- void ImmediateRead (T *destination, size_t num_elements)
- void Overwrite (const T *source, size_t num_elements)

6.33.1 Detailed Description

template<typename T, size_t size>
class daisy::RingBuffer< T, size >

Utility Ring Buffer
imported from pichenettes/stmlib

6.33.2 Member Function Documentation

6.33.2.1 capacity()

```
template<typename T, size_t size>
size_t daisy::RingBuffer< T, size >::capacity ( ) const  [inline]
```

Returns

The total size of the ring buffer

6.33.2.2 Flush()

```
template<typename T, size_t size>
void daisy::RingBuffer< T, size >::Flush ( )  [inline]
```

Flushes unread elements from the ring buffer

6.33.2.3 ImmediateRead() [1/2]

```
template<typename T, size_t size>
T daisy::RingBuffer< T, size >::ImmediateRead ( )  [inline]
```

Reads next element from ring buffer immediately

Returns

read value

6.33.2.4 ImmediateRead() [2/2]

```
template<typename T, size_t size>
void daisy::RingBuffer< T, size >::ImmediateRead (
            T * destination,
            size_t num_elements )  [inline]
```

Reads a number of elements into a buffer immediately

Parameters

destination	buffer to write to
num_elements	number of elements in buffer

6.33.2.5 Init()

```
template<typename T, size_t size>
void daisy::RingBuffer< T, size >::Init ( )  [inline]
```

Initializes the Ring Buffer

6.33.2.6 Overwrite() [1/2]

```
template<typename T, size_t size>
void daisy::RingBuffer< T, size >::Overwrite (
            T v )  [inline]
```

Writes the new element to the ring buffer, overwriting unread data if necessary.

Parameters

v	Value to overwrite

6.33.2.7 Overwrite() [2/2]

```
template<typename T, size_t size>
void daisy::RingBuffer< T, size >::Overwrite (
            const T * source,
            size_t num_elements )  [inline]
```

Overwrites a number of elements using the source buffer as input.

Parameters

source	Input buffer
num_elements	Number of elements in source

6.33.2.8 Read()

```
template<typename T, size_t size>
T daisy::RingBuffer< T, size >::Read ( )  [inline]
```

Reads the first available element from the ring buffer

Returns

read value

6.33.2.9 readable()

```
template<typename T, size_t size>
size_t daisy::RingBuffer< T, size >::readable ( ) const  [inline]
```

Returns

number of unread elements in ring buffer

6.33.2.10 Swallow()

```
template<typename T, size_t size>
void daisy::RingBuffer< T, size >::Swallow (
            size_t n )  [inline]
```

Read enough samples to make it possible to read 1 sample.

Parameters

n	Size of T ?

6.33.2.11 writable()

```
template<typename T, size_t size>
size_t daisy::RingBuffer< T, size >::writable ( ) const  [inline]
```

Returns

the number of samples that can be written to ring buffer without overwriting unread data.

6.33.2.12 Write()

```
template<typename T, size_t size>
void daisy::RingBuffer< T, size >::Write (
            T v )  [inline]
```

Writes the value to the next available position in the ring buffer

Parameters

v	Value to write

The documentation for this class was generated from the following file:

- src/util_ringbuffer.h

6.34 daisy::RingBuffer< T, 0 > Class Template Reference

```
#include <util_ringbuffer.h>
```

Public Member Functions

- void Init ()
- size_t capacity () const
- size_t writable () const
- size_t readable () const
- void Write (T v)
- void Overwrite (T v)
- T Read ()
- T ImmediateRead ()
- void Flush ()
- void ImmediateRead (T *destination, size_t num_elements)
- void Overwrite (const T *source, size_t num_elements)

6.34.1 Detailed Description

template<typename T>
class daisy::RingBuffer< T, 0 >

Utility Ring Buffer imported from pichenettes/stmlib

6.34.2 Member Function Documentation

6.34.2.1 capacity()

```
template<typename T >
size_t daisy::RingBuffer< T, 0 >::capacity ( ) const  [inline]
```

Returns

0

6.34.2.2 Flush()

```
template<typename T >
void daisy::RingBuffer< T, 0 >::Flush ( )  [inline]
```

Flush the buffer

6.34.2.3 ImmediateRead() [1/2]

```
template<typename T >
T daisy::RingBuffer< T, 0 >::ImmediateRead ( )  [inline]
```

Returns

Read value

6.34.2.4 ImmediateRead() [2/2]

```
template<typename T >
void daisy::RingBuffer< T, 0 >::ImmediateRead (
            T * destination,
            size_t num_elements )  [inline]
```

Parameters

destination	&
num_elements	&

6.34.2.5 Init()

```
template<typename T >
void daisy::RingBuffer< T, 0 >::Init ( )  [inline]
```

Initialize ringbuffer

6.34.2.6 Overwrite() [1/2]

```
template<typename T >
void daisy::RingBuffer< T, 0 >::Overwrite (
            T v )  [inline]
```

Parameters

v	Value to overwrite

6.34.2.7 Overwrite() [2/2]

```
template<typename T >
void daisy::RingBuffer< T, 0 >::Overwrite (
            const T * source,
            size_t num_elements )  [inline]
```

Parameters

source	3
num_elements	&

6.34.2.8 Read()

```
template<typename T >
T daisy::RingBuffer< T, 0 >::Read ( )  [inline]
```

Returns

Read value

6.34.2.9 readable()

```
template<typename T >
size_t daisy::RingBuffer< T, 0 >::readable ( ) const  [inline]
```

Returns

0

6.34.2.10 writable()

```
template<typename T >
size_t daisy::RingBuffer< T, 0 >::writable ( ) const  [inline]
```

Returns

0

6.34.2.11 Write()

```
template<typename T >
void daisy::RingBuffer< T, 0 >::Write (
            T v )  [inline]
```

Parameters

v	Value to write

The documentation for this class was generated from the following file:

- src/util_ringbuffer.h

6.35 daisy::SdmmcHandler Class Reference

```
#include <per_sdmmc.h>
```

Public Member Functions

- void Init ()

6.35.1 Detailed Description

Configuration for interfacing with SD cards. Currently only supports operation using FatFS filesystem

6.35.2 Member Function Documentation

6.35.2.1 Init()

```
void daisy::SdmmcHandler::Init ( )
```

Initializes the SD Card Interface For now all settings are fixed (See todo at top of section)

The documentation for this class was generated from the following file:

- src/per_sdmmc.h

6.36 daisy::SdmmcHandlerInit Struct Reference

```
#include <per_sdmmc.h>
```

Public Attributes

- SdmmcBitWidth bitdepth
- SdmmcSpeed speed

6.36.1 Detailed Description

Structure for setting the options above. Used to intiailize SdmmcHandler

6.36.2 Member Data Documentation

6.36.2.1 bitdepth

```
SdmmcBitWidth daisy::SdmmcHandlerInit::bitdepth
```

&

6.36.2.2 speed

```
SdmmcSpeed daisy::SdmmcHandlerInit::speed
```

&

The documentation for this struct was generated from the following file:

- src/per_sdmmc.h

6.37 ShiftRegister595 Class Reference

Device Driver for 8-bit shift register.
CD74HC595 - 8-bit serial to parallel output shift.

```
#include <dev_sr_595.h>
```

Public Types

- enum Pins { **PIN_LATCH**, PIN_CLK, PIN_DATA, NUM_PINS }

Public Member Functions

- void Init (dsy_gpio_pin *pin_cfg, size_t num_daisy_chained=1)
- void Set (uint8_t idx, bool state)
- void Write ()

6.37.1 Detailed Description

Device Driver for 8-bit shift register.
CD74HC595 - 8-bit serial to parallel output shift.

Author

shensley

Date

May 2020

6.37.2 Member Enumeration Documentation

6.37.2.1 Pins

```
enum ShiftRegister595::Pins
```

The following pins correspond to the hardware connections to the 595.

Enumerator

PIN_CLK	LATCH corresonds to Pin 12 "RCLK"
PIN_DATA	CLK corresponds to Pin 11 "SRCLK"
NUM_PINS	DATA corresponds to Pin 14 "SER"

6.37.3 Member Function Documentation

6.37.3.1 Init()

```
void ShiftRegister595::Init (
            dsy_gpio_pin * pin_cfg,
            size_t num_daisy_chained = 1 )
```

Initializes the GPIO, and data for the ShiftRegister

Parameters

pin_cfg	is an array of dsy_gpio_pin corresponding the the Pins enum above.
num_daisy_chained	(default = 1) is the number of 595 devices daisy chained together.

6.37.3.2 Set()

```
void ShiftRegister595::Set (
            uint8_t idx,
            bool state )
```

Sets the state of the specified output.

Parameters

idx	The index starts with QA on the first device and ends with QH on the last device.
state	A true state will set the output HIGH, while a false state will set the output LOW.

6.37.3.3 Write()

```
void ShiftRegister595::Write ( )
```

Writes the states of shift register out to the connected devices.

The documentation for this class was generated from the following file:

- src/dev_sr_595.h

6.38 daisy::SpiHandle Class Reference

`#include <per_spi.h>`

Public Member Functions

- void Init ()
- void BlockingTransmit (uint8_t *buff, size_t size)

6.38.1 Detailed Description

Handler for serial peripheral interface

6.38.2 Member Function Documentation

6.38.2.1 BlockingTransmit()

```
void daisy::SpiHandle::BlockingTransmit (
            uint8_t * buff,
            size_t size )
```

Blocking transmit

Parameters

*buff	input buffer
size	buffer size

6.38.2.2 Init()

```
void daisy::SpiHandle::Init ( )
```

Initializes handler

The documentation for this class was generated from the following file:

- src/per_spi.h

6.39 daisy::Switch Class Reference

`#include <hid_switch.h>`

Public Types

- enum Type { TYPE_TOGGLE, TYPE_MOMENTARY }
- enum Polarity { POLARITY_NORMAL, POLARITY_INVERTED }
- enum Pull { PULL_UP, PULL_DOWN, PULL_NONE }

Public Member Functions

- void Init (dsy_gpio_pin pin, float update_rate, Type t, Polarity pol, Pull pu)
- void Init (dsy_gpio_pin pin, float update_rate)
- void Debounce ()
- bool RisingEdge () const
- bool FallingEdge () const
- bool Pressed () const
- float TimeHeldMs () const

6.39.1 Detailed Description

Generic Class for handling momentary/latching switches
Inspired/influenced by Mutable Instruments (pichenettes) Switch classes

Author

Stephen Hensley

Date

December 2019

6.39.2 Member Enumeration Documentation

6.39.2.1 Polarity

`enum daisy::Switch::Polarity`

Specifies whether the pressed is HIGH or LOW.

Enumerator

POLARITY_NORMAL	&
POLARITY_INVERTED	&

6.39.2.2 Pull

enum daisy::Switch::Pull

Specifies whether to use built-in Pull Up/Down resistors to hold button at a given state when not engaged.

Enumerator

PULL_UP	&
PULL_DOWN	&
PULL_NONE	&

6.39.2.3 Type

enum daisy::Switch::Type

Specifies the expected behavior of the switch

Enumerator

TYPE_TOGGLE	&
TYPE_MOMENTARY	&

6.39.3 Member Function Documentation

6.39.3.1 Debounce()

void daisy::Switch::Debounce ( )

Called at update_rate to debounce and handle timing for the switch. In order for events not to be missed, its important that the Edge/Pressed checks be made at the same rate as the debounce function is being called.

6.39.3.2 FallingEdge()

bool daisy::Switch::FallingEdge ( ) const [inline]

Returns

true if the button was just released

6.39.3.3 Init() [1/2]

```
void daisy::Switch::Init (
            dsy_gpio_pin pin,
            float update_rate,
            Type t,
            Polarity pol,
            Pull pu )
```

Initializes the switch object with a given port/pin combo.

Parameters

pin	port/pin object to tell the switch which hardware pin to use.
update_rate	the rate at which the Debounce() function will be called. (used for timing).
t	switch type – Default: TYPE_MOMENTARY
pol	switch polarity – Default: POLARITY_INVERTED
pu	switch pull up/down – Default: PULL_UP

6.39.3.4 Init() [2/2]

```
void daisy::Switch::Init (
            dsy_gpio_pin pin,
            float update_rate )
```

Simplified Init.

Parameters

pin	port/pin object to tell the switch which hardware pin to use.
update_rate	the rate at which the Debounce() function will be called. (used for timing).

6.39.3.5 Pressed()

```
bool daisy::Switch::Pressed ( ) const  [inline]
```

Returns

true if the button is held down (or if the toggle is on)

6.39.3.6 RisingEdge()

```
bool daisy::Switch::RisingEdge ( ) const  [inline]
```

Returns

true if a button was just pressed.

6.39.3.7 TimeHeldMs()

```
float daisy::Switch::TimeHeldMs ( ) const  [inline]
```

Returns

the time in milliseconds that the button has been held (or toggle has been on)

The documentation for this class was generated from the following file:

- src/hid_switch.h

6.40 daisy::UartHandler Class Reference

```
#include <per_uart.h>
```

Public Member Functions

- void Init ()
- int PollReceive (uint8_t *buff, size_t size, uint32_t timeout)
- int StartRx (size_t size)
- bool RxActive ()
- int FlushRx ()
- int PollTx (uint8_t *buff, size_t size)
- uint8_t PopRx ()
- size_t Readable ()
- int CheckError ()

6.40.1 Detailed Description

Uart Peripheral

Author

shensley

Date

March 2020

6.40.2 Member Function Documentation

6.40.2.1 CheckError()

```
int daisy::UartHandler::CheckError ( )
```

Returns

the result of HAL_UART_GetError() to the user.

6.40.2.2 FlushRx()

```
int daisy::UartHandler::FlushRx ( )
```

Flushes the Receive Queue

Returns

OK or ERROR

6.40.2.3 Init()

```
void daisy::UartHandler::Init ( )
```

Initializes the UART Peripheral

6.40.2.4 PollReceive()

```
int daisy::UartHandler::PollReceive (
            uint8_t * buff,
            size_t size,
            uint32_t timeout )
```

Reads the amount of bytes in blocking mode with a 10ms timeout.

Parameters

∗buff	Buffer to read to
size	Buff size
timeout	How long to timeout for (10ms?)

Returns

Data received

6.40.2.5 PollTx()

```
int daisy::UartHandler::PollTx (
            uint8_t * buff,
            size_t size )
```

Sends an amount of data in blocking mode.

Parameters

**buff*	Buffer of data to send
size	Buffer size

Returns

OK or ERROR

6.40.2.6 PopRx()

```
uint8_t daisy::UartHandler::PopRx ( )
```

Pops the oldest byte from the FIFO.

Returns

Popped byte

6.40.2.7 Readable()

```
size_t daisy::UartHandler::Readable ( )
```

Checks if there are any unread bytes in the FIFO

Returns

1 or 0 ??

6.40.2.8 RxActive()

```
bool daisy::UartHandler::RxActive ( )
```

Returns

whether Rx DMA is listening or not.

6.40.2.9 StartRx()

```
int daisy::UartHandler::StartRx (
            size_t size )
```

Starts a DMA Receive callback to fill a buffer of specified size. Data is populated into a FIFO queue, and can be queried with the functions below. Maximum Buffer size is defined above. If a value outside of the maximum is specified, the size will be set to the maximum.

Parameters

size	Queue size

Returns

OK or ERROR

The documentation for this class was generated from the following file:

- src/per_uart.h

6.41 daisy::UsbHandle Class Reference

Interface for initializing and using the USB Peripherals on the daisy.

```
#include <hid_usb.h>
```

Public Types

- enum UsbPeriph { FS_INTERNAL, FS_EXTERNAL, FS_BOTH }
- typedef void(* ReceiveCallback) (uint8_t *buff, uint32_t *len)

Public Member Functions

- void Init (UsbPeriph dev)
- void TransmitInternal (uint8_t *buff, size_t size)
- void TransmitExternal (uint8_t *buff, size_t size)
- void SetReceiveCallback (ReceiveCallback cb)

6.41.1 Detailed Description

Interface for initializing and using the USB Peripherals on the daisy.

Author

Stephen Hensley

Date

December 2019

6.41.2 Member Typedef Documentation

6.41.2.1 ReceiveCallback

```
typedef void(* daisy::UsbHandle::ReceiveCallback) (uint8_t *buff, uint32_t *len)
```

Function called upon reception of a buffer

6.41.3 Member Enumeration Documentation

6.41.3.1 UsbPeriph

```
enum daisy::UsbHandle::UsbPeriph
```

Specified which of the two USB Peripherals to initialize.

Enumerator

FS_INTERNAL	Internal pin
FS_EXTERNAL	FS External D+ pin is Pin 38 (GPIO32). FS External D- pin is Pin 37 (GPIO31)
FS_BOTH	Both

6.41.4 Member Function Documentation

6.41.4.1 Init()

```
void daisy::UsbHandle::Init (
            UsbPeriph dev )
```

Initializes the specified peripheral(s) as USB CDC Devices

Parameters

dev	Device to initialize

6.41.4.2 SetReceiveCallback()

```
void daisy::UsbHandle::SetReceiveCallback (
            ReceiveCallback cb )
```

sets the callback to be called upon reception of new data

Parameters

cb	Function to serve as callback

6.41.4.3 TransmitExternal()

```
void daisy::UsbHandle::TransmitExternal (
            uint8_t * buff,
            size_t size )
```

Transmits a buffer of 'size' bytes from a USB port connected to the external USB Pins of the daisy seed.

Parameters

buff	Buffer to transmit
size	Buffer size

6.41.4.4 TransmitInternal()

```
void daisy::UsbHandle::TransmitInternal (
            uint8_t * buff,
            size_t size )
```

Transmits a buffer of 'size' bytes from the on board USB FS port.

Parameters

buff	Buffer to transmit
size	Buffer size

The documentation for this class was generated from the following file:

- src/hid_usb.h

6.42 WAV_FormatTypeDef Struct Reference

`#include <util_wav_format.h>`

Public Attributes

- uint32_t ChunkId
- uint32_t FileSize
- uint32_t FileFormat
- uint32_t SubChunk1ID
- uint32_t SubChunk1Size
- uint16_t AudioFormat
- uint16_t NbrChannels
- uint32_t SampleRate
- uint32_t ByteRate
- uint16_t BlockAlign
- uint16_t BitPerSample
- uint32_t SubChunk2ID
- uint32_t SubCHunk2Size

6.42.1 Detailed Description

Helper struct for handling the WAV file format

6.42.2 Member Data Documentation

6.42.2.1 AudioFormat

`uint16_t WAV_FormatTypeDef::AudioFormat`

&

6.42.2.2 BitPerSample

```
uint16_t WAV_FormatTypeDef::BitPerSample
```

&

6.42.2.3 BlockAlign

```
uint16_t WAV_FormatTypeDef::BlockAlign
```

&

6.42.2.4 ByteRate

```
uint32_t WAV_FormatTypeDef::ByteRate
```

&

6.42.2.5 ChunkId

```
uint32_t WAV_FormatTypeDef::ChunkId
```

&

6.42.2.6 FileFormat

```
uint32_t WAV_FormatTypeDef::FileFormat
```

&

6.42.2.7 FileSize

```
uint32_t WAV_FormatTypeDef::FileSize
```

&

6.42.2.8 NbrChannels

```
uint16_t WAV_FormatTypeDef::NbrChannels
```

&

6.42.2.9 SampleRate

```
uint32_t WAV_FormatTypeDef::SampleRate
```

&

6.42.2.10 SubChunk1ID

```
uint32_t WAV_FormatTypeDef::SubChunk1ID
```

&

6.42.2.11 SubChunk1Size

```
uint32_t WAV_FormatTypeDef::SubChunk1Size
```

&

6.42.2.12 SubChunk2ID

```
uint32_t WAV_FormatTypeDef::SubChunk2ID
```

&

6.42.2.13 SubCHunk2Size

```
uint32_t WAV_FormatTypeDef::SubCHunk2Size
```

&

The documentation for this struct was generated from the following file:

- src/util_wav_format.h

6.43 daisy::WavFileInfo Struct Reference

```
#include <hid_wavplayer.h>
```

Public Attributes

- WAV_FormatTypeDef raw_data
- char name [256]

6.43.1 Detailed Description

Struct containing details of Wav File.

6.43.2 Member Data Documentation

6.43.2.1 name

```
char daisy::WavFileInfo::name[256]
```

Wav filename

6.43.2.2 raw_data

```
WAV_FormatTypeDef daisy::WavFileInfo::raw_data
```

Raw wav data

The documentation for this struct was generated from the following file:

- src/hid_wavplayer.h

6.44 daisy::WavPlayer Class Reference

```
#include <hid_wavplayer.h>
```

Public Member Functions

- void Init ()
- int Open (size_t sel)
- int Close ()
- int16_t Stream ()
- void Prepare ()
- void Restart ()
- void SetLooping (bool loop)
- bool GetLooping () const
- size_t GetNumberFiles () const
- size_t GetCurrentFile () const

6.44.1 Detailed Description

Wav Player that will load .wav files from an SD Card, and then provide a method of accessing the samples with double-buffering.

6.44.2 Member Function Documentation

6.44.2.1 Close()

```
int daisy::WavPlayer::Close ( )
```

Closes whatever file is currently open.

Returns

&

6.44.2.2 GetCurrentFile()

```
size_t daisy::WavPlayer::GetCurrentFile ( ) const  [inline]
```

Returns

currently selected file.

6.44.2.3 GetLooping()

```
bool daisy::WavPlayer::GetLooping ( ) const  [inline]
```

Returns

Whether the WavPlayer is looping or not.

6.44.2.4 GetNumberFiles()

```
size_t daisy::WavPlayer::GetNumberFiles ( ) const  [inline]
```

Returns

The number of files loaded by the WavPlayer

6.44.2.5 Init()

```
void daisy::WavPlayer::Init ( )
```

Initializes the WavPlayer, loading up to max_files of wav files from an SD Card.

6.44.2.6 Open()

```
int daisy::WavPlayer::Open (
            size_t sel )
```

Opens the file at index sel for reading.

Parameters

sel	File to open

6.44.2.7 Prepare()

```
void daisy::WavPlayer::Prepare ( )
```

Collects buffer for playback when needed.

6.44.2.8 Restart()

```
void daisy::WavPlayer::Restart ( )
```

Resets the playback position to the beginning of the file immediately

6.44.2.9 SetLooping()

```
void daisy::WavPlayer::SetLooping (
            bool loop )  [inline]
```

Sets whether or not the current file will repeat after completing playback.

Parameters

loop	To loop or not to loop.

6.44.2.10 Stream()

```
int16_t daisy::WavPlayer::Stream ( )
```

Returns

The next sample if playing, otherwise returns 0

The documentation for this class was generated from the following file:

- src/hid_wavplayer.h

Chapter 7

File Documentation

7.1 src/daisy.h File Reference

```
#include <stdint.h>
#include "daisy_core.h"
#include "sys_system.h"
#include "per_qspi.h"
#include "per_dac.h"
#include "per_gpio.h"
#include "per_i2c.h"
#include "per_sai.h"
#include "per_tim.h"
#include "dev_leddriver.h"
#include "dev_sdram.h"
#include "dev_sr_4021.h"
#include "hid_audio.h"
#include "util_unique_id.h"
#include "per_adc.h"
#include "per_uart.h"
#include "hid_midi.h"
#include "hid_encoder.h"
#include "hid_switch.h"
#include "hid_ctrl.h"
#include "hid_gatein.h"
#include "hid_parameter.h"
#include "hid_usb.h"
#include "per_sdmmc.h"
#include "per_spi.h"
#include "hid_oled_display.h"
#include "hid_wavplayer.h"
#include "hid_led.h"
#include "hid_rgb_led.h"
#include "dev_sr_595.h"
```

Macros

- #define FBIPMAX 0.999985f
- #define FBIPMIN (-FBIPMAX)

- #define S162F_SCALE 3.0517578125e-05f
- #define F2S16_SCALE 32767.0f
- #define F2S24_SCALE 8388608.0f
- #define S242F_SCALE 1.192092896e-07f
- #define S24SIGN 0x800000

Functions

- FORCE_INLINE float s162f (int16_t x)
- FORCE_INLINE int16_t f2s16 (float x)
- FORCE_INLINE float s242f (int32_t x)
- FORCE_INLINE int32_t f2s24 (float x)

7.1.1 Macro Definition Documentation

7.1.1.1 F2S16_SCALE

```
#define F2S16_SCALE 32767.0f
```

(2 ** 15) - 1

7.1.1.2 F2S24_SCALE

```
#define F2S24_SCALE 8388608.0f
```

2 ** 23

7.1.1.3 FBIPMAX

```
#define FBIPMAX 0.999985f
```

close to 1.0f-LSB at 16 bit

7.1.1.4 FBIPMIN

```
#define FBIPMIN (-FBIPMAX)
```

- (1 - LSB)

7.1.1.5 S162F_SCALE

```
#define S162F_SCALE 3.0517578125e-05f
```

1 / (2** 15)

7.1.1.6 S242F_SCALE

```
#define S242F_SCALE 1.192092896e-07f
```

1 / (2 ** 23)

7.1.1.7 S24SIGN

```
#define S24SIGN 0x800000
```

2 ** 23

7.1.2 Function Documentation

7.1.2.1 f2s16()

```
FORCE_INLINE int16_t f2s16 (
            float x )
```

& < close to 1.0f-LSB at 16 bit

< - (1 - LSB)

< close to 1.0f-LSB at 16 bit

< - (1 - LSB)

< close to 1.0f-LSB at 16 bit

< close to 1.0f-LSB at 16 bit

< (2 ** 15) - 1

7.1.2.2 f2s24()

```
FORCE_INLINE int32_t f2s24 (
            float x )
```

& < close to 1.0f-LSB at 16 bit

< - (1 - LSB)

< close to 1.0f-LSB at 16 bit

< - (1 - LSB)

< close to 1.0f-LSB at 16 bit

< close to 1.0f-LSB at 16 bit

< 2 ** 23

7.1.2.3 s162f()

```
FORCE_INLINE float s162f (
            int16_t x )
```

Scales float by 1/(2 ^ 15)

Parameters

x	Number to be scaled.

Returns

Scaled number.

< 1 / (2** 15)

7.1.2.4 s242f()

```
FORCE_INLINE float s242f (
            int32_t x )
```

< 2 ** 23

< 2 ** 23

< 1 / (2 ** 23)

7.2 src/daisy_core.h File Reference

```
#include <stdint.h>
#include <stdlib.h>
```

Classes

- struct dsy_gpio_pin

Macros

- #define DSY_CORE_HW_H
- #define DMA_BUFFER_MEM_SECTION __attribute__((section(".sram1_bss")))
- #define DTCM_MEM_SECTION __attribute__((section(".dtcmram_bss")))

Enumerations

- enum dsy_gpio_port {
  DSY_GPIOA, DSY_GPIOB, DSY_GPIOC, DSY_GPIOD,
  DSY_GPIOE, DSY_GPIOF, DSY_GPIOG, DSY_GPIOH,
  DSY_GPIOI, DSY_GPIOJ, DSY_GPIOK, **DSY_GPIOX**,
  DSY_GPIO_LAST }

Functions

- FORCE_INLINE float cube (float x)
- FORCE_INLINE dsy_gpio_pin dsy_pin (dsy_gpio_port port, uint8_t pin)
- FORCE_INLINE uint8_t dsy_pin_cmp (dsy_gpio_pin *a, dsy_gpio_pin *b)

7.2.1 Macro Definition Documentation

7.2.1.1 DMA_BUFFER_MEM_SECTION

```
#define DMA_BUFFER_MEM_SECTION __attribute__((section(".sram1_bss")))
```

Macro for area of memory that is configured as cacheless This should be used primarily for DMA buffers, and the like.

7.2.1.2 DSY_CORE_HW_H

```
#define DSY_CORE_HW_H
```

&

7.2.1.3 DTCM_MEM_SECTION

```
#define DTCM_MEM_SECTION __attribute__((section(".dtcmram_bss")))
```

THE DTCM RAM section is also non-cached. However, is not suitable for DMA transfers. Performance is on par with internal SRAM w/ cache enabled.

7.2.2 Enumeration Type Documentation

7.2.2.1 dsy_gpio_port

```
enum dsy_gpio_port
```

Enums and a simple struct for defining a hardware pin on the MCU These correlate with the stm32 datasheet, and are used to configure the hardware.

Enumerator

DSY_GPIOA	&
DSY_GPIOB	&
DSY_GPIOC	&
DSY_GPIOD	&
DSY_GPIOE	&
DSY_GPIOF	&
DSY_GPIOG	&
DSY_GPIOH	&

7.2.3 Function Documentation

7.2.3.1 cube()

```
FORCE_INLINE float cube (
            float x )
```

Computes cube.

Parameters

x	Number to be cubed

Returns

x ^ 3

7.2.3.2 dsy_pin()

```
FORCE_INLINE dsy_gpio_pin dsy_pin (
            dsy_gpio_port port,
            uint8_t pin )
```

Helper for creating pins from port/pin combos easily

7.2.3.3 dsy_pin_cmp()

```
FORCE_INLINE uint8_t dsy_pin_cmp (
            dsy_gpio_pin * a,
            dsy_gpio_pin * b )
```

Helper for testing sameness of two dsy_gpio_pins

Returns

1 if same, 0 if different

7.3 src/daisy_field.h File Reference

Hardware defines and helpers for daisy field platform.

```
#include "daisy_seed.h"
```

Classes

- struct daisy::daisy_field

Macros

- #define DSY_FIELD_BSP_H
- #define SAMPLE_RATE DSY_AUDIO_SAMPLE_RATE
- #define SW_1_PIN 29
- #define SW_2_PIN 28
- #define SW_3_PIN 27
- #define GATE_OUT_PIN 0
- #define GATE_IN_PIN 1
- #define KB_SW_SR_CS_PIN 8
- #define KB_SW_SR_CLK_PIN 9
- #define KB_SW_SR_D1_PIN 10
- #define KB_SW_SR_D2_PIN 11
- #define MIDI_OUT_PIN 14
- #define MIDI_IN_PIN 15
- #define MUX_SEL_0_PIN 21
- #define MUX_SEL_1_PIN 20
- #define MUX_SEL_2_PIN 19
- #define MUX_ADC_PIN 16
- #define CV1_ADC_PIN 17
- #define CV2_ADC_PIN 18
- #define CV3_ADC_PIN 23
- #define CV4_ADC_PIN 22
- #define LED_DRIVER_I2C i2c1_handle

Enumerations

- enum { daisy::SW_2, daisy::SW_1, daisy::SW_3, daisy::SW_LAST }
- enum {
  daisy::KNOB_1, daisy::KNOB_3, daisy::KNOB_5, daisy::KNOB_2,
  daisy::KNOB_4, daisy::KNOB_6, daisy::KNOB_7, daisy::KNOB_8,
  daisy::KNOB_LAST }
- enum {
  CV_1, daisy::CV_2, daisy::CV_3, daisy::CV_4,
  daisy::CV_LAST }
- enum {
  daisy::LED_KEY_A8, daisy::LED_KEY_A7, daisy::LED_KEY_A6, daisy::LED_KEY_A5,
  daisy::LED_KEY_A4, daisy::LED_KEY_A3, daisy::LED_KEY_A2, daisy::LED_KEY_A1,
  daisy::LED_KEY_B1, daisy::LED_KEY_B2, daisy::LED_KEY_B3, daisy::LED_KEY_B4,
  daisy::LED_KEY_B5, daisy::LED_KEY_B6, daisy::LED_KEY_B7, daisy::LED_KEY_B8,
  daisy::LED_KNOB_1, daisy::LED_KNOB_2, daisy::LED_KNOB_3, daisy::LED_KNOB_4,
  daisy::LED_KNOB_5, daisy::LED_KNOB_6, daisy::LED_KNOB_7, daisy::LED_KNOB_8,
  daisy::LED_SW_1, daisy::LED_SW_2, daisy::LED_LAST }

Functions

- FORCE_INLINE void daisy::daisy_field_init (daisy_field *p)

7.3.1 Detailed Description

Hardware defines and helpers for daisy field platform.

7.3.2 Macro Definition Documentation

7.3.2.1 CV1_ADC_PIN

```
#define CV1_ADC_PIN 17
```

&

7.3.2.2 CV2_ADC_PIN

```
#define CV2_ADC_PIN 18
```

&

7.3.2.3 CV3_ADC_PIN

```
#define CV3_ADC_PIN 23
```

&

7.3.2.4 CV4_ADC_PIN

```
#define CV4_ADC_PIN 22
```

&

7.3.2.5 DSY_FIELD_BSP_H

```
#define DSY_FIELD_BSP_H
```

&

7.3.2.6 GATE_IN_PIN

```
#define GATE_IN_PIN 1
```

&

7.3.2.7 GATE_OUT_PIN

```
#define GATE_OUT_PIN 0
```

&

7.3.2.8 KB_SW_SR_CLK_PIN

```
#define KB_SW_SR_CLK_PIN 9
```

&

7.3.2.9 KB_SW_SR_CS_PIN

```
#define KB_SW_SR_CS_PIN 8
```

&

7.3.2.10 KB_SW_SR_D1_PIN

```
#define KB_SW_SR_D1_PIN 10
```

&

7.3.2.11 KB_SW_SR_D2_PIN

```
#define KB_SW_SR_D2_PIN 11
```

&

7.3.2.12 LED_DRIVER_I2C

```
#define LED_DRIVER_I2C i2c1_handle
```

&

7.3.2.13 MIDI_IN_PIN

```
#define MIDI_IN_PIN 15
```

&

7.3.2.14 MIDI_OUT_PIN

```
#define MIDI_OUT_PIN 14
```

&

7.3.2.15 MUX_ADC_PIN

```
#define MUX_ADC_PIN 16
```

&

7.3.2.16 MUX_SEL_0_PIN

```
#define MUX_SEL_0_PIN 21
```

&

7.3.2.17 MUX_SEL_1_PIN

```
#define MUX_SEL_1_PIN 20
```

&

7.3.2.18 MUX_SEL_2_PIN

```
#define MUX_SEL_2_PIN 19
```

&

7.3.2.19 SAMPLE_RATE

```
#define SAMPLE_RATE DSY_AUDIO_SAMPLE_RATE
```

&

7.3.2.20 SW_1_PIN

```
#define SW_1_PIN 29
```

&

7.3.2.21 SW_2_PIN

```
#define SW_2_PIN 28
```

&

7.3.2.22 SW_3_PIN

```
#define SW_3_PIN 27
```

&

7.3.3 Enumeration Type Documentation

7.3.3.1 anonymous enum

```
anonymous enum
```

enums for controls, etc.

Enumerator

SW_2	tactile switch
SW_1	tactile switch
SW_3	toggle
SW_LAST	&

7.3.3.2 anonymous enum

```
anonymous enum
```

All knobs connect to ADC1_INP10 via CD4051 mux

Enumerator

KNOB_1	&
KNOB_3	&
KNOB_5	&
KNOB_2	&
KNOB_4	&
KNOB_6	&
KNOB_7	&
KNOB_8	&
KNOB_LAST	&

7.3.3.3 anonymous enum

```
anonymous enum
```

Enumerator

CV_2	Connected to ADC1_INP17
CV_3	Connected to ADC1_INP15
CV_4	Connected to ADC1_INP4
CV_LAST	Connected to ADC1_INP11 &

7.3.3.4 anonymous enum

```
anonymous enum
```

Enumerator

LED_KEY_A8	&
LED_KEY_A7	&
LED_KEY_A6	&
LED_KEY_A5	&
LED_KEY_A4	&
LED_KEY_A3	&
LED_KEY_A2	&
LED_KEY_A1	&
LED_KEY_B1	&
LED_KEY_B2	&
LED_KEY_B3	&
LED_KEY_B4	&
LED_KEY_B5	&
LED_KEY_B6	&
LED_KEY_B7	&
LED_KEY_B8	&
LED_KNOB↩_1	&
LED_KNOB↩_2	&
LED_KNOB↩_3	&
LED_KNOB↩_4	&
LED_KNOB↩_5	&
LED_KNOB↩_6	&
LED_KNOB↩_7	&
LED_KNOB↩_8	&
LED_SW_1	&
LED_SW_2	&
LED_LAST	&

7.3.4 Function Documentation

7.3.4.1 daisy_field_init()

```
FORCE_INLINE void daisy::daisy_field_init (
            daisy_field * p )
```

Initializes daisy field

Parameters

p	daisy_field struct to initialize

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

< &

7.4 src/daisy_patch.h File Reference

```
#include "daisy_seed.h"
```

Classes

- class daisy::DaisyPatch

  Class that handles initializing all of the hardware specific to the Daisy Patch Board.
  Helper funtions are also in place to provide easy access to built-in controls and peripherals.

7.5 src/daisy_petal.h File Reference

```
#include "daisy_seed.h"
```

Classes

- class daisy::DaisyPetal

  Helpers and hardware definitions for daisy petal.

Macros

- #define DSY_PETAL_H

7.5.1 Macro Definition Documentation

7.5.1.1 DSY_PETAL_H

```
#define DSY_PETAL_H
```

&

7.6 src/daisy_pod.h File Reference

```
#include "daisy_seed.h"
```

Classes

- class daisy::DaisyPod

  Class that handles initializing all of the hardware specific to the Daisy Patch Board.
  Helper funtions are also in place to provide easy access to built-in controls and peripherals.

7.7 src/daisy_seed.h File Reference

```
#include "daisy.h"
```

Classes

- class daisy::DaisySeed

  This is the higher-level interface for the Daisy board.
  All basic peripheral configuration/initialization is setup here.

7.8 src/dev_codec_ak4556.h File Reference

Driver for the AK4556 Stereo Codec.

```
#include "daisy_core.h"
```

Functions

- void codec_ak4556_init (dsy_gpio_pin reset_pin)

7.8.1 Detailed Description

Driver for the AK4556 Stereo Codec.

7.8.2 Function Documentation

7.8.2.1 codec_ak4556_init()

```
void codec_ak4556_init (
            dsy_gpio_pin reset_pin )
```

Resets the AK4556

Parameters

reset_pin	should be a dsy_gpio_pin that is connected to the RST pin of the AK4556

7.9 src/dev_codec_pcm3060.h File Reference

Driver for the PCM3060 Codec.

```
#include "per_i2c.h"
```

Functions

- void codec_pcm3060_init (dsy_i2c_handle *hi2c)

7.9.1 Detailed Description

Driver for the PCM3060 Codec.

7.9.2 Function Documentation

7.9.2.1 codec_pcm3060_init()

```
void codec_pcm3060_init (
            dsy_i2c_handle * hi2c )
```

Resets the PCM060

Parameters

*hi2c	array of pins handling i2c?

7.10 src/dev_codec_wm8731.h File Reference

Driver for the WM8731 Codec.

```
#include <stddef.h>
#include "per_sai.h"
#include "per_i2c.h"
```

Functions

- uint8_t codec_wm8731_init (dsy_i2c_handle *hi2c, uint8_t mcu_is_master, int32_t sample_rate, uint8_t bitdepth)
- uint8_t codec_wm8731_enter_bypass (dsy_i2c_handle *hi2c)
- uint8_t codec_wm8731_exit_bypass (dsy_i2c_handle *hi2c)

7.10.1 Detailed Description

Driver for the WM8731 Codec.

7.10.2 Function Documentation

7.10.2.1 codec_wm8731_enter_bypass()

```
uint8_t codec_wm8731_enter_bypass (
            dsy_i2c_handle * hi2c )
```

Put codec into bypass mode

Parameters

**hi2c*	pins handling i2c

7.10.2.2 codec_wm8731_exit_bypass()

```
uint8_t codec_wm8731_exit_bypass (
            dsy_i2c_handle * hi2c )
```

Take codec out of bypass mode

Parameters

**hi2c*	pins handling i2c

7.10.2.3 codec_wm8731_init()

```
uint8_t codec_wm8731_init (
            dsy_i2c_handle * hi2c,
            uint8_t mcu_is_master,
            int32_t sample_rate,
            uint8_t bitdepth )
```

Resets the WM8731

Parameters

**hi2c*	array of pins handling i2c?
mcu_is_master	&
sample_rate	Sample rate to run codec at
bitdepth	Bit depth to run codec at

7.11 src/dev_codec_wm8731_frame.h File Reference

WM8731 Codec framework.

```
#include <stddef.h>
```

Classes

- struct codec_frame_t

Typedefs

- typedef void(* sa_audio_callback) (codec_frame_t *, codec_frame_t *, size_t)

7.11.1 Detailed Description

WM8731 Codec framework.

7.11.2 Typedef Documentation

7.11.2.1 sa_audio_callback

```
typedef void(* sa_audio_callback) (codec_frame_t *, codec_frame_t *, size_t)
```

&

7.12 src/dev_flash_IS25LP064A.h File Reference

IS25LP08D Commands.

Macros

- #define IS25LP064A_H
- #define IS25LP064A_FLASH_SIZE 0x800000
- #define IS25LP064A_SECTOR_SIZE 0x10000
- #define IS25LP064A_SUBSECTOR_SIZE 0x1000
- #define IS25LP064A_PAGE_SIZE 0x100
- #define IS25LP064A_DUMMY_CYCLES_READ_QUAD 8
- #define IS25LP064A_DUMMY_CYCLES_READ 8
- #define IS25LP064A_DUMMY_CYCLES_READ_DTR 6
- #define IS25LP064A_DUMMY_CYCLES_READ_QUAD_DTR 6
- #define IS25LP064A_DIE_ERASE_MAX_TIME 460000
- #define IS25LP064A_SECTOR_ERASE_MAX_TIME 1000
- #define IS25LP064A_SUBSECTOR_ERASE_MAX_TIME 400
- #define RESET_ENABLE_CMD 0x66
- #define RESET_MEMORY_CMD 0x99
- #define READ_ID_CMD 0x9E
- #define READ_ID_CMD2 0x9F
- #define MULTIPLE_IO_READ_ID_CMD 0xAF
- #define READ_SERIAL_FLASH_DISCO_PARAM_CMD 0x5A
- #define READ_CMD 0x03
- #define READ_4_BYTE_ADDR_CMD 0x13
- #define FAST_READ_CMD 0x0B
- #define FAST_READ_DTR_CMD 0x0D
- #define FAST_READ_4_BYTE_ADDR_CMD 0x0C
- #define DUAL_OUT_FAST_READ_CMD 0x3B
- #define DUAL_OUT_FAST_READ_DTR_CMD 0x3D
- #define DUAL_OUT_FAST_READ_4_BYTE_ADDR_CMD 0x3C
- #define DUAL_INOUT_FAST_READ_CMD 0xBB
- #define DUAL_INOUT_FAST_READ_DTR_CMD 0xBD
- #define DUAL_INOUT_FAST_READ_4_BYTE_ADDR_CMD 0xBC
- #define QUAD_OUT_FAST_READ_CMD 0x6B
- #define QUAD_OUT_FAST_READ_DTR_CMD 0x0D
- #define QUAD_OUT_FAST_READ_4_BYTE_ADDR_CMD 0x6C
- #define QUAD_INOUT_FAST_READ_CMD 0xEB
- #define QUAD_INOUT_FAST_READ_DTR_CMD 0xED
- #define QUAD_INOUT_FAST_READ_4_BYTE_ADDR_CMD 0xEC
- #define WRITE_ENABLE_CMD 0x06
- #define WRITE_DISABLE_CMD 0x04
- #define READ_STATUS_REG_CMD 0x05
- #define WRITE_STATUS_REG_CMD 0x01
- #define READ_LOCK_REG_CMD 0xE8
- #define WRITE_LOCK_REG_CMD 0xE5
- #define READ_FLAG_STATUS_REG_CMD 0x70
- #define CLEAR_FLAG_STATUS_REG_CMD 0x50
- #define READ_NONVOL_CFG_REG_CMD 0xB5
- #define WRITE_NONVOL_CFG_REG_CMD 0xB1
- #define READ_READ_PARAM_REG_CMD 0x61
- #define WRITE_READ_PARAM_REG_CMD 0xC0
- #define READ_ENHANCED_VOL_CFG_REG_CMD 0x81
- #define WRITE_ENHANCED_VOL_CFG_REG_CMD 0x85
- #define READ_EXT_ADDR_REG_CMD 0xC8
- #define WRITE_EXT_ADDR_REG_CMD 0xC5
- #define PAGE_PROG_CMD 0x02
- #define PAGE_PROG_4_BYTE_ADDR_CMD 0x12

- #define DUAL_IN_FAST_PROG_CMD 0xA2
- #define EXT_DUAL_IN_FAST_PROG_CMD 0xD2
- #define QUAD_IN_FAST_PROG_CMD 0x32
- #define EXT_QUAD_IN_FAST_PROG_CMD 0x38
- #define QUAD_IN_FAST_PROG_4_BYTE_ADDR_CMD 0x34
- #define SUBSECTOR_ERASE_CMD 0xd7
- #define SUBSECTOR_ERASE_QPI_CMD 0x20
- #define SUBSECTOR_ERASE_4_BYTE_ADDR_CMD 0x21
- #define SECTOR_ERASE_CMD 0xD8
- #define SECTOR_ERASE_4_BYTE_ADDR_CMD 0xDC
- #define BLOCK_ERASE_32K_CMD 0x52
- #define DIE_ERASE_CMD 0xC4
- #define PROG_ERASE_RESUME_CMD 0x7A
- #define PROG_ERASE_SUSPEND_CMD 0x75
- #define READ_OTP_ARRAY_CMD 0x4B
- #define PROG_OTP_ARRAY_CMD 0x42
- #define ENTER_4_BYTE_ADDR_MODE_CMD 0xB7
- #define EXIT_4_BYTE_ADDR_MODE_CMD 0xE9
- #define ENTER_QUAD_CMD 0x35
- #define EXIT_QUAD_CMD 0xF5
- #define IS25LP064A_SR_WIP ((uint8_t)0x01)

  IS25LP08D Registers.
- #define IS25LP064A_SR_WREN ((uint8_t)0x02)
- #define IS25LP064A_SR_SRWREN ((uint8_t)0x80)
- #define IS25LP064A_SR_QE ((uint8_t)0x40)
- #define IS25LP064A_NVCR_NBADDR ((uint16_t)0x0001)
- #define IS25LP064A_NVCR_SEGMENT ((uint16_t)0x0002)
- #define IS25LP064A_NVCR_DUAL ((uint16_t)0x0004)
- #define IS25LP064A_NVCR_QUAB ((uint16_t)0x0008)
- #define IS25LP064A_NVCR_RH ((uint16_t)0x0010)
- #define IS25LP064A_NVCR_DTRP ((uint16_t)0x0020)
- #define IS25LP064A_NVCR_ODS ((uint16_t)0x01C0)
- #define IS25LP064A_NVCR_XIP ((uint16_t)0x0E00)
- #define IS25LP064A_NVCR_NB_DUMMY ((uint16_t)0xF000)
- #define IS25LP064A_VCR_WRAP ((uint8_t)0x03)
- #define IS25LP064A_VCR_XIP ((uint8_t)0x08)
- #define IS25LP064A_VCR_NB_DUMMY ((uint8_t)0xF0)
- #define IS25LP064A_EAR_HIGHEST_SE ((uint8_t)0x03)
- #define IS25LP064A_EAR_THIRD_SEG ((uint8_t)0x02)
- #define IS25LP064A_EAR_SECOND_SEG ((uint8_t)0x01)
- #define IS25LP064A_EAR_LOWEST_SEG ((uint8_t)0x00)
- #define IS25LP064A_EVCR_ODS ((uint8_t)0x07)
- #define IS25LP064A_EVCR_RH ((uint8_t)0x10)
- #define IS25LP064A_EVCR_DTRP ((uint8_t)0x20)
- #define IS25LP064A_EVCR_DUAL ((uint8_t)0x40)
- #define IS25LP064A_EVCR_QUAD ((uint8_t)0x80)
- #define IS25LP064A_FSR_NBADDR ((uint8_t)0x01)
- #define IS25LP064A_FSR_PRERR ((uint8_t)0x02)
- #define IS25LP064A_FSR_PGSUS ((uint8_t)0x04)
- #define IS25LP064A_FSR_PGERR ((uint8_t)0x10)
- #define IS25LP064A_FSR_ERERR ((uint8_t)0x20)
- #define IS25LP064A_FSR_ERSUS ((uint8_t)0x40)
- #define IS25LP064A_FSR_READY ((uint8_t)0x80)

7.12.1 Detailed Description

IS25LP08D Commands.

7.12.2 Macro Definition Documentation

7.12.2.1 BLOCK_ERASE_32K_CMD

```
#define BLOCK_ERASE_32K_CMD 0x52
```

&

7.12.2.2 CLEAR_FLAG_STATUS_REG_CMD

```
#define CLEAR_FLAG_STATUS_REG_CMD 0x50
```

&

7.12.2.3 DIE_ERASE_CMD

```
#define DIE_ERASE_CMD 0xC4
```

&

7.12.2.4 DUAL_IN_FAST_PROG_CMD

```
#define DUAL_IN_FAST_PROG_CMD 0xA2
```

&

7.12.2.5 DUAL_INOUT_FAST_READ_4_BYTE_ADDR_CMD

```
#define DUAL_INOUT_FAST_READ_4_BYTE_ADDR_CMD 0xBC
```

&

7.12.2.6 DUAL_INOUT_FAST_READ_CMD

```
#define DUAL_INOUT_FAST_READ_CMD 0xBB
```

&

7.12.2.7 DUAL_INOUT_FAST_READ_DTR_CMD

```
#define DUAL_INOUT_FAST_READ_DTR_CMD 0xBD
```

&

7.12.2.8 DUAL_OUT_FAST_READ_4_BYTE_ADDR_CMD

```
#define DUAL_OUT_FAST_READ_4_BYTE_ADDR_CMD 0x3C
```

&

7.12.2.9 DUAL_OUT_FAST_READ_CMD

```
#define DUAL_OUT_FAST_READ_CMD 0x3B
```

&

7.12.2.10 DUAL_OUT_FAST_READ_DTR_CMD

```
#define DUAL_OUT_FAST_READ_DTR_CMD 0x3D
```

&

7.12.2.11 ENTER_4_BYTE_ADDR_MODE_CMD

```
#define ENTER_4_BYTE_ADDR_MODE_CMD 0xB7
```

4-byte Address Mode Operations

7.12.2.12 ENTER_QUAD_CMD

```
#define ENTER_QUAD_CMD 0x35
```

Quad Operations

7.12.2.13 EXIT_4_BYTE_ADDR_MODE_CMD

```
#define EXIT_4_BYTE_ADDR_MODE_CMD 0xE9
```

&

7.12.2.14 EXIT_QUAD_CMD

```
#define EXIT_QUAD_CMD 0xF5
```

&

7.12.2.15 EXT_DUAL_IN_FAST_PROG_CMD

```
#define EXT_DUAL_IN_FAST_PROG_CMD 0xD2
```

&

7.12.2.16 EXT_QUAD_IN_FAST_PROG_CMD

```
#define EXT_QUAD_IN_FAST_PROG_CMD 0x38
```

&

7.12.2.17 FAST_READ_4_BYTE_ADDR_CMD

```
#define FAST_READ_4_BYTE_ADDR_CMD 0x0C
```

&

7.12.2.18 FAST_READ_CMD

```
#define FAST_READ_CMD 0x0B
```

&

7.12.2.19 FAST_READ_DTR_CMD

```
#define FAST_READ_DTR_CMD 0x0D
```

&

7.12.2.20 IS25LP064A_DIE_ERASE_MAX_TIME

```
#define IS25LP064A_DIE_ERASE_MAX_TIME 460000
```

&

7.12.2.21 IS25LP064A_DUMMY_CYCLES_READ

```
#define IS25LP064A_DUMMY_CYCLES_READ 8
```

&

7.12.2.22 IS25LP064A_DUMMY_CYCLES_READ_DTR

```
#define IS25LP064A_DUMMY_CYCLES_READ_DTR 6
```

&

7.12.2.23 IS25LP064A_DUMMY_CYCLES_READ_QUAD

```
#define IS25LP064A_DUMMY_CYCLES_READ_QUAD 8
```

&

7.12.2.24 IS25LP064A_DUMMY_CYCLES_READ_QUAD_DTR

```
#define IS25LP064A_DUMMY_CYCLES_READ_QUAD_DTR 6
```

&

7.12.2.25 IS25LP064A_EAR_HIGHEST_SE

```
#define IS25LP064A_EAR_HIGHEST_SE ((uint8_t)0x03)
```

Select the Highest 128Mb segment

7.12.2.26 IS25LP064A_EAR_LOWEST_SEG

```
#define IS25LP064A_EAR_LOWEST_SEG ((uint8_t)0x00)
```

Select the Lowest 128Mb segment (default)

7.12.2.27 IS25LP064A_EAR_SECOND_SEG

```
#define IS25LP064A_EAR_SECOND_SEG ((uint8_t)0x01)
```

Select the Second 128Mb segment

7.12.2.28 IS25LP064A_EAR_THIRD_SEG

```
#define IS25LP064A_EAR_THIRD_SEG ((uint8_t)0x02)
```

Select the Third 128Mb segment

7.12.2.29 IS25LP064A_EVCR_DTRP

```
#define IS25LP064A_EVCR_DTRP ((uint8_t)0x20)
```

Double transfer rate protocol

7.12.2.30 IS25LP064A_EVCR_DUAL

```
#define IS25LP064A_EVCR_DUAL ((uint8_t)0x40)
```

Dual I/O protocol

7.12.2.31 IS25LP064A_EVCR_ODS

```
#define IS25LP064A_EVCR_ODS ((uint8_t)0x07)
```

Output driver strength

7.12.2.32 IS25LP064A_EVCR_QUAD

```
#define IS25LP064A_EVCR_QUAD ((uint8_t)0x80)
```

Quad I/O protocol

7.12.2.33 IS25LP064A_EVCR_RH

```
#define IS25LP064A_EVCR_RH ((uint8_t)0x10)
```

Reset/hold

7.12.2.34 IS25LP064A_FLASH_SIZE

```
#define IS25LP064A_FLASH_SIZE 0x800000
```

2 $*$ 8 MBits => 1 $*$ 1MBytes => 1MBytes

7.12.2.35 IS25LP064A_FSR_ERERR

```
#define IS25LP064A_FSR_ERERR ((uint8_t)0x20)
```

Erase error

7.12.2.36 IS25LP064A_FSR_ERSUS

```
#define IS25LP064A_FSR_ERSUS ((uint8_t)0x40)
```

Erase operation suspended

7.12.2.37 IS25LP064A_FSR_NBADDR

```
#define IS25LP064A_FSR_NBADDR ((uint8_t)0x01)
```

3-bytes or 4-bytes addressing

7.12.2.38 IS25LP064A_FSR_PGERR

```
#define IS25LP064A_FSR_PGERR ((uint8_t)0x10)
```

Program error

7.12.2.39 IS25LP064A_FSR_PGSUS

```
#define IS25LP064A_FSR_PGSUS ((uint8_t)0x04)
```

Program operation suspended

7.12.2.40 IS25LP064A_FSR_PRERR

```
#define IS25LP064A_FSR_PRERR ((uint8_t)0x02)
```

Protection error

7.12.2.41 IS25LP064A_FSR_READY

```
#define IS25LP064A_FSR_READY ((uint8_t)0x80)
```

Ready or command in progress

7.12.2.42 IS25LP064A_H

```
#define IS25LP064A_H
```

&

7.12.2.43 IS25LP064A_NVCR_DTRP

```
#define IS25LP064A_NVCR_DTRP ((uint16_t)0x0020)
```

Double transfer rate protocol

7.12.2.44 IS25LP064A_NVCR_DUAL

```
#define IS25LP064A_NVCR_DUAL ((uint16_t)0x0004)
```

Dual I/O protocol

7.12.2.45 IS25LP064A_NVCR_NB_DUMMY

```
#define IS25LP064A_NVCR_NB_DUMMY ((uint16_t)0xF000)
```

Number of dummy clock cycles

7.12.2.46 IS25LP064A_NVCR_NBADDR

```
#define IS25LP064A_NVCR_NBADDR ((uint16_t)0x0001)
```

3-bytes or 4-bytes addressing

7.12.2.47 IS25LP064A_NVCR_ODS

```
#define IS25LP064A_NVCR_ODS ((uint16_t)0x01C0)
```

Output driver strength

7.12.2.48 IS25LP064A_NVCR_QUAB

```
#define IS25LP064A_NVCR_QUAB ((uint16_t)0x0008)
```

Quad I/O protocol

7.12.2.49 IS25LP064A_NVCR_RH

```
#define IS25LP064A_NVCR_RH ((uint16_t)0x0010)
```

Reset/hold

7.12.2.50 IS25LP064A_NVCR_SEGMENT

```
#define IS25LP064A_NVCR_SEGMENT ((uint16_t)0x0002)
```

Upper or lower 128Mb segment selected by default

7.12.2.51 IS25LP064A_NVCR_XIP

```
#define IS25LP064A_NVCR_XIP ((uint16_t)0x0E00)
```

XIP mode at power-on reset

7.12.2.52 IS25LP064A_PAGE_SIZE

```
#define IS25LP064A_PAGE_SIZE 0x100
```

2 ∗ 262144 pages of 256 bytes

7.12.2.53 IS25LP064A_SECTOR_ERASE_MAX_TIME

```
#define IS25LP064A_SECTOR_ERASE_MAX_TIME 1000
```

&

7.12.2.54 IS25LP064A_SECTOR_SIZE

```
#define IS25LP064A_SECTOR_SIZE 0x10000
```

2 ∗ 1024 sectors of 64KBytes

7.12.2.55 IS25LP064A_SR_QE

```
#define IS25LP064A_SR_QE ((uint8_t)0x40)
```

&

7.12.2.56 IS25LP064A_SR_SRWREN

```
#define IS25LP064A_SR_SRWREN ((uint8_t)0x80)
```

Status register write enable/disable

7.12.2.57 IS25LP064A_SR_WIP

```
#define IS25LP064A_SR_WIP ((uint8_t)0x01)
```

IS25LP08D Registers.

Write in progress

7.12.2.58 IS25LP064A_SR_WREN

```
#define IS25LP064A_SR_WREN ((uint8_t)0x02)
```

Write enable latch

7.12.2.59 IS25LP064A_SUBSECTOR_ERASE_MAX_TIME

```
#define IS25LP064A_SUBSECTOR_ERASE_MAX_TIME 400
```

&

7.12.2.60 IS25LP064A_SUBSECTOR_SIZE

```
#define IS25LP064A_SUBSECTOR_SIZE 0x1000
```

2 ∗ 16384 subsectors of 4kBytes

7.12.2.61 IS25LP064A_VCR_NB_DUMMY

```
#define IS25LP064A_VCR_NB_DUMMY ((uint8_t)0xF0)
```

Number of dummy clock cycles

7.12.2.62 IS25LP064A_VCR_WRAP

```
#define IS25LP064A_VCR_WRAP ((uint8_t)0x03)
```

Wrap

7.12.2.63 IS25LP064A_VCR_XIP

```
#define IS25LP064A_VCR_XIP ((uint8_t)0x08)
```

XIP

7.12.2.64 MULTIPLE_IO_READ_ID_CMD

```
#define MULTIPLE_IO_READ_ID_CMD 0xAF
```

&

7.12.2.65 PAGE_PROG_4_BYTE_ADDR_CMD

```
#define PAGE_PROG_4_BYTE_ADDR_CMD 0x12
```

&

7.12.2.66 PAGE_PROG_CMD

```
#define PAGE_PROG_CMD 0x02
```

Program Operations

7.12.2.67 PROG_ERASE_RESUME_CMD

```
#define PROG_ERASE_RESUME_CMD 0x7A
```

&

7.12.2.68 PROG_ERASE_SUSPEND_CMD

```
#define PROG_ERASE_SUSPEND_CMD 0x75
```

&

7.12.2.69 PROG_OTP_ARRAY_CMD

```
#define PROG_OTP_ARRAY_CMD 0x42
```

&

7.12.2.70 QUAD_IN_FAST_PROG_4_BYTE_ADDR_CMD

```
#define QUAD_IN_FAST_PROG_4_BYTE_ADDR_CMD 0x34
```

&

7.12.2.71 QUAD_IN_FAST_PROG_CMD

```
#define QUAD_IN_FAST_PROG_CMD 0x32
```

&

7.12.2.72 QUAD_INOUT_FAST_READ_4_BYTE_ADDR_CMD

```
#define QUAD_INOUT_FAST_READ_4_BYTE_ADDR_CMD 0xEC
```

&

7.12.2.73 QUAD_INOUT_FAST_READ_CMD

```
#define QUAD_INOUT_FAST_READ_CMD 0xEB
```

&

7.12.2.74 QUAD_INOUT_FAST_READ_DTR_CMD

```
#define QUAD_INOUT_FAST_READ_DTR_CMD 0xED
```

&

7.12.2.75 QUAD_OUT_FAST_READ_4_BYTE_ADDR_CMD

```
#define QUAD_OUT_FAST_READ_4_BYTE_ADDR_CMD 0x6C
```

&

7.12.2.76 QUAD_OUT_FAST_READ_CMD

```
#define QUAD_OUT_FAST_READ_CMD 0x6B
```

&

7.12.2.77 QUAD_OUT_FAST_READ_DTR_CMD

```
#define QUAD_OUT_FAST_READ_DTR_CMD 0x0D
```

&

7.12.2.78 READ_4_BYTE_ADDR_CMD

```
#define READ_4_BYTE_ADDR_CMD 0x13
```

&

7.12.2.79 READ_CMD

```
#define READ_CMD 0x03
```

Read Operations

7.12.2.80 READ_ENHANCED_VOL_CFG_REG_CMD

```
#define READ_ENHANCED_VOL_CFG_REG_CMD 0x81
```

&

7.12.2.81 READ_EXT_ADDR_REG_CMD

```
#define READ_EXT_ADDR_REG_CMD 0xC8
```

&

7.12.2.82 READ_FLAG_STATUS_REG_CMD

```
#define READ_FLAG_STATUS_REG_CMD 0x70
```

&

7.12.2.83 READ_ID_CMD

```
#define READ_ID_CMD 0x9E
```

Identification Operations

7.12.2.84 READ_ID_CMD2

```
#define READ_ID_CMD2 0x9F
```

&

7.12.2.85 READ_LOCK_REG_CMD

```
#define READ_LOCK_REG_CMD 0xE8
```

&

7.12.2.86 READ_NONVOL_CFG_REG_CMD

```
#define READ_NONVOL_CFG_REG_CMD 0xB5
```

&

7.12.2.87 READ_OTP_ARRAY_CMD

```
#define READ_OTP_ARRAY_CMD 0x4B
```

One-Time Programmable Operations

7.12.2.88 READ_READ_PARAM_REG_CMD

```
#define READ_READ_PARAM_REG_CMD 0x61
```

&

7.12.2.89 READ_SERIAL_FLASH_DISCO_PARAM_CMD

```
#define READ_SERIAL_FLASH_DISCO_PARAM_CMD 0x5A
```

&

7.12.2.90 READ_STATUS_REG_CMD

```
#define READ_STATUS_REG_CMD 0x05
```

Register Operations

7.12.2.91 RESET_ENABLE_CMD

```
#define RESET_ENABLE_CMD 0x66
```

Reset Operations

7.12.2.92 RESET_MEMORY_CMD

```
#define RESET_MEMORY_CMD 0x99
```

&

7.12.2.93 SECTOR_ERASE_4_BYTE_ADDR_CMD

```
#define SECTOR_ERASE_4_BYTE_ADDR_CMD 0xDC
```

&

7.12.2.94 SECTOR_ERASE_CMD

```
#define SECTOR_ERASE_CMD 0xD8
```

&

7.12.2.95 SUBSECTOR_ERASE_4_BYTE_ADDR_CMD

```
#define SUBSECTOR_ERASE_4_BYTE_ADDR_CMD 0x21
```

&

7.12.2.96 SUBSECTOR_ERASE_CMD

```
#define SUBSECTOR_ERASE_CMD 0xd7
```

Erase Operations

7.12.2.97 SUBSECTOR_ERASE_QPI_CMD

```
#define SUBSECTOR_ERASE_QPI_CMD 0x20
```

&

7.12.2.98 WRITE_DISABLE_CMD

```
#define WRITE_DISABLE_CMD 0x04
```

&

7.12.2.99 WRITE_ENABLE_CMD

```
#define WRITE_ENABLE_CMD 0x06
```

Write Operations

7.12.2.100 WRITE_ENHANCED_VOL_CFG_REG_CMD

```
#define WRITE_ENHANCED_VOL_CFG_REG_CMD 0x85
```

&

7.12.2.101 WRITE_EXT_ADDR_REG_CMD

```
#define WRITE_EXT_ADDR_REG_CMD 0xC5
```

&

7.12.2.102 WRITE_LOCK_REG_CMD

```
#define WRITE_LOCK_REG_CMD 0xE5
```

&

7.12.2.103 WRITE_NONVOL_CFG_REG_CMD

```
#define WRITE_NONVOL_CFG_REG_CMD 0xB1
```

&

7.12.2.104 WRITE_READ_PARAM_REG_CMD

```
#define WRITE_READ_PARAM_REG_CMD 0xC0
```

&

7.12.2.105 WRITE_STATUS_REG_CMD

```
#define WRITE_STATUS_REG_CMD 0x01
```

&

7.13 src/dev_flash_IS25LP080D.h File Reference

IS25LP08D Commands.

Macros

- #define IS25LP080D_FLASH_SIZE 0x100000
- #define IS25LP080D_SECTOR_SIZE 0x10000
- #define IS25LP080D_SUBSECTOR_SIZE 0x1000
- #define IS25LP080D_PAGE_SIZE 0x100
- #define IS25LP080D_DUMMY_CYCLES_READ_QUAD 8
- #define IS25LP080D_DUMMY_CYCLES_READ 8
- #define IS25LP080D_DUMMY_CYCLES_READ_DTR 6
- #define IS25LP080D_DUMMY_CYCLES_READ_QUAD_DTR 6
- #define IS25LP080D_DIE_ERASE_MAX_TIME 460000
- #define IS25LP080D_SECTOR_ERASE_MAX_TIME 1000
- #define IS25LP080D_SUBSECTOR_ERASE_MAX_TIME 400
- #define RESET_ENABLE_CMD 0x66
- #define RESET_MEMORY_CMD 0x99
- #define READ_ID_CMD 0x9E
- #define READ_ID_CMD2 0x9F
- #define MULTIPLE_IO_READ_ID_CMD 0xAF
- #define READ_SERIAL_FLASH_DISCO_PARAM_CMD 0x5A

- #define READ_CMD 0x03
- #define READ_4_BYTE_ADDR_CMD 0x13
- #define FAST_READ_CMD 0x0B
- #define FAST_READ_DTR_CMD 0x0D
- #define FAST_READ_4_BYTE_ADDR_CMD 0x0C
- #define DUAL_OUT_FAST_READ_CMD 0x3B
- #define DUAL_OUT_FAST_READ_DTR_CMD 0x3D
- #define DUAL_OUT_FAST_READ_4_BYTE_ADDR_CMD 0x3C
- #define DUAL_INOUT_FAST_READ_CMD 0xBB
- #define DUAL_INOUT_FAST_READ_DTR_CMD 0xBD
- #define DUAL_INOUT_FAST_READ_4_BYTE_ADDR_CMD 0xBC
- #define QUAD_OUT_FAST_READ_CMD 0x6B
- #define QUAD_OUT_FAST_READ_DTR_CMD 0x0D
- #define QUAD_OUT_FAST_READ_4_BYTE_ADDR_CMD 0x6C
- #define QUAD_INOUT_FAST_READ_CMD 0xEB
- #define QUAD_INOUT_FAST_READ_DTR_CMD 0xED
- #define QUAD_INOUT_FAST_READ_4_BYTE_ADDR_CMD 0xEC
- #define WRITE_ENABLE_CMD 0x06
- #define WRITE_DISABLE_CMD 0x04
- #define READ_STATUS_REG_CMD 0x05
- #define WRITE_STATUS_REG_CMD 0x01
- #define READ_LOCK_REG_CMD 0xE8
- #define WRITE_LOCK_REG_CMD 0xE5
- #define READ_FLAG_STATUS_REG_CMD 0x70
- #define CLEAR_FLAG_STATUS_REG_CMD 0x50
- #define READ_NONVOL_CFG_REG_CMD 0xB5
- #define WRITE_NONVOL_CFG_REG_CMD 0xB1
- #define READ_READ_PARAM_REG_CMD 0x61
- #define WRITE_READ_PARAM_REG_CMD 0xC0
- #define READ_ENHANCED_VOL_CFG_REG_CMD 0x81
- #define WRITE_ENHANCED_VOL_CFG_REG_CMD 0x85
- #define READ_EXT_ADDR_REG_CMD 0xC8
- #define WRITE_EXT_ADDR_REG_CMD 0xC5
- #define PAGE_PROG_CMD 0x02
- #define PAGE_PROG_4_BYTE_ADDR_CMD 0x12
- #define DUAL_IN_FAST_PROG_CMD 0xA2
- #define EXT_DUAL_IN_FAST_PROG_CMD 0xD2
- #define QUAD_IN_FAST_PROG_CMD 0x32
- #define EXT_QUAD_IN_FAST_PROG_CMD 0x38
- #define QUAD_IN_FAST_PROG_4_BYTE_ADDR_CMD 0x34
- #define SUBSECTOR_ERASE_CMD 0xd7
- #define SUBSECTOR_ERASE_QPI_CMD 0x20
- #define SUBSECTOR_ERASE_4_BYTE_ADDR_CMD 0x21
- #define SECTOR_ERASE_CMD 0xD8
- #define SECTOR_ERASE_4_BYTE_ADDR_CMD 0xDC
- #define BLOCK_ERASE_32K_CMD 0x52
- #define DIE_ERASE_CMD 0xC4
- #define PROG_ERASE_RESUME_CMD 0x7A
- #define PROG_ERASE_SUSPEND_CMD 0x75
- #define READ_OTP_ARRAY_CMD 0x4B
- #define PROG_OTP_ARRAY_CMD 0x42
- #define ENTER_4_BYTE_ADDR_MODE_CMD 0xB7
- #define EXIT_4_BYTE_ADDR_MODE_CMD 0xE9
- #define ENTER_QUAD_CMD 0x35
- #define EXIT_QUAD_CMD 0xF5

- #define IS25LP080D_SR_WIP ((uint8_t)0x01)

  IS25LP08D Registers.
- #define IS25LP080D_SR_WREN ((uint8_t)0x02)
- #define IS25LP080D_SR_SRWREN ((uint8_t)0x80)
- #define IS25LP080D_SR_QE ((uint8_t)0x40)
- #define IS25LP080D_NVCR_NBADDR ((uint16_t)0x0001)
- #define IS25LP080D_NVCR_SEGMENT ((uint16_t)0x0002)
- #define IS25LP080D_NVCR_DUAL ((uint16_t)0x0004)
- #define IS25LP080D_NVCR_QUAB ((uint16_t)0x0008)
- #define IS25LP080D_NVCR_RH ((uint16_t)0x0010)
- #define IS25LP080D_NVCR_DTRP ((uint16_t)0x0020)
- #define IS25LP080D_NVCR_ODS ((uint16_t)0x01C0)
- #define IS25LP080D_NVCR_XIP ((uint16_t)0x0E00)
- #define IS25LP080D_NVCR_NB_DUMMY ((uint16_t)0xF000)
- #define IS25LP080D_VCR_WRAP ((uint8_t)0x03)
- #define IS25LP080D_VCR_XIP ((uint8_t)0x08)
- #define IS25LP080D_VCR_NB_DUMMY ((uint8_t)0xF0)
- #define IS25LP080D_EAR_HIGHEST_SE ((uint8_t)0x03)
- #define IS25LP080D_EAR_THIRD_SEG ((uint8_t)0x02)
- #define IS25LP080D_EAR_SECOND_SEG ((uint8_t)0x01)
- #define IS25LP080D_EAR_LOWEST_SEG ((uint8_t)0x00)
- #define IS25LP080D_EVCR_ODS ((uint8_t)0x07)
- #define IS25LP080D_EVCR_RH ((uint8_t)0x10)
- #define IS25LP080D_EVCR_DTRP ((uint8_t)0x20)
- #define IS25LP080D_EVCR_DUAL ((uint8_t)0x40)
- #define IS25LP080D_EVCR_QUAD ((uint8_t)0x80)
- #define IS25LP080D_FSR_NBADDR ((uint8_t)0x01)
- #define IS25LP080D_FSR_PRERR ((uint8_t)0x02)
- #define IS25LP080D_FSR_PGSUS ((uint8_t)0x04)
- #define IS25LP080D_FSR_PGERR ((uint8_t)0x10)
- #define IS25LP080D_FSR_ERERR ((uint8_t)0x20)
- #define IS25LP080D_FSR_ERSUS ((uint8_t)0x40)
- #define IS25LP080D_FSR_READY ((uint8_t)0x80)

7.13.1 Detailed Description

IS25LP08D Commands.

7.13.2 Macro Definition Documentation

7.13.2.1 BLOCK_ERASE_32K_CMD

```
#define BLOCK_ERASE_32K_CMD 0x52
```

&

7.13.2.2 CLEAR_FLAG_STATUS_REG_CMD

```
#define CLEAR_FLAG_STATUS_REG_CMD 0x50
```

&

7.13.2.3 DIE_ERASE_CMD

```
#define DIE_ERASE_CMD 0xC4
```

&

7.13.2.4 DUAL_IN_FAST_PROG_CMD

```
#define DUAL_IN_FAST_PROG_CMD 0xA2
```

&

7.13.2.5 DUAL_INOUT_FAST_READ_4_BYTE_ADDR_CMD

```
#define DUAL_INOUT_FAST_READ_4_BYTE_ADDR_CMD 0xBC
```

&

7.13.2.6 DUAL_INOUT_FAST_READ_CMD

```
#define DUAL_INOUT_FAST_READ_CMD 0xBB
```

&

7.13.2.7 DUAL_INOUT_FAST_READ_DTR_CMD

```
#define DUAL_INOUT_FAST_READ_DTR_CMD 0xBD
```

&

7.13.2.8 DUAL_OUT_FAST_READ_4_BYTE_ADDR_CMD

```
#define DUAL_OUT_FAST_READ_4_BYTE_ADDR_CMD 0x3C
```

&

7.13.2.9 DUAL_OUT_FAST_READ_CMD

```
#define DUAL_OUT_FAST_READ_CMD 0x3B
```

&

7.13.2.10 DUAL_OUT_FAST_READ_DTR_CMD

```
#define DUAL_OUT_FAST_READ_DTR_CMD 0x3D
```

&

7.13.2.11 ENTER_4_BYTE_ADDR_MODE_CMD

```
#define ENTER_4_BYTE_ADDR_MODE_CMD 0xB7
```

4-byte Address Mode Operations

7.13.2.12 ENTER_QUAD_CMD

```
#define ENTER_QUAD_CMD 0x35
```

Quad Operations

7.13.2.13 EXIT_4_BYTE_ADDR_MODE_CMD

```
#define EXIT_4_BYTE_ADDR_MODE_CMD 0xE9
```

&

7.13.2.14 EXIT_QUAD_CMD

```
#define EXIT_QUAD_CMD 0xF5
```

&

7.13.2.15 EXT_DUAL_IN_FAST_PROG_CMD

```
#define EXT_DUAL_IN_FAST_PROG_CMD 0xD2
```

&

7.13.2.16 EXT_QUAD_IN_FAST_PROG_CMD

```
#define EXT_QUAD_IN_FAST_PROG_CMD 0x38
```

&

7.13.2.17 FAST_READ_4_BYTE_ADDR_CMD

```
#define FAST_READ_4_BYTE_ADDR_CMD 0x0C
```

&

7.13.2.18 FAST_READ_CMD

```
#define FAST_READ_CMD 0x0B
```

&

7.13.2.19 FAST_READ_DTR_CMD

```
#define FAST_READ_DTR_CMD 0x0D
```

&

7.13.2.20 IS25LP080D_DIE_ERASE_MAX_TIME

```
#define IS25LP080D_DIE_ERASE_MAX_TIME 460000
```

&

7.13.2.21 IS25LP080D_DUMMY_CYCLES_READ

```
#define IS25LP080D_DUMMY_CYCLES_READ 8
```

&

7.13.2.22 IS25LP080D_DUMMY_CYCLES_READ_DTR

```
#define IS25LP080D_DUMMY_CYCLES_READ_DTR 6
```

&

7.13.2.23 IS25LP080D_DUMMY_CYCLES_READ_QUAD

```
#define IS25LP080D_DUMMY_CYCLES_READ_QUAD 8
```

&

7.13.2.24 IS25LP080D_DUMMY_CYCLES_READ_QUAD_DTR

```
#define IS25LP080D_DUMMY_CYCLES_READ_QUAD_DTR 6
```

&

7.13.2.25 IS25LP080D_EAR_HIGHEST_SE

```
#define IS25LP080D_EAR_HIGHEST_SE ((uint8_t)0x03)
```

Select the Highest 128Mb segment

7.13.2.26 IS25LP080D_EAR_LOWEST_SEG

```
#define IS25LP080D_EAR_LOWEST_SEG ((uint8_t)0x00)
```

Select the Lowest 128Mb segment (default)

7.13.2.27 IS25LP080D_EAR_SECOND_SEG

```
#define IS25LP080D_EAR_SECOND_SEG ((uint8_t)0x01)
```

Select the Second 128Mb segment

7.13.2.28 IS25LP080D_EAR_THIRD_SEG

```
#define IS25LP080D_EAR_THIRD_SEG ((uint8_t)0x02)
```

Select the Third 128Mb segment

7.13.2.29 IS25LP080D_EVCR_DTRP

```
#define IS25LP080D_EVCR_DTRP ((uint8_t)0x20)
```

Double transfer rate protocol

7.13.2.30 IS25LP080D_EVCR_DUAL

```
#define IS25LP080D_EVCR_DUAL ((uint8_t)0x40)
```

Dual I/O protocol

7.13.2.31 IS25LP080D_EVCR_ODS

```
#define IS25LP080D_EVCR_ODS ((uint8_t)0x07)
```

Output driver strength

7.13.2.32 IS25LP080D_EVCR_QUAD

```
#define IS25LP080D_EVCR_QUAD ((uint8_t)0x80)
```

Quad I/O protocol

7.13.2.33 IS25LP080D_EVCR_RH

```
#define IS25LP080D_EVCR_RH ((uint8_t)0x10)
```

Reset/hold

7.13.2.34 IS25LP080D_FLASH_SIZE

```
#define IS25LP080D_FLASH_SIZE 0x100000
```

2 * 8 MBits => 1 * 1MBytes => 1MBytes

7.13.2.35 IS25LP080D_FSR_ERERR

```
#define IS25LP080D_FSR_ERERR ((uint8_t)0x20)
```

Erase error

7.13.2.36 IS25LP080D_FSR_ERSUS

```
#define IS25LP080D_FSR_ERSUS ((uint8_t)0x40)
```

Erase operation suspended

7.13.2.37 IS25LP080D_FSR_NBADDR

```
#define IS25LP080D_FSR_NBADDR ((uint8_t)0x01)
```

3-bytes or 4-bytes addressing

7.13.2.38 IS25LP080D_FSR_PGERR

```
#define IS25LP080D_FSR_PGERR ((uint8_t)0x10)
```

Program error

7.13.2.39 IS25LP080D_FSR_PGSUS

```
#define IS25LP080D_FSR_PGSUS ((uint8_t)0x04)
```

Program operation suspended

7.13.2.40 IS25LP080D_FSR_PRERR

```
#define IS25LP080D_FSR_PRERR ((uint8_t)0x02)
```

Protection error

7.13.2.41 IS25LP080D_FSR_READY

```
#define IS25LP080D_FSR_READY ((uint8_t)0x80)
```

Ready or command in progress

7.13.2.42 IS25LP080D_NVCR_DTRP

```
#define IS25LP080D_NVCR_DTRP ((uint16_t)0x0020)
```

Double transfer rate protocol

7.13.2.43 IS25LP080D_NVCR_DUAL

```
#define IS25LP080D_NVCR_DUAL ((uint16_t)0x0004)
```

Dual I/O protocol

7.13.2.44 IS25LP080D_NVCR_NB_DUMMY

```
#define IS25LP080D_NVCR_NB_DUMMY ((uint16_t)0xF000)
```

Number of dummy clock cycles

7.13.2.45 IS25LP080D_NVCR_NBADDR

```
#define IS25LP080D_NVCR_NBADDR ((uint16_t)0x0001)
```

3-bytes or 4-bytes addressing

7.13.2.46 IS25LP080D_NVCR_ODS

```
#define IS25LP080D_NVCR_ODS ((uint16_t)0x01C0)
```

Output driver strength

7.13.2.47 IS25LP080D_NVCR_QUAB

```
#define IS25LP080D_NVCR_QUAB ((uint16_t)0x0008)
```

Quad I/O protocol

7.13.2.48 IS25LP080D_NVCR_RH

```
#define IS25LP080D_NVCR_RH ((uint16_t)0x0010)
```

Reset/hold

7.13.2.49 IS25LP080D_NVCR_SEGMENT

```
#define IS25LP080D_NVCR_SEGMENT ((uint16_t)0x0002)
```

Upper or lower 128Mb segment selected by default

7.13.2.50 IS25LP080D_NVCR_XIP

```
#define IS25LP080D_NVCR_XIP ((uint16_t)0x0E00)
```

XIP mode at power-on reset

7.13.2.51 IS25LP080D_PAGE_SIZE

```
#define IS25LP080D_PAGE_SIZE 0x100
```

2 ∗ 262144 pages of 256 bytes

7.13.2.52 IS25LP080D_SECTOR_ERASE_MAX_TIME

```
#define IS25LP080D_SECTOR_ERASE_MAX_TIME 1000
```

&

7.13.2.53 IS25LP080D_SECTOR_SIZE

```
#define IS25LP080D_SECTOR_SIZE 0x10000
```

2 ∗ 1024 sectors of 64KBytes

7.13.2.54 IS25LP080D_SR_QE

```
#define IS25LP080D_SR_QE ((uint8_t)0x40)
```

&

7.13.2.55 IS25LP080D_SR_SRWREN

```
#define IS25LP080D_SR_SRWREN ((uint8_t)0x80)
```

Status register write enable/disable

7.13.2.56 IS25LP080D_SR_WIP

```
#define IS25LP080D_SR_WIP ((uint8_t)0x01)
```

IS25LP08D Registers.

Status Register Write in progress

7.13.2.57 IS25LP080D_SR_WREN

```
#define IS25LP080D_SR_WREN ((uint8_t)0x02)
```

Write enable latch

7.13.2.58 IS25LP080D_SUBSECTOR_ERASE_MAX_TIME

```
#define IS25LP080D_SUBSECTOR_ERASE_MAX_TIME 400
```

&

7.13.2.59 IS25LP080D_SUBSECTOR_SIZE

```
#define IS25LP080D_SUBSECTOR_SIZE 0x1000
```

2 ∗ 16384 subsectors of 4kBytes

7.13.2.60 IS25LP080D_VCR_NB_DUMMY

```
#define IS25LP080D_VCR_NB_DUMMY ((uint8_t)0xF0)
```

Number of dummy clock cycles

7.13.2.61 IS25LP080D_VCR_WRAP

```
#define IS25LP080D_VCR_WRAP ((uint8_t)0x03)
```

Wrap

7.13.2.62 IS25LP080D_VCR_XIP

```
#define IS25LP080D_VCR_XIP ((uint8_t)0x08)
```

XIP

7.13.2.63 MULTIPLE_IO_READ_ID_CMD

```
#define MULTIPLE_IO_READ_ID_CMD 0xAF
```

&

7.13.2.64 PAGE_PROG_4_BYTE_ADDR_CMD

```
#define PAGE_PROG_4_BYTE_ADDR_CMD 0x12
```

&

7.13.2.65 PAGE_PROG_CMD

```
#define PAGE_PROG_CMD 0x02
```

Program Operations

7.13.2.66 PROG_ERASE_RESUME_CMD

```
#define PROG_ERASE_RESUME_CMD 0x7A
```

&

7.13.2.67 PROG_ERASE_SUSPEND_CMD

```
#define PROG_ERASE_SUSPEND_CMD 0x75
```

&

7.13.2.68 PROG_OTP_ARRAY_CMD

```
#define PROG_OTP_ARRAY_CMD 0x42
```

&

7.13.2.69 QUAD_IN_FAST_PROG_4_BYTE_ADDR_CMD

```
#define QUAD_IN_FAST_PROG_4_BYTE_ADDR_CMD 0x34
```

&

7.13.2.70 QUAD_IN_FAST_PROG_CMD

```
#define QUAD_IN_FAST_PROG_CMD 0x32
```

&

7.13.2.71 QUAD_INOUT_FAST_READ_4_BYTE_ADDR_CMD

```
#define QUAD_INOUT_FAST_READ_4_BYTE_ADDR_CMD 0xEC
```

&

7.13.2.72 QUAD_INOUT_FAST_READ_CMD

```
#define QUAD_INOUT_FAST_READ_CMD 0xEB
```

&

7.13.2.73 QUAD_INOUT_FAST_READ_DTR_CMD

```
#define QUAD_INOUT_FAST_READ_DTR_CMD 0xED
```

&

7.13.2.74 QUAD_OUT_FAST_READ_4_BYTE_ADDR_CMD

```
#define QUAD_OUT_FAST_READ_4_BYTE_ADDR_CMD 0x6C
```

&

7.13.2.75 QUAD_OUT_FAST_READ_CMD

```
#define QUAD_OUT_FAST_READ_CMD 0x6B
```

&

7.13.2.76 QUAD_OUT_FAST_READ_DTR_CMD

```
#define QUAD_OUT_FAST_READ_DTR_CMD 0x0D
```

&

7.13.2.77 READ_4_BYTE_ADDR_CMD

```
#define READ_4_BYTE_ADDR_CMD 0x13
```

&

7.13.2.78 READ_CMD

```
#define READ_CMD 0x03
```

Read Operations

7.13.2.79 READ_ENHANCED_VOL_CFG_REG_CMD

```
#define READ_ENHANCED_VOL_CFG_REG_CMD 0x81
```

&

7.13.2.80 READ_EXT_ADDR_REG_CMD

```
#define READ_EXT_ADDR_REG_CMD 0xC8
```

&

7.13.2.81 READ_FLAG_STATUS_REG_CMD

```
#define READ_FLAG_STATUS_REG_CMD 0x70
```

&

7.13.2.82 READ_ID_CMD

```
#define READ_ID_CMD 0x9E
```

Identification Operations

7.13.2.83 READ_ID_CMD2

```
#define READ_ID_CMD2 0x9F
```

&

7.13.2.84 READ_LOCK_REG_CMD

```
#define READ_LOCK_REG_CMD 0xE8
```

&

7.13.2.85 READ_NONVOL_CFG_REG_CMD

```
#define READ_NONVOL_CFG_REG_CMD 0xB5
```

&

7.13.2.86 READ_OTP_ARRAY_CMD

```
#define READ_OTP_ARRAY_CMD 0x4B
```

One-Time Programmable Operations

7.13.2.87 READ_READ_PARAM_REG_CMD

```
#define READ_READ_PARAM_REG_CMD 0x61
```

&

7.13.2.88 READ_SERIAL_FLASH_DISCO_PARAM_CMD

```
#define READ_SERIAL_FLASH_DISCO_PARAM_CMD 0x5A
```

&

7.13.2.89 READ_STATUS_REG_CMD

```
#define READ_STATUS_REG_CMD 0x05
```

Register Operations

7.13.2.90 RESET_ENABLE_CMD

```
#define RESET_ENABLE_CMD 0x66
```

Reset Operations

7.13.2.91 RESET_MEMORY_CMD

```
#define RESET_MEMORY_CMD 0x99
```

&

7.13.2.92 SECTOR_ERASE_4_BYTE_ADDR_CMD

```
#define SECTOR_ERASE_4_BYTE_ADDR_CMD 0xDC
```

&

7.13.2.93 SECTOR_ERASE_CMD

```
#define SECTOR_ERASE_CMD 0xD8
```

&

7.13.2.94 SUBSECTOR_ERASE_4_BYTE_ADDR_CMD

```
#define SUBSECTOR_ERASE_4_BYTE_ADDR_CMD 0x21
```

&

7.13.2.95 SUBSECTOR_ERASE_CMD

```
#define SUBSECTOR_ERASE_CMD 0xd7
```

Erase Operations

7.13.2.96 SUBSECTOR_ERASE_QPI_CMD

```
#define SUBSECTOR_ERASE_QPI_CMD 0x20
```

&

7.13.2.97 WRITE_DISABLE_CMD

```
#define WRITE_DISABLE_CMD 0x04
```

&

7.13.2.98 WRITE_ENABLE_CMD

```
#define WRITE_ENABLE_CMD 0x06
```

Write Operations

7.13.2.99 WRITE_ENHANCED_VOL_CFG_REG_CMD

```
#define WRITE_ENHANCED_VOL_CFG_REG_CMD 0x85
```

&

7.13.2.100 WRITE_EXT_ADDR_REG_CMD

```
#define WRITE_EXT_ADDR_REG_CMD 0xC5
```

&

7.13.2.101 WRITE_LOCK_REG_CMD

```
#define WRITE_LOCK_REG_CMD 0xE5
```

&

7.13.2.102 WRITE_NONVOL_CFG_REG_CMD

```
#define WRITE_NONVOL_CFG_REG_CMD 0xB1
```

&

7.13.2.103 WRITE_READ_PARAM_REG_CMD

```
#define WRITE_READ_PARAM_REG_CMD 0xC0
```

&

7.13.2.104 WRITE_STATUS_REG_CMD

```
#define WRITE_STATUS_REG_CMD 0x01
```

&

7.14 src/dev_leddriver.h File Reference

Device driver for PCA9685 16-channel 12-bit PWM generator.

```
#include <stdint.h>
#include "per_i2c.h"
```

Classes

- struct color

Macros

- #define SA_LED_DRIVER_H
- #define DSY_LED_DRIVER_MAX_DRIVERS 8

Enumerations

- enum {
  LED_COLOR_RED, LED_COLOR_GREEN, LED_COLOR_BLUE, LED_COLOR_WHITE,
  LED_COLOR_PURPLE, LED_COLOR_CYAN, LED_COLOR_GOLD, LED_COLOR_OFF,
  LED_COLOR_LAST }

Functions

- void dsy_led_driver_init (dsy_i2c_handle *dsy_i2c, uint8_t *addr, uint8_t addr_cnt)
- void dsy_led_driver_update ()
- void dsy_led_driver_set_led (uint8_t idx, float bright)
- color * dsy_led_driver_color_by_name (uint8_t name)

7.14.1 Detailed Description

Device driver for PCA9685 16-channel 12-bit PWM generator.

7.14.2 Macro Definition Documentation

7.14.2.1 DSY_LED_DRIVER_MAX_DRIVERS

```
#define DSY_LED_DRIVER_MAX_DRIVERS 8
```

Maximum number of drivers

7.14.2.2 SA_LED_DRIVER_H

```
#define SA_LED_DRIVER_H
```

&

7.14.3 Enumeration Type Documentation

7.14.3.1 anonymous enum

```
anonymous enum
```

Different Led colors

Enumerator

LED_COLOR_RED	&
LED_COLOR_GREEN	&
LED_COLOR_BLUE	&
LED_COLOR_WHITE	&
LED_COLOR_PURPLE	&
LED_COLOR_CYAN	&
LED_COLOR_GOLD	&
LED_COLOR_OFF	&
LED_COLOR_LAST	&

7.14.4 Function Documentation

7.14.4.1 dsy_led_driver_color_by_name()

```
color* dsy_led_driver_color_by_name (
            uint8_t name )
```

Passing in one of the preset colors will return a pointer to a color struct

Parameters

name	Preset color

7.14.4.2 dsy_led_driver_init()

```
void dsy_led_driver_init (
             dsy_i2c_handle * dsy_i2c,
             uint8_t * addr,
             uint8_t addr_cnt )
```

Initializes the LED Driver(s) on the specified I2C Bus

Parameters

∗dsy_i2c	should be any dsy_i2c_handle with pins and speed configured.
addr	is either a pointer to 1 device address, or an array of addresses for multiple devices
addr_cnt	is the number of addresses passed in (use '1' for a single device)

7.14.4.3 dsy_led_driver_set_led()

```
void dsy_led_driver_set_led (
             uint8_t idx,
             float bright )
```

sets the LED at the index to the specified brightness (0-1) Index is sequential so device 0 will have idx 0-15, while device 1 will have idx 16-31, etc.

Parameters

idx	Index
bright	Brightness

7.14.4.4 dsy_led_driver_update()

```
void dsy_led_driver_update ( )
```

Updates the LED Driver with the values set using the set function Currently only updates one driver at a time due to the time it takes to update all of the devices. This can likely be set up to use DMA so that the function doesn't block for so long.

7.15 src/dev_sdram.h File Reference

```
#include <stdint.h>
#include "daisy_core.h"
```

Classes

- struct dsy_sdram_handle

Macros

- #define RAM_AS4C16M16SA_H
- #define DSY_SDRAM_DATA __attribute__((section(".sdram_data")))
- #define DSY_SDRAM_BSS __attribute__((section(".sdram_bss")))

Enumerations

- enum { DSY_SDRAM_OK, DSY_SDRAM_ERR }
- enum dsy_sdram_state { DSY_SDRAM_STATE_ENABLE, DSY_SDRAM_STATE_DISABLE, DSY_SDRAM_STATE_LAST }
- enum dsy_sdram_pin { DSY_SDRAM_PIN_SDNWE, DSY_SDRAM_PIN_LAST }

Functions

- uint8_t dsy_sdram_init (dsy_sdram_handle *dsy_hsdram)

7.15.1 Macro Definition Documentation

7.15.1.1 DSY_SDRAM_BSS

```
#define DSY_SDRAM_BSS __attribute__((section(".sdram_bss")))
```

Variables placed here will not be initialized.
Usage
E.g. int DSY_SDRAM_BSS uninitialized_var;

7.15.1.2 DSY_SDRAM_DATA

```
#define DSY_SDRAM_DATA __attribute__((section(".sdram_data")))
```

Usage:
E.g. int DSY_SDRAM_DATA initialized_var = 1;

7.15.1.3 RAM_AS4C16M16SA_H

```
#define RAM_AS4C16M16SA_H
```

SDRAM for 32MB AS4C16M16SA (and 64MB equivalent). Thanks to whoever this awesome person is: http://main.lv/writeup/stm32f4_sdram_configuration.md The Init function is basically a copy and paste. He has references to timing, etc. RAM is configured at 100MHz (fastest possible on the MCU). To use these the .sdram_data/_bss sections must be configured correctly in the LINKER SCRIPT. using BSS is advised for most things, since the DATA section must also fit in flash in order to be initialized. Data section init not properly set up, as SDRAM is not initialized until after startup code.&

7.15.2 Enumeration Type Documentation

7.15.2.1 anonymous enum

```
anonymous enum
```

Enumerator

DSY_SDRAM_OK	&
DSY_SDRAM_ERR	&

7.15.2.2 dsy_sdram_pin

```
enum dsy_sdram_pin
```

This is PH5 on Daisy

Enumerator

DSY_SDRAM_PIN_SDNWE	&
DSY_SDRAM_PIN_LAST	&

7.15.2.3 dsy_sdram_state

```
enum dsy_sdram_state
```

Determines whether chip is initialized, and activated.

Enumerator

DSY_SDRAM_STATE_ENABLE	&
DSY_SDRAM_STATE_DISABLE	&
DSY_SDRAM_STATE_LAST	&

7.15.3 Function Documentation

7.15.3.1 dsy_sdram_init()

```
uint8_t dsy_sdram_init (
            dsy_sdram_handle * dsy_hsdram )
```

Initializes the SDRAM peripheral

7.16 src/dev_sr_4021.h File Reference

Device driver for the CD4021. Bit-banged serial shift input.

```
#include "per_gpio.h"
```

Classes

- struct dsy_sr_4021_handle

Macros

- #define DEV_SR_4021_H
- #define SR_4021_MAX_PARALLEL 2
- #define SR_4021_MAX_DAISYCHAIN 1

Enumerations

- enum {
  DSY_SR_4021_PIN_CS, DSY_SR_4021_PIN_CLK, DSY_SR_4021_PIN_DATA, DSY_SR_4021_PIN_D↩
  ATA2,
  DSY_SR_4021_PIN_LAST }

Functions

- void dsy_sr_4021_init (dsy_sr_4021_handle *sr)
- void dsy_sr_4021_update (dsy_sr_4021_handle *sr)
- uint8_t dsy_sr_4021_state (dsy_sr_4021_handle *sr, uint8_t idx)

7.16.1 Detailed Description

Device driver for the CD4021. Bit-banged serial shift input.

7.16.2 Macro Definition Documentation

7.16.2.1 DEV_SR_4021_H

```
#define DEV_SR_4021_H
```

&

7.16.2.2 SR_4021_MAX_DAISYCHAIN

```
#define SR_4021_MAX_DAISYCHAIN 1
```

fixed maximum for daisychained use

7.16.2.3 SR_4021_MAX_PARALLEL

```
#define SR_4021_MAX_PARALLEL 2
```

Fixed maximums for parallel/daisychained use
These could be expanded, but haven't been tested beyond this

7.16.3 Enumeration Type Documentation

7.16.3.1 anonymous enum

```
anonymous enum
```

Pins that need to be configured to use. DATA2 only needs to be set if num_parallel is >1

Enumerator

DSY_SR_4021_PIN_CS	CS Pin
DSY_SR_4021_PIN_CLK	CLK Pin
DSY_SR_4021_PIN_DATA	DATA pin
DSY_SR_4021_PIN_DATA2	DATA2 Pin, optional
DSY_SR_4021_PIN_LAST	Enum Last

7.16.4 Function Documentation

7.16.4.1 dsy_sr_4021_init()

```
void dsy_sr_4021_init (
            dsy_sr_4021_handle * sr )
```

Initialize CD4021 with settings from sr_4021_handle

Parameters

sr	handle to initialize

7.16.4.2 dsy_sr_4021_state()

```
uint8_t dsy_sr_4021_state (
            dsy_sr_4021_handle * sr,
            uint8_t idx )
```

Returns the state of a pin at a given index.

Parameters

*sr	Handle containing desired pin
idx	Pin index

7.16.4.3 dsy_sr_4021_update()

```
void dsy_sr_4021_update (
            dsy_sr_4021_handle * sr )
```

Fills internal states with CD4021 data states.

Parameters

*sr	Handle to update

7.17 src/dev_sr_595.h File Reference

```
#include "daisy_core.h"
#include "per_gpio.h"
```

Classes

- class ShiftRegister595

  Device Driver for 8-bit shift register.
  CD74HC595 - 8-bit serial to parallel output shift.

Variables

- const size_t kMaxSr595DaisyChain = 16

7.17.1 Variable Documentation

7.17.1.1 kMaxSr595DaisyChain

```
const size_t kMaxSr595DaisyChain = 16
```

Maximum Number of chained devices Connect device's QH' pin to the next chips serial input

7.18 src/fatfs.h File Reference

fatfs support.

```
#include "ff.h"
#include "ff_gen_drv.h"
#include "util_sd_diskio.h"
```

Macros

- #define __fatfs_H

Functions

- void dsy_fatfs_init (void)

Variables

- uint8_t retSD
- char SDPath [4]
- FATFS SDFatFS
- FIL SDFile

7.18.1 Detailed Description

fatfs support.

7.18.2 Macro Definition Documentation

7.18.2.1 __fatfs_H

```
#define __fatfs_H
```

&

7.18.3 Function Documentation

7.18.3.1 dsy_fatfs_init()

```
void dsy_fatfs_init (
             void  )
```

&

7.18.4 Variable Documentation

7.18.4.1 retSD

```
uint8_t retSD
```

&

7.18.4.2 SDFatFS

```
FATFS SDFatFS
```

&

7.18.4.3 SDFile

```
FIL SDFile
```

&

7.18.4.4 SDPath

```
char SDPath[4]
```

&

7.19 src/ffconf.h File Reference

```
#include "util_bsp_sd_diskio.h"
#include <stdlib.h>
```

Macros

- #define _FFCONF 68300
- #define _FS_READONLY 0
- #define _FS_MINIMIZE 0
- #define _USE_STRFUNC 2
- #define _USE_FIND 0
- #define _USE_MKFS 1
- #define _USE_FASTSEEK 1
- #define _USE_EXPAND 0
- #define _USE_CHMOD 0
- #define _USE_LABEL 0
- #define _USE_FORWARD 0
- #define _CODE_PAGE 850
- #define _USE_LFN 1
- #define _MAX_LFN 255
- #define _LFN_UNICODE 0
- #define _STRF_ENCODE 3
- #define _FS_RPATH 0
- #define _VOLUMES 1
- #define _STR_VOLUME_ID 0
- #define _VOLUME_STRS
- #define _MULTI_PARTITION 0
- #define _MIN_SS 512
- #define _MAX_SS 512
- #define _USE_TRIM 0
- #define _FS_NOFSINFO 0
- #define _FS_TINY 0
- #define _FS_EXFAT 0
- #define _FS_NORTC 0
- #define _NORTC_MON 6
- #define _NORTC_MDAY 4
- #define _NORTC_YEAR 2015
- #define _FS_LOCK 2
- #define _FS_REENTRANT 0
- #define _FS_TIMEOUT 1000
- #define _SYNC_t osSemaphoreId
- #define ff_malloc malloc
- #define ff_free free

7.19.1 Detailed Description

Further fatfs support.

7.19.2 Macro Definition Documentation

7.19.2.1 _CODE_PAGE

```
#define _CODE_PAGE 850
```

This option specifies the OEM code page to be used on the target system. / Incorrect setting of the code page can cause a file open failure. / / 1 - ASCII (No extended character. Non-LFN cfg. only) / 437 - U.S. / 720 - Arabic / 737 - Greek / 771 - KBL / 775 - Baltic / 850 - Latin 1 / 852 - Latin 2 / 855 - Cyrillic / 857 - Turkish / 860 - Portuguese / 861 - Icelandic / 862 - Hebrew / 863 - Canadian French / 864 - Arabic / 865 - Nordic / 866 - Russian / 869 - Greek 2 / 932 - Japanese (DBCS) / 936 - Simplified Chinese (DBCS) / 949 - Korean (DBCS) / 950 - Traditional Chinese (DBCS)

7.19.2.2 _FFCONF

```
#define _FFCONF 68300
```

FatFs - Generic FAT file system module R0.12c (C)ChaN, 2017

Attention

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7.19.2.3 _FS_EXFAT

```
#define _FS_EXFAT 0
```

This option switches support of exFAT file system. (0:Disable or 1:Enable) / When enable exFAT, also LFN needs to be enabled. (_USE_LFN >= 1) / Note that enabling exFAT discards C89 compatibility.

7.19.2.4 _FS_LOCK

```
#define _FS_LOCK 2
```

0:Disable or >=1:Enable The option _FS_LOCK switches file lock function to control duplicated file open / and illegal operation to open objects. This option must be 0 when _FS_READONLY / is 1. / / 0: Disable file lock function. To avoid volume corruption, application program / should avoid illegal open, remove and rename to the open objects. / >0: Enable file lock function. The value defines how many files/sub-directories / can be opened simultaneously under file lock control. Note that the file / lock control is independent of re-entrancy.

7.19.2.5 _FS_MINIMIZE

```
#define _FS_MINIMIZE 0
```

0 to 3 This option defines minimization level to remove some basic API functions. / / 0: All basic functions are enabled. / 1: f_stat(), f_getfree(), f_unlink(), f_mkdir(), f_truncate() and f_rename() / are removed. / 2: f_opendir(), f_readdir() and f_closedir() are removed in addition to 1. / 3: f_lseek() function is removed in addition to 2.

7.19.2.6 _FS_NOFSINFO

```
#define _FS_NOFSINFO 0
```

0,1,2 or 3 If you need to know correct free space on the FAT32 volume, set bit 0 of this / option, and f_getfree() function at first time after volume mount will force / a full FAT scan. Bit 1 controls the use of last allocated cluster number. / / bit0=0: Use free cluster count in the FSINFO if available. / bit0=1: Do not trust free cluster count in the FSINFO. / bit1=0: Use last allocated cluster number in the FSINFO if available. / bit1=1: Do not trust last allocated cluster number in the FSINFO.

7.19.2.7 _FS_NORTC

```
#define _FS_NORTC 0
```

&

7.19.2.8 _FS_READONLY

```
#define _FS_READONLY 0
```

0:Read/Write or 1:Read only This option switches read-only configuration. (0:Read/Write or 1:Read-only) / Read-only configuration removes writing API functions, f_write(), f_sync(), / f_unlink(), f_mkdir(), f_chmod(), f_rename(), f_truncate(), f_getfree() / and optional writing functions as well.

7.19.2.9 _FS_REENTRANT

```
#define _FS_REENTRANT 0
```

0:Disable or 1:Enable

7.19.2.10 _FS_RPATH

```
#define _FS_RPATH 0
```

0 to 2 This option configures support of relative path. / / 0: Disable relative path and remove related functions. / 1: Enable relative path. f_chdir() and f_chdrive() are available. / 2: f_getcwd() function is available in addition to 1.

7.19.2.11 _FS_TIMEOUT

```
#define _FS_TIMEOUT 1000
```

Timeout period in unit of time ticks

7.19.2.12 _FS_TINY

```
#define _FS_TINY 0
```

0:Normal or 1:Tiny This option switches tiny buffer configuration. (0:Normal or 1:Tiny) / At the tiny configuration, size of file object (FIL) is reduced _MAX_SS bytes. / Instead of private sector buffer eliminated from the file object, common sector / buffer in the file system object (FATFS) is used for the file data transfer.

7.19.2.13 _LFN_UNICODE

```
#define _LFN_UNICODE 0
```

0:ANSI/OEM or 1:Unicode This option switches character encoding on the API. (0:ANSI/OEM or 1:UTF-16) / To use Unicode string for the path name, enable LFN and set _LFN_UNICODE = 1. / This option also affects behavior of string I/O functions.

7.19.2.14 _MAX_LFN

```
#define _MAX_LFN 255
```

Maximum LFN length to handle (12 to 255) The _USE_LFN switches the support of long file name (LFN). / / 0: Disable support of LFN. _MAX_LFN has no effect. / 1: Enable LFN with static working buffer on the BSS. Always NOT thread-safe. / 2: Enable LFN with dynamic working buffer on the STACK. / 3: Enable LFN with dynamic working buffer on the HEAP. / / To enable the LFN, Unicode handling functions (option/unicode.c) must be added / to the project. The working buffer occupies (_MAX_LFN + 1) $*$ 2 bytes and / additional 608 bytes at exFAT enabled. _MAX_LFN can be in range from 12 to 255. / It should be set 255 to support full featured LFN operations. / When use stack for the working buffer, take care on stack overflow. When use heap / memory for the working buffer, memory management functions, ff_memalloc() and / ff_memfree(), must be added to the project.

7.19.2.15 _MAX_SS

```
#define _MAX_SS 512
```

512, 1024, 2048 or 4096 These options configure the range of sector size to be supported. (512, 1024, / 2048 or 4096) Always set both 512 for most systems, all type of memory cards and / harddisk. But a larger value may be required for on-board flash memory and some / type of optical media. When _MAX_SS is larger than _MIN_SS, FatFs is configured / to variable sector size and GET_SECTOR_SIZE command must be implemented to the / disk_ioctl() function.

7.19.2.16 _MIN_SS

```
#define _MIN_SS 512
```

512, 1024, 2048 or 4096

7.19.2.17 _MULTI_PARTITION

```
#define _MULTI_PARTITION 0
```

0:Single partition, 1:Multiple partition This option switches support of multi-partition on a physical drive. / By default (0), each logical drive number is bound to the same physical drive / number and only an FAT volume found on the physical drive will be mounted. / When multi-partition is enabled (1), each logical drive number can be bound to / arbitrary physical drive and partition listed in the VolToPart[]. Also f_fdisk() / funciton will be available.

7.19.2.18 _NORTC_MDAY

```
#define _NORTC_MDAY 4
```

&

7.19.2.19 _NORTC_MON

```
#define _NORTC_MON 6
```

&

7.19.2.20 _NORTC_YEAR

```
#define _NORTC_YEAR 2015
```

The option _FS_NORTC switches timestamp functiton. If the system does not have / any RTC function or valid timestamp is not needed, set _FS_NORTC = 1 to disable / the timestamp function. All objects modified by FatFs will have a fixed timestamp / defined by _NORTC_MON, _NORTC_MDAY and _NORTC_YEAR in local time. / To enable timestamp function (_FS_NORTC = 0), get_fattime() function need to be / added to the project to get current time form real-time clock. _NORTC_MON, / _NORTC_MDAY and _NORTC_YEAR have no effect. / These options have no effect at read-only configuration (_FS_READONLY = 1).

7.19.2.21 _STR_VOLUME_ID

```
#define _STR_VOLUME_ID 0
```

0:Use only 0-9 for drive ID, 1:Use strings for drive ID

7.19.2.22 _STRF_ENCODE

```
#define _STRF_ENCODE 3
```

When _LFN_UNICODE == 1, this option selects the character encoding ON THE FILE to / be read/written via string I/O functions, f_gets(), f_putc(), f_puts and f_printf(). / / 0: ANSI/OEM / 1: UTF-16LE / 2: UTF-16BE / 3: UTF-8 / / This option has no effect when _LFN_UNICODE == 0.

7.19.2.23 _SYNC_t

```
#define _SYNC_t osSemaphoreId
```

The option _FS_REENTRANT switches the re-entrancy (thread safe) of the FatFs / module itself. Note that regardless of this option, file access to different / volume is always re-entrant and volume control functions, f_mount(), f_mkfs() / and f_fdisk() function, are always not re-entrant. Only file/directory access / to the same volume is under control of this function. / / 0: Disable re-entrancy. _FS_TIMEOUT and _SYNC_t have no effect. / 1: Enable re-entrancy. Also user provided synchronization handlers, / ff_req_grant(), ff_rel_grant(), ff_del_syncobj() and ff_cre_syncobj() / function, must be added to the project. Samples are available in / option/syscall.c. / / The _FS↩ _TIMEOUT defines timeout period in unit of time tick. / The _SYNC_t defines O/S dependent sync object type. e.g. HANDLE, ID, OS_EVENT∗, / SemaphoreHandle_t and etc.. A header file for O/S definitions needs to be / included somewhere in the scope of ff.h.

7.19.2.24 _USE_CHMOD

```
#define _USE_CHMOD 0
```

This option switches attribute manipulation functions, f_chmod() and f_utime(). / (0:Disable or 1:Enable) Also _F↩ S_READONLY needs to be 0 to enable this option.

7.19.2.25 _USE_EXPAND

```
#define _USE_EXPAND 0
```

This option switches f_expand function. (0:Disable or 1:Enable)

7.19.2.26 _USE_FASTSEEK

```
#define _USE_FASTSEEK 1
```

This option switches fast seek feature. (0:Disable or 1:Enable)

7.19.2.27 _USE_FIND

```
#define _USE_FIND 0
```

This option switches filtered directory read functions, f_findfirst() and / f_findnext(). (0:Disable, 1:Enable 2:Enable with matching altname[] too)

7.19.2.28 _USE_FORWARD

```
#define _USE_FORWARD 0
```

This option switches f_forward() function. (0:Disable or 1:Enable)

7.19.2.29 _USE_LABEL

```
#define _USE_LABEL 0
```

This option switches volume label functions, f_getlabel() and f_setlabel(). / (0:Disable or 1:Enable)

7.19.2.30 _USE_LFN

```
#define _USE_LFN 1
```

0 to 3

7.19.2.31 _USE_MKFS

```
#define _USE_MKFS 1
```

This option switches f_mkfs() function. (0:Disable or 1:Enable)

7.19.2.32 _USE_STRFUNC

```
#define _USE_STRFUNC 2
```

0:Disable or 1-2:Enable This option switches string functions, f_gets(), f_putc(), f_puts() and / f_printf(). / / 0: Disable string functions. / 1: Enable without LF-CRLF conversion. / 2: Enable with LF-CRLF conversion.

7.19.2.33 _USE_TRIM

```
#define _USE_TRIM 0
```

This option switches support of ATA-TRIM. (0:Disable or 1:Enable) / To enable Trim function, also CTRL_TRIM command should be implemented to the / disk_ioctl() function.

7.19.2.34 _VOLUME_STRS

```
#define _VOLUME_STRS
```

Value:

```
"RAM", "NAND", "CF", "SD1", "SD2", "USB1", "USB2", \
        "USB3"
```

_STR_VOLUME_ID switches string support of volume ID. / When _STR_VOLUME_ID is set to 1, also pre-defined strings can be used as drive / number in the path name. _VOLUME_STRS defines the drive ID strings for each / logical drives. Number of items must be equal to _VOLUMES. Valid characters for / the drive ID strings are: A-Z and 0-9.

7.19.2.35 _VOLUMES

```
#define _VOLUMES 1
```

Number of volumes (logical drives) to be used.

7.19.2.36 ff_free

```
#define ff_free free
```

define the ff_malloc ff_free macros as standard malloc free

7.19.2.37 ff_malloc

```
#define ff_malloc malloc
```

define the ff_malloc ff_free macros as standard malloc free

7.20 src/hid_audio.h File Reference

Audio Driver
Configures Audio Device and provides callback for signal processing.
Many of the hard-coded values here will change (increase), and/or
be replaced by configurable options
.

```
#include <stddef.h>
#include <stdint.h>
#include "per_sai.h"
#include "per_i2c.h"
```

Classes

- struct dsy_audio_handle

Macros

- #define DSY_AUDIO_H
- #define DSY_AUDIO_BLOCK_SIZE_MAX 128
- #define DSY_AUDIO_CHANNELS_MAX 2
- #define DSY_AUDIO_SAMPLE_RATE 48014.0f

Typedefs

- typedef void(* dsy_audio_mc_callback) (float **, float **, size_t)

Enumerations

- enum { DSY_AUDIO_INTERNAL, DSY_AUDIO_EXTERNAL, DSY_AUDIO_LAST }

Functions

- void dsy_audio_init (dsy_audio_handle *handle)
- void dsy_audio_set_callback (uint8_t intext, dsy_audio_callback cb)
- void dsy_audio_set_mc_callback (dsy_audio_mc_callback cb)
- void dsy_audio_set_blocksize (uint8_t intext, size_t blocksize)
- void dsy_audio_start (uint8_t intext)
- void dsy_audio_stop (uint8_t intext)
- void dsy_audio_enter_bypass (uint8_t intext)
- void dsy_audio_exit_bypass (uint8_t intext)
- void dsy_audio_passthru (float *in, float *out, size_t size)
- void dsy_audio_silence (float *in, float *out, size_t size)

7.20.1 Detailed Description

Audio Driver
Configures Audio Device and provides callback for signal processing.
Many of the hard-coded values here will change (increase), and/or
be replaced by configurable options
.

7.20.2 Macro Definition Documentation

7.20.2.1 DSY_AUDIO_BLOCK_SIZE_MAX

```
#define DSY_AUDIO_BLOCK_SIZE_MAX 128
```

Defines for generic maximums While 'Audio Channels Max' is set to 2, this is per-SAI 4x4 Audio I/O is possible using the dsy_audio_mc_callback Hard-coded samplerate is calculated from original clock tree. The new clock tree has less than 0.01% error for all supported sampleratesMax block size

7.20.2.2 DSY_AUDIO_CHANNELS_MAX

```
#define DSY_AUDIO_CHANNELS_MAX 2
```

Max number of audio channels

7.20.2.3 DSY_AUDIO_H

```
#define DSY_AUDIO_H
```

&

7.20.2.4 DSY_AUDIO_SAMPLE_RATE

```
#define DSY_AUDIO_SAMPLE_RATE 48014.0f
```

Default sample rate

7.20.3 Typedef Documentation

7.20.3.1 dsy_audio_mc_callback

```
typedef void(* dsy_audio_mc_callback) (float **, float **, size_t)
```

These are user-defineable callbacks that are called when audio data is ready to be received/transmitted. This function is called at samplerate/blocksize (e.g. 1kHz when Function to define for using a single Stereo device for I/Oaudio is packed as: { LEFT | RIGHT | LEFT | RIGHT } typical example:

```
void AudioCallback(float *in, float *out, size_t size)
{        for (size_t i = 0; i < size; i+=2)
{
    out[i] = in[i]; // Left
    out[i+1] = in[i+1]; // Right
}        }
```

∗/ typedef void (*dsy_audio_callback)(float*, float∗, size_t);

/∗∗ Defaults to 4 channels, and is fixed for now. (still works for stereo, but will still fill buffers) ∗/ // /∗∗ audio is packed as: ∗/ // /∗∗ { LEFT | LEFT + 1 | . . . | LEFT + SIZE | RIGHT | RIGHT + 1 | . . . | RIGHT + SIZE } ∗/ // /∗∗ typical example:

```
void AudioCallback(float **in, float **out, size_t size)
{
*/
```

7.20.4 Enumeration Type Documentation

7.20.4.1 anonymous enum

```
anonymous enum
```

Internally, there are two separate 'audio blocks' that can be configured together or separately

Enumerator

DSY_AUDIO_INTERNAL	&
DSY_AUDIO_EXTERNAL	&
DSY_AUDIO_LAST	&

7.20.5 Function Documentation

7.20.5.1 dsy_audio_enter_bypass()

```
void dsy_audio_enter_bypass (
            uint8_t intext )
```

If the device supports hardware bypass, enter that mode.**Only minimally tested with WM8731 codec.**

7.20.5.2 dsy_audio_exit_bypass()

```
void dsy_audio_exit_bypass (
            uint8_t intext )
```

If the device supports hardware bypass, exit that mode.**Only minimally tested with WM8731 codec.**

7.20.5.3 dsy_audio_init()

```
void dsy_audio_init (
            dsy_audio_handle * handle )
```

Initializes the Audio Engine using configurations set to the sai_handlei2c_handles can be set to NULL if not needed.

7.20.5.4 dsy_audio_passthru()

```
void dsy_audio_passthru (
            float * in,
            float * out,
            size_t size )
```

A few useful stereo-interleaved callbacks Passes the input to the output

7.20.5.5 dsy_audio_set_blocksize()

```
void dsy_audio_set_blocksize (
            uint8_t intext,
            size_t blocksize )
```

Sets the number of samples (per-channel) to be handled in a single audio frame.

7.20.5.6 dsy_audio_set_callback()

```
void dsy_audio_set_callback (
             uint8_t intext,
             dsy_audio_callback cb )
```

Sets the user defined, interleaving callback to be called when audio data is ready.

intext is a specifier for DSY_AUDIO_INT/EXT (which audio peripheral to use).

When using this, each 'audio block' can have completely independent callbacks.

7.20.5.7 dsy_audio_set_mc_callback()

```
void dsy_audio_set_mc_callback (
             dsy_audio_mc_callback cb )
```

Sets the user defined, non-interleaving callback to be called when audio data is ready.This will always use both DSY_AUDIO_INT and DSY_AUDIO_EXT blocks together.To ensure clean audio you'll want to make sure the two SAIs are set to the same samplerate

7.20.5.8 dsy_audio_silence()

```
void dsy_audio_silence (
             float * in,
             float * out,
             size_t size )
```

sets outputs to 0 without stopping the Audio Engine.

7.20.5.9 dsy_audio_start()

```
void dsy_audio_start (
             uint8_t intext )
```

Starts Audio Engine, callbacks will begin getting called. When using with dsy_audio_mc_callback (for 4 channels), this function should be called for both audio blocks

7.20.5.10 dsy_audio_stop()

```
void dsy_audio_stop (
             uint8_t intext )
```

Stops transmitting/receiving audio on the specified audio block.

7.21 src/hid_ctrl.h File Reference

```
#include <stdint.h>
```

Classes

- class daisy::AnalogControl

  Hardware Interface for control inputs
  Primarily designed for ADC input controls such as
  potentiometers, and control voltage.
  .

7.22 src/hid_encoder.h File Reference

```
#include "daisy_core.h"
#include "per_gpio.h"
#include "hid_switch.h"
```

Classes

- class daisy::Encoder

  Generic Class for handling Quadrature Encoders
  Inspired/influenced by Mutable Instruments (pichenettes) Encoder classes.

7.23 src/hid_gatein.h File Reference

```
#include "per_gpio.h"
```

Classes

- class daisy::GateIn

  Generic Class for handling gate inputs through GPIO.

7.24 src/hid_led.h File Reference

```
#include "daisy_core.h"
#include "per_gpio.h"
```

Classes

- class daisy::Led

  LED Class providing simple Software PWM ability, etc
  Eventually this will work with hardware PWM, and external LED Driver devices as well.

7.25 src/hid_midi.h File Reference

```
#include <stdint.h>
#include <stdlib.h>
#include "per_uart.h"
#include "util_ringbuffer.h"
```

Classes

- struct daisy::NoteOnEvent
- struct daisy::ControlChangeEvent
- struct daisy::MidiEvent
- class daisy::MidiHandler

  Simple MIDI Handler
  Parses bytes from an input into valid MidiEvents.
  The MidiEvents fill a FIFO queue that the user can pop messages from.

Enumerations

- enum daisy::MidiMessageType {
  daisy::NoteOff, daisy::NoteOn, daisy::PolyphonicKeyPressure, daisy::ControlChange,
  daisy::ProgramChange, daisy::ChannelPressure, daisy::PitchBend, daisy::MessageLast }

7.25.1 Enumeration Type Documentation

7.25.1.1 MidiMessageType

```
enum daisy::MidiMessageType
```

Parsed from the Status Byte, these are the common Midi Messages that can be handled.
At this time only 3-byte messages are correctly parsed into MidiEvents.

Enumerator

NoteOff	&
NoteOn	&
PolyphonicKeyPressure	&
ControlChange	&
ProgramChange	&
ChannelPressure	&
PitchBend	&
MessageLast	&

7.26 src/hid_oled_display.h File Reference

```
#include <stdlib.h>
#include <stdint.h>
#include "util_oled_fonts.h"
#include "daisy_core.h"
```

Classes

- class daisy::OledDisplay

Macros

- #define DSY_OLED_DISPLAY_H
- #define SSD1309_HEIGHT 64
- #define SSD1309_WIDTH 128

7.26.1 Macro Definition Documentation

7.26.1.1 DSY_OLED_DISPLAY_H

```
#define DSY_OLED_DISPLAY_H
```

Macro

7.26.1.2 SSD1309_HEIGHT

```
#define SSD1309_HEIGHT 64
```

SSD1309 height in pixels

7.26.1.3 SSD1309_WIDTH

```
#define SSD1309_WIDTH 128
```

SSD1309 width in pixels

7.27 src/hid_parameter.h File Reference

```
#include <stdint.h>
#include "hid_ctrl.h"
```

Classes

- class daisy::Parameter

7.28 src/hid_rgb_led.h File Reference

```
#include "hid_led.h"
#include "util_color.h"
```

Classes

- class daisy::RgbLed

7.29 src/hid_switch.h File Reference

```
#include "daisy_core.h"
#include "per_gpio.h"
```

Classes

- class daisy::Switch

7.30 src/hid_usb.h File Reference

```
#include <stdint.h>
#include <stdlib.h>
```

Classes

- class daisy::UsbHandle

  Interface for initializing and using the USB Peripherals on the daisy.

7.31 src/hid_wavplayer.h File Reference

```
#include "daisy_core.h"
#include "util_wav_format.h"
```

Classes

- struct daisy::WavFileInfo
- class daisy::WavPlayer

Macros

- #define DSY_WAVPLAYER_H
- #define WAV_FILENAME_MAX 256

7.31.1 Macro Definition Documentation

7.31.1.1 DSY_WAVPLAYER_H

```
#define DSY_WAVPLAYER_H
```

Macro

7.31.1.2 WAV_FILENAME_MAX

```
#define WAV_FILENAME_MAX 256
```

Maximum LFN (set to same in FatFs (ffconf.h)

7.32 src/per_adc.h File Reference

```
#include <stdint.h>
#include <stdlib.h>
#include "daisy_core.h"
#include "per_gpio.h"
```

Classes

- struct daisy::AdcChannelConfig
- class daisy::AdcHandle

Macros

- #define DSY_ADC_H
- #define DSY_ADC_MAX_CHANNELS 14

7.32.1 Macro Definition Documentation

7.32.1.1 DSY_ADC_H

```
#define DSY_ADC_H
```

Macro

7.32.1.2 DSY_ADC_MAX_CHANNELS

```
#define DSY_ADC_MAX_CHANNELS 14
```

Maximum number of ADC channels

7.33 src/per_dac.h File Reference

```
#include "daisy_core.h"
```

Classes

- struct dsy_dac_handle

Enumerations

- enum dsy_dac_mode { DSY_DAC_MODE_POLLING, DSY_DAC_MODE_LAST }
- enum dsy_dac_bitdepth { DSY_DAC_BITS_8, DSY_DAC_BITS_12, DSY_DAC_BITS_LAST }
- enum dsy_dac_channel { DSY_DAC_CHN1, DSY_DAC_CHN2, DSY_DAC_CHN_LAST, DSY_DAC_CH↩ N_BOTH }

Functions

- void dsy_dac_init (dsy_dac_handle *dsy_hdac, dsy_dac_channel channel)
- void dsy_dac_start (dsy_dac_channel channel)
- void dsy_dac_write (dsy_dac_channel channel, uint16_t val)

7.33.1 Enumeration Type Documentation

7.33.1.1 dsy_dac_bitdepth

```
enum dsy_dac_bitdepth
```

Sets the bit depth of the DAC output This can be set independently for each channel.

Enumerator

DSY_DAC_BITS_8	&
DSY_DAC_BITS_12	&
DSY_DAC_BITS_LAST	&

7.33.1.2 dsy_dac_channel

```
enum dsy_dac_channel
```

Sets which channel(s) are initialized with the settings chosen.

Enumerator

DSY_DAC_CHN1	&
DSY_DAC_CHN2	&
DSY_DAC_CHN_LAST	&
DSY_DAC_CHN_BOTH	&

7.33.1.3 dsy_dac_mode

```
enum dsy_dac_mode
```

Driver for the built in DAC on the STM32 The STM32 has 2 Channels of independently configurable DACs, with up to 12-bit resolution.Currently only Polling is supported.

Enumerator

DSY_DAC_MODE_POLLING	Polling mode
DSY_DAC_MODE_LAST	3

7.33.2 Function Documentation

7.33.2.1 dsy_dac_init()

```
void dsy_dac_init (
            dsy_dac_handle * dsy_hdac,
            dsy_dac_channel channel )
```

Initializes the specified channel(s) of the DAC

Parameters

**dsy_hdac*	Dac to initialize
channel	Channels to init

7.33.2.2 dsy_dac_start()

```
void dsy_dac_start (
            dsy_dac_channel channel )
```

Turns on the DAC and turns on any internal timer if necessary.

Parameters

channel	Channel to start

7.33.2.3 dsy_dac_write()

```
void dsy_dac_write (
            dsy_dac_channel channel,
            uint16_t val )
```

Sets the specified channel of the dac to the value (within bitdepth) resolution. When set to 8-bit, val should be 0-255 When set to 12-bit, val should be 0-4095

Parameters

channel	Channel to write to
val	Value to write

7.34 src/per_gpio.h File Reference

```
#include "daisy_core.h"
```

Classes

- struct dsy_gpio

Enumerations

- enum dsy_gpio_mode { DSY_GPIO_MODE_INPUT, DSY_GPIO_MODE_OUTPUT_PP, DSY_GPIO_MODE_OUTPUT_OD, DSY_GPIO_MODE_ANALOG, DSY_GPIO_MODE_LAST }
- enum dsy_gpio_pull { DSY_GPIO_NOPULL, DSY_GPIO_PULLUP, DSY_GPIO_PULLDOWN }

Functions

- void dsy_gpio_init (dsy_gpio *p)
- void dsy_gpio_deinit (dsy_gpio *p)
- uint8_t dsy_gpio_read (dsy_gpio *p)
- void dsy_gpio_write (dsy_gpio *p, uint8_t state)
- void dsy_gpio_toggle (dsy_gpio *p)

7.34.1 Detailed Description

General Purpose IO driver

7.34.2 Enumeration Type Documentation

7.34.2.1 dsy_gpio_mode

enum dsy_gpio_mode

Sets the mode of the GPIO

Enumerator

DSY_GPIO_MODE_INPUT	&
DSY_GPIO_MODE_OUTPUT_PP	Push-Pull
DSY_GPIO_MODE_OUTPUT_OD	Open-Drain
DSY_GPIO_MODE_ANALOG	&
DSY_GPIO_MODE_LAST	&

7.34.2.2 dsy_gpio_pull

enum dsy_gpio_pull

Configures whether an internal Pull up or Pull down resistor is used

Enumerator

DSY_GPIO_NOPULL	&
DSY_GPIO_PULLUP	&
DSY_GPIO_PULLDOWN	&

7.34.3 Function Documentation

7.34.3.1 dsy_gpio_deinit()

```
void dsy_gpio_deinit (
            dsy_gpio * p )
```

Deinitializes the gpio pin

Parameters

*p	Pin pointer

7.34.3.2 dsy_gpio_init()

```
void dsy_gpio_init (
            dsy_gpio * p )
```

Initializes the gpio with the settings configured.

Parameters

*p	Pin pointer

7.34.3.3 dsy_gpio_read()

```
uint8_t dsy_gpio_read (
            dsy_gpio * p )
```

Reads the state of the gpio pin

Parameters

*p	Pin pointer

Returns

1 if the pin is HIGH, and 0 if the pin is LOW

7.34.3.4 dsy_gpio_toggle()

```
void dsy_gpio_toggle (
            dsy_gpio * p )
```

Toggles the state of the pin so that it is not at the same state as it was previously.

Parameters

*p	Pin pointer

7.34.3.5 dsy_gpio_write()

```
void dsy_gpio_write (
            dsy_gpio * p,
            uint8_t state )
```

Writes the state to the gpio pin Pin will be set to 3v3 when state is 1, and 0V when state is 0

Parameters

*p	Pin pointer
state	State to write

7.35 src/per_i2c.h File Reference

```
#include "daisy_core.h"
```

Classes

- struct dsy_i2c_handle

Macros

- #define DSY_I2C_H

Enumerations

- enum dsy_i2c_periph { DSY_I2C_PERIPH_1, DSY_I2C_PERIPH_2, DSY_I2C_PERIPH_3, DSY_I2C_PE↩ RIPH_4 }
- enum dsy_i2c_pin { DSY_I2C_PIN_SCL, DSY_I2C_PIN_SDA, DSY_I2C_PIN_LAST }
- enum dsy_i2c_speed { DSY_I2C_SPEED_100KHZ, DSY_I2C_SPEED_400KHZ, DSY_I2C_SPEED_1MHZ, DSY_I2C_SPEED_LAST }

Functions

- void dsy_i2c_init (dsy_i2c_handle *dsy_hi2c)

7.35.1 Macro Definition Documentation

7.35.1.1 DSY_I2C_H

```
#define DSY_I2C_H
```

Macro

7.35.2 Enumeration Type Documentation

7.35.2.1 dsy_i2c_periph

```
enum dsy_i2c_periph
```

Driver for controlling I2C devices Specifices the internal peripheral to use (these are mapped to different pins on the hardware).

Enumerator

DSY_I2C_PERIPH↩ _1	&
DSY_I2C_PERIPH↩ _2	&
DSY_I2C_PERIPH↩ _3	&
DSY_I2C_PERIPH↩ _4	&

7.35.2.2 dsy_i2c_pin

```
enum dsy_i2c_pin
```

List of pins associated with the peripheral. These must be set in the handle's pin_config.

Enumerator

DSY_I2C_PIN_SCL	&
DSY_I2C_PIN_SDA	&
DSY_I2C_PIN_LAST	&

7.35.2.3 dsy_i2c_speed

```
enum dsy_i2c_speed
```

Rate at which the clock/data will be sent/received. The device being used will have maximum speeds. 1MHZ Mode is currently 886kHz∗∗

Enumerator

DSY_I2C_SPEED_100KHZ	&
DSY_I2C_SPEED_400KHZ	&
DSY_I2C_SPEED_1MHZ	&
DSY_I2C_SPEED_LAST	&

7.35.3 Function Documentation

7.35.3.1 dsy_i2c_init()

```
void dsy_i2c_init (
            dsy_i2c_handle * dsy_hi2c )
```

Initializes an I2C peripheral with the data given from the handle.

Parameters

∗*dsy_hi2c*	Required to initialize.

7.36 src/per_qspi.h File Reference

```
#include <stdint.h>
#include "daisy_core.h"
```

Classes

- struct dsy_qspi_handle

Macros

- #define DSY_QSPI
- #define DSY_MEMORY_OK ((uint32_t)0x00)
- #define DSY_MEMORY_ERROR ((uint32_t)0x01)
- #define DSY_QSPI_TEXT
- #define DSY_QSPI_DATA
- #define DSY_QSPI_BSS

Enumerations

- enum dsy_qspi_pin {
  DSY_QSPI_PIN_IO0, DSY_QSPI_PIN_IO1, DSY_QSPI_PIN_IO2, DSY_QSPI_PIN_IO3,
  DSY_QSPI_PIN_CLK, DSY_QSPI_PIN_NCS, DSY_QSPI_PIN_LAST }
- enum dsy_qspi_mode { DSY_QSPI_MODE_DSY_MEMORY_MAPPED, DSY_QSPI_MODE_INDIRECT_↩
  POLLING, DSY_QSPI_MODE_LAST }
- enum dsy_qspi_device { DSY_QSPI_DEVICE_IS25LP080D, DSY_QSPI_DEVICE_IS25LP064A, DSY_Q↩
  SPI_DEVICE_LAST }

Functions

- int dsy_qspi_init (dsy_qspi_handle *hqspi)
- int dsy_qspi_deinit ()
- int dsy_qspi_writepage (uint32_t adr, uint32_t sz, uint8_t *buf)
- int dsy_qspi_write (uint32_t address, uint32_t size, uint8_t *buffer)
- int dsy_qspi_erase (uint32_t start_adr, uint32_t end_adr)
- int dsy_qspi_erasesector (uint32_t addr)

7.36.1 Macro Definition Documentation

7.36.1.1 DSY_MEMORY_ERROR

```
#define DSY_MEMORY_ERROR ((uint32_t)0x01)
```

&

7.36.1.2 DSY_MEMORY_OK

```
#define DSY_MEMORY_OK ((uint32_t)0x00)
```

&

7.36.1.3 DSY_QSPI

```
#define DSY_QSPI
```

Macro

7.36.1.4 DSY_QSPI_BSS

```
#define DSY_QSPI_BSS
```

Value:

```
__attribute__((section( \
        ".qspiflash_bss")))
```

used for reading memory in memory_mapped mode.

7.36.1.5 DSY_QSPI_DATA

```
#define DSY_QSPI_DATA
```

Value:

```
__attribute__((section( \
        ".qspiflash_data")))
```

used for reading memory in memory_mapped mode.

7.36.1.6 DSY_QSPI_TEXT

```
#define DSY_QSPI_TEXT
```

Value:

```
__attribute__((section( \
        ".qspiflash_text")))
```

used for reading memory in memory_mapped mode.

7.36.2 Enumeration Type Documentation

7.36.2.1 dsy_qspi_device

```
enum dsy_qspi_device
```

Flash Devices supported. (Both of these are more-or-less the same, just different sizes).

Enumerator

DSY_QSPI_DEVICE_IS25LP080D	&
DSY_QSPI_DEVICE_IS25LP064A	&
DSY_QSPI_DEVICE_LAST	&

7.36.2.2 dsy_qspi_mode

`enum dsy_qspi_mode`

Modes of operation. Memory Mapped mode: QSPI configured so that the QSPI can be read from starting address 0x90000000. Writing is not possible in this mode.
Indirect Polling mode: Device driver enabled.
Read/Write possible via dsy_qspi_∗ functions

Enumerator

DSY_QSPI_MODE_DSY_MEMORY_MAPPED	&
DSY_QSPI_MODE_INDIRECT_POLLING	&
DSY_QSPI_MODE_LAST	&

7.36.2.3 dsy_qspi_pin

`enum dsy_qspi_pin`

Driver for QSPI peripheral to interface with external flash memory.
Currently supported QSPI Devices:
IS25LP080DList of Pins used in QSPI (passed in during Init)

Enumerator

DSY_QSPI_PIN_IO0	&
DSY_QSPI_PIN_IO1	&
DSY_QSPI_PIN_IO2	&
DSY_QSPI_PIN_IO3	&
DSY_QSPI_PIN_CLK	&
DSY_QSPI_PIN_NCS	&
DSY_QSPI_PIN_LAST	&

7.36.3 Function Documentation

7.36.3.1 dsy_qspi_deinit()

```
int dsy_qspi_deinit ( )
```

Deinitializes the peripheral This should be called before reinitializing QSPI in a different mode.

Returns

DSY_MEMORY_OK or DSY_MEMORY_ERROR

7.36.3.2 dsy_qspi_erase()

```
int dsy_qspi_erase (
            uint32_t start_adr,
            uint32_t end_adr )
```

Erases the area specified on the chip. Erasures will happen by 4K, 32K or 64K increments. Smallest erase possible is 4kB at a time. (on IS25LP∗)

Parameters

start_adr	Address to begin erasing from
end_adr	Address to stop erasing at

Returns

DSY_MEMORY_OK or DSY_MEMORY_ERROR

7.36.3.3 dsy_qspi_erasesector()

```
int dsy_qspi_erasesector (
            uint32_t addr )
```

Erases a single sector of the chip. TODO: Document the size of this function.

Parameters

addr	Address of sector to erase

Returns

DSY_MEMORY_OK or DSY_MEMORY_ERROR

7.36.3.4 dsy_qspi_init()

```
int dsy_qspi_init (
            dsy_qspi_handle * hqspi )
```

Initializes QSPI peripheral, and Resets, and prepares memory for access.

Parameters

hqspi	should be populated with the mode, device and pin_config before calling this function.

Returns

DSY_MEMORY_OK or DSY_MEMORY_ERROR

7.36.3.5 dsy_qspi_write()

```
int dsy_qspi_write (
            uint32_t address,
            uint32_t size,
            uint8_t * buffer )
```

Writes data in buffer to to the QSPI. Starting at address to address+size

Parameters

address	Address to write to
size	Buffer size
buffer	Buffer to write

Returns

DSY_MEMORY_OK or DSY_MEMORY_ERROR

7.36.3.6 dsy_qspi_writepage()

```
int dsy_qspi_writepage (
            uint32_t adr,
            uint32_t sz,
            uint8_t * buf )
```

Writes a single page to to the specified address on the QSPI chip. For IS25LP∗ page size is 256 bytes.

Parameters

adr	Address to write to
sz	Buff size
buf	Buffer to write

Returns

DSY_MEMORY_OK or DSY_MEMORY_ERROR

7.37 src/per_sai.h File Reference

```
#include "daisy_core.h"
```

Classes

- struct dsy_sai_handle

Enumerations

- enum dsy_audio_sai { DSY_AUDIO_INIT_SAI1, DSY_AUDIO_INIT_SAI2, DSY_AUDIO_INIT_BOTH, DSY_AUDIO_INIT_NONE, DSY_AUDIO_INIT_LAST }
- enum dsy_audio_samplerate { DSY_AUDIO_SAMPLERATE_32K, DSY_AUDIO_SAMPLERATE_48K, DSY_AUDIO_SAMPLERATE_96K, DSY_AUDIO_SAMPLERATE_LAST }
- enum dsy_audio_bitdepth { DSY_AUDIO_BITDEPTH_16, DSY_AUDIO_BITDEPTH_24, DSY_AUDIO_BITDEPTH_LAST }
- enum dsy_audio_sync { DSY_AUDIO_SYNC_MASTER, DSY_AUDIO_SYNC_SLAVE, DSY_AUDIO_SYNC_LAST }
- enum dsy_audio_dir { DSY_AUDIO_RX, DSY_AUDIO_TX }
- enum dsy_sai_pin { DSY_SAI_PIN_MCLK, DSY_SAI_PIN_FS, DSY_SAI_PIN_SCK, DSY_SAI_PIN_SIN, DSY_SAI_PIN_SOUT, DSY_SAI_PIN_LAST }
- enum dsy_audio_device { DSY_AUDIO_NONE, DSY_AUDIO_DEVICE_PCM3060, DSY_AUDIO_DEVICE_WM8731, DSY_AUDIO_DEVICE_AK4556, DSY_AUDIO_DEVICE_LAST }
- enum { DSY_SAI_1, DSY_SAI_2, DSY_SAI_LAST }

Functions

- void dsy_sai_init (dsy_audio_sai init, dsy_audio_samplerate sr[2], dsy_audio_bitdepth bitdepth[2], dsy_audio_sync sync_config[2], dsy_gpio_pin *sai1_pin_list, dsy_gpio_pin *sai2_pin_list)
- void dsy_sai_init_from_handle (dsy_sai_handle *hsai)

7.37.1 Enumeration Type Documentation

7.37.1.1 anonymous enum

```
anonymous enum
```

Index for the several arrays in the sai_handle struct below.

Enumerator

DSY_SAI_1	&
DSY_SAI_2	&
DSY_SAI_LAST	&

7.37.1.2 dsy_audio_bitdepth

`enum dsy_audio_bitdepth`

Specifies the bitdepth of the hardware connected to the SAI peripheral

Enumerator

DSY_AUDIO_BITDEPTH_16	&
DSY_AUDIO_BITDEPTH_24	&
DSY_AUDIO_BITDEPTH_LAST	&

7.37.1.3 dsy_audio_device

`enum dsy_audio_device`

List of devices with built in support. Devices not listed here, will need to have initialization done externally.

Enumerator

DSY_AUDIO_NONE	For unsupported, or custom devices.
DSY_AUDIO_DEVICE_PCM3060	&
DSY_AUDIO_DEVICE_WM8731	&
DSY_AUDIO_DEVICE_AK4556	&
DSY_AUDIO_DEVICE_LAST	&

7.37.1.4 dsy_audio_dir

`enum dsy_audio_dir`

Each SAI has two datalines, they can independently be configured as inputs or outputs.

Enumerator

DSY_AUDIO_RX	&
DSY_AUDIO_TX	&

7.37.1.5 dsy_audio_sai

enum dsy_audio_sai

Driver for the SAI peripheral Supports SAI1 and SAI2 with several configuration options selects which SAI (or both/none) to initialize

Enumerator

DSY_AUDIO_INIT_SAI1	&
DSY_AUDIO_INIT_SAI2	&
DSY_AUDIO_INIT_BOTH	&
DSY_AUDIO_INIT_NONE	&
DSY_AUDIO_INIT_LAST	&

7.37.1.6 dsy_audio_samplerate

enum dsy_audio_samplerate

Currently Sample Rates are not correctly supported. All audio is currently run at 48kHz

Enumerator

DSY_AUDIO_SAMPLERATE_32K	&
DSY_AUDIO_SAMPLERATE_48K	&
DSY_AUDIO_SAMPLERATE_96K	&
DSY_AUDIO_SAMPLERATE_LAST	&

7.37.1.7 dsy_audio_sync

enum dsy_audio_sync

Setting for each SAI that sets whether the processor is generating the MCLK signal or not.

Enumerator

DSY_AUDIO_SYNC_MASTER	No Crystal
DSY_AUDIO_SYNC_SLAVE	Crystal
DSY_AUDIO_SYNC_LAST	&

7.37.1.8 dsy_sai_pin

```
enum dsy_sai_pin
```

List of the pins that need to be initialized SIN/SOUT is a bit misleading, and should be turned into A/B since it is possible to configure two inputs or two outputs on a single SAI.

Enumerator

DSY_SAI_PIN_MCLK	&
DSY_SAI_PIN_FS	&
DSY_SAI_PIN_SCK	&
DSY_SAI_PIN_SIN	&
DSY_SAI_PIN_SOUT	&
DSY_SAI_PIN_LAST	&

7.37.2 Function Documentation

7.37.2.1 dsy_sai_init()

```
void dsy_sai_init (
            dsy_audio_sai init,
            dsy_audio_samplerate sr[2],
            dsy_audio_bitdepth bitdepth[2],
            dsy_audio_sync sync_config[2],
            dsy_gpio_pin * sai1_pin_list,
            dsy_gpio_pin * sai2_pin_list )
```

Intializes the SAI peripheral(s) with the specified settings. Pinlists should be arrays of DSY_SAI_PIN_LAST elements

Parameters

init	&
sr[ ]	Sample rate per chan: 0, 1
bitdepth[ ]	Bitdepth per chan: 0, 1
sync_config[ ]	& sync config per chan: 0, 1
∗*sai1_pin_list*	&
∗*sai2_pin_list*	&

7.37.2.2 dsy_sai_init_from_handle()

```
void dsy_sai_init_from_handle (
            dsy_sai_handle * hsai )
```

Uses the data within ∗hsai to initialize the peripheral(s)

Parameters

hsai	&

7.38 src/per_sdmmc.h File Reference

```
#include <stdint.h>
```

Classes

- struct daisy::SdmmcHandlerInit
- class daisy::SdmmcHandler

Macros

- #define DSY_SDMMC_H
- #define DSY_SD_OK 0
- #define DSY_SD_ERROR 1

Enumerations

- enum daisy::SdmmcMode { daisy::SDMMC_MODE_FATFS }
- enum daisy::SdmmcBitWidth { daisy::SDMMC_BITS_1, daisy::SDMMC_BITS_4 }
- enum daisy::SdmmcSpeed { daisy::SDMMC_SPEED_400KHZ, daisy::SDMMC_SPEED_12MHZ }

7.38.1 Macro Definition Documentation

7.38.1.1 DSY_SD_ERROR

```
#define DSY_SD_ERROR 1
```

ERROR return

7.38.1.2 DSY_SD_OK

```
#define DSY_SD_OK 0
```

OK return

7.38.1.3 DSY_SDMMC_H

```
#define DSY_SDMMC_H
```

macro

7.38.2 Enumeration Type Documentation

7.38.2.1 SdmmcBitWidth

```
enum daisy::SdmmcBitWidth
```

Sets whether 4-bit mode or 1-bit mode is used for the SDMMC

Enumerator

SDMMC_BITS↩ _1	&
SDMMC_BITS↩ _4	&

7.38.2.2 SdmmcMode

```
enum daisy::SdmmcMode
```

Operating Mode. Currently only FatFS is supported.

Enumerator

SDMMC_MODE_FATFS	&

7.38.2.3 SdmmcSpeed

```
enum daisy::SdmmcSpeed
```

Sets the desired clock speed of the SD card bus.
Initialization is always done at or below 400kHz, and then the user speed is set.

Enumerator

SDMMC_SPEED_400KHZ	&
SDMMC_SPEED_12MHZ	&

7.39 src/per_spi.h File Reference

```
#include "daisy_core.h"
```

Classes

- class daisy::SpiHandle

Enumerations

- enum daisy::SpiPeriph { daisy::SPI_PERIPH_1, daisy::SPI_PERIPH_3, daisy::SPI_PERIPH_6 }
- enum daisy::SpiPin { daisy::SPI_PIN_CS, daisy::SPI_PIN_SCK, daisy::SPI_PIN_MOSI, daisy::SPI_PIN_↩ MISO }

7.39.1 Enumeration Type Documentation

7.39.1.1 SpiPeriph

enum daisy::SpiPeriph

SPI peripheral enum

Enumerator

SPI_PERIPH↩ _1	SPI peripheral 1
SPI_PERIPH↩ _3	SPI peripheral 3
SPI_PERIPH↩ _6	SPI peripheral 3

7.39.1.2 SpiPin

enum daisy::SpiPin

SPI pins

Enumerator

SPI_PIN_CS	CS pin
SPI_PIN_SCK	SCK pin
SPI_PIN_MOSI	MOSI pin
SPI_PIN_MISO	MISO pin

7.40 src/per_tim.h File Reference

```
#include <stdint.h>
```

Functions

- void dsy_tim_init ()
- void dsy_tim_start ()
- uint32_t dsy_tim_get_tick ()
- void dsy_tim_delay_tick (uint32_t cnt)
- uint32_t dsy_tim_get_ms ()
- void dsy_tim_delay_ms (uint32_t cnt)
- uint32_t dsy_tim_get_us ()
- void dsy_tim_delay_us (uint32_t cnt)

7.40.1 Function Documentation

7.40.1.1 dsy_tim_delay_ms()

```
void dsy_tim_delay_ms (
            uint32_t cnt )
```

blocking delay of cnt milliseconds.

Parameters

cnt	Delay time in ms

7.40.1.2 dsy_tim_delay_tick()

```
void dsy_tim_delay_tick (
            uint32_t cnt )
```

blocking delay of cnt timer ticks.

Parameters

cnt	Number of ticks

7.40.1.3 dsy_tim_delay_us()

```
void dsy_tim_delay_us (
            uint32_t cnt )
```

blocking delay of cnt microseconds.

Parameters

cnt	Delay time in us

7.40.1.4 dsy_tim_get_ms()

```
uint32_t dsy_tim_get_ms ( )
```

These functions are converted to use milliseconds as their time base.

Returns

the number of milliseconds through the timer period.

7.40.1.5 dsy_tim_get_tick()

```
uint32_t dsy_tim_get_tick ( )
```

These functions are specific to the actual clock ticks at the timer frequency which is currently fixed at 200MHz

Returns

a number 0x00000000-0xffffffff of the current tick

7.40.1.6 dsy_tim_get_us()

```
uint32_t dsy_tim_get_us ( )
```

These functions are converted to use microseconds as their time base.

Returns

the number of microseconds through the timer period.

7.40.1.7 dsy_tim_init()

```
void dsy_tim_init ( )
```

General purpose timer for delays and general timing. initializes the TIM2 peripheral with maximum counter autoreload, and no prescalers.

7.40.1.8 dsy_tim_start()

```
void dsy_tim_start ( )
```

Starts the timer ticking.

7.41 src/per_uart.h File Reference

```
#include "daisy_core.h"
```

Classes

- class daisy::UartHandler

Macros

- #define DSY_UART_H

Variables

- const size_t daisy::kUartMaxBufferSize = 32

7.41.1 Macro Definition Documentation

7.41.1.1 DSY_UART_H

```
#define DSY_UART_H
```

macro

7.41.2 Variable Documentation

7.41.2.1 kUartMaxBufferSize

```
const size_t daisy::kUartMaxBufferSize = 32
```

Maximum Queue buffer size

7.42 src/sys_dma.h File Reference

Functions

- void dsy_dma_init (void)

7.42.1 Function Documentation

7.42.1.1 dsy_dma_init()

```
void dsy_dma_init (
            void  )
```

Initializes the Direct Memory Access Peripheral used by many internal elements of libdaisy. Initializes the DMA (specifically for the modules used within the library)

7.43 src/sys_system.h File Reference

```
#include <stdint.h>
```

Functions

- void dsy_system_init ()
- void dsy_system_jumpto (uint32_t addr)
- void dsy_system_jumptoqspi ()
- uint32_t dsy_system_getnow ()
- void dsy_system_delay (uint32_t delay_ms)

7.43.1 Detailed Description

Low level System Configuration

7.43.2 Function Documentation

7.43.2.1 dsy_system_delay()

```
void dsy_system_delay (
            uint32_t delay_ms )
```

Blocking Delay that uses the SysTick (1ms callback) to wait.

Parameters

delay_ms	Time to delay in ms

7.43.2.2 dsy_system_getnow()

```
uint32_t dsy_system_getnow ( )
```

Returns

a uint32_t value of milliseconds since the SysTick started
Note! This is a HAL_GetTick()

7.43.2.3 dsy_system_init()

```
void dsy_system_init ( )
```

Initializes Clock tree, MPU, and internal memories voltage regulators. This function *must* be called at the beginning of any program using libdaisy Higher level daisy_ files call this through the DaisySeed object.

7.43.2.4 dsy_system_jumpto()

```
void dsy_system_jumpto (
            uint32_t addr )
```

Jump to an address within the internal memory
This may not work correctly, and may not be very useful with the single sector of memory on the stm32h750**

Parameters

addr	Address to jump to

7.43.2.5 dsy_system_jumptoqspi()

```
void dsy_system_jumptoqspi ( )
```

Jumps to the first address of the external flash chip (0x90000000) If there is no code there, the chip will likely fall through to the while() loop TODO: Documentation/Loader for using external flash coming soon.

7.44 src/usbd_cdc_if.h File Reference

: Header for usbd_cdc_if.c file.

```
#include "usbd_cdc.h"
```

Typedefs

- typedef void(∗ CDC_ReceiveCallback) (uint8_t ∗buf, uint32_t ∗size)

Functions

- void CDC_Set_Rx_Callback_FS (CDC_ReceiveCallback cb)
- uint8_t CDC_Transmit_FS (uint8_t ∗Buf, uint16_t Len)
- uint8_t CDC_Transmit_HS (uint8_t ∗Buf, uint16_t Len)

Variables

- USBD_CDC_ItfTypeDef USBD_Interface_fops_FS
- USBD_CDC_ItfTypeDef USBD_Interface_fops_HS

7.44.1 Detailed Description

: Header for usbd_cdc_if.c file.

Version

: v1.0_Cube

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7.45 src/usbd_conf.h File Reference

: Header for usbd_conf.c file.

```
#include <stdio.h>
#include <stdlib.h>
#include <string.h>
#include "stm32h7xx.h"
#include "stm32h7xx_hal.h"
```

Macros

- #define USBD_MAX_NUM_INTERFACES 1U
- #define USBD_MAX_NUM_CONFIGURATION 1U
- #define USBD_MAX_STR_DESC_SIZ 512U
- #define USBD_SUPPORT_USER_STRING 0U
- #define USBD_DEBUG_LEVEL 3U
- #define USBD_LPM_ENABLED 0U
- #define USBD_SELF_POWERED 1U
- #define DEVICE_FS 0
- #define DEVICE_HS 1
- #define USBD_malloc malloc
- #define USBD_free free
- #define USBD_memset memset
- #define USBD_memcpy memcpy
- #define USBD_Delay HAL_Delay
- #define USBD_UsrLog(...)
- #define USBD_ErrLog(...)
- #define USBD_DbgLog(...)

7.45.1 Detailed Description

: Header for usbd_conf.c file.

Version

: v1.0_Cube

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7.46 src/usbd_desc.h File Reference

: Header for usbd_conf.c file.

```
#include "usbd_def.h"
```

Macros

- #define DEVICE_ID1 (UID_BASE)
- #define DEVICE_ID2 (UID_BASE + 0x4)
- #define DEVICE_ID3 (UID_BASE + 0x8)
- #define USB_SIZ_STRING_SERIAL 0x1A

Variables

- USBD_DescriptorsTypeDef HS_Desc
- USBD_DescriptorsTypeDef FS_Desc

7.46.1 Detailed Description

: Header for usbd_conf.c file.

Version

: v1.0_Cube

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7.47 src/util_bsp_sd_diskio.h File Reference

```
#include <stdint.h>
```

Classes

- struct DSY_SD_CardInfoTypeDef

Macros

- #define DSY_BSP_SD_DISKIO_H
- #define BSP_SD_CardInfo DSY_SD_CardInfoTypeDef
- #define MSD_OK ((uint8_t)0x00)
- #define MSD_ERROR ((uint8_t)0x01)
- #define MSD_ERROR_SD_NOT_PRESENT ((uint8_t)0x02)
- #define SD_TRANSFER_OK ((uint8_t)0x00)
- #define SD_TRANSFER_BUSY ((uint8_t)0x01)
- #define SD_PRESENT ((uint8_t)0x01)
- #define SD_NOT_PRESENT ((uint8_t)0x00)
- #define SD_DATATIMEOUT ((uint32_t)100000000)

Functions

- uint8_t BSP_SD_Init (void)
- uint8_t BSP_SD_ITConfig (void)
- uint8_t BSP_SD_ReadBlocks (uint32_t *pData, uint32_t ReadAddr, uint32_t NumOfBlocks, uint32_t Timeout)
- uint8_t BSP_SD_WriteBlocks (uint32_t *pData, uint32_t WriteAddr, uint32_t NumOfBlocks, uint32_t Timeout)
- uint8_t BSP_SD_ReadBlocks_DMA (uint32_t *pData, uint32_t ReadAddr, uint32_t NumOfBlocks)
- uint8_t BSP_SD_WriteBlocks_DMA (uint32_t *pData, uint32_t WriteAddr, uint32_t NumOfBlocks)
- uint8_t BSP_SD_Erase (uint32_t StartAddr, uint32_t EndAddr)
- uint8_t BSP_SD_GetCardState (void)
- void BSP_SD_GetCardInfo (DSY_SD_CardInfoTypeDef *CardInfo)
- uint8_t BSP_SD_IsDetected (void)
- void BSP_SD_AbortCallback (void)
- void BSP_SD_WriteCpltCallback (void)
- void BSP_SD_ReadCpltCallback (void)

7.47.1 Macro Definition Documentation

7.47.1.1 BSP_SD_CardInfo

```
#define BSP_SD_CardInfo DSY_SD_CardInfoTypeDef
```

&

7.47.1.2 DSY_BSP_SD_DISKIO_H

```
#define DSY_BSP_SD_DISKIO_H
```

&

7.47.1.3 MSD_ERROR

```
#define MSD_ERROR ((uint8_t)0x01)
```

&

7.47.1.4 MSD_ERROR_SD_NOT_PRESENT

```
#define MSD_ERROR_SD_NOT_PRESENT ((uint8_t)0x02)
```

&

7.47.1.5 MSD_OK

```
#define MSD_OK ((uint8_t)0x00)
```

&

7.47.1.6 SD_DATATIMEOUT

```
#define SD_DATATIMEOUT ((uint32_t)100000000)
```

&

7.47.1.7 SD_NOT_PRESENT

```
#define SD_NOT_PRESENT ((uint8_t)0x00)
```

&

7.47.1.8 SD_PRESENT

```
#define SD_PRESENT ((uint8_t)0x01)
```

&

7.47.1.9 SD_TRANSFER_BUSY

```
#define SD_TRANSFER_BUSY ((uint8_t)0x01)
```

&

7.47.1.10 SD_TRANSFER_OK

```
#define SD_TRANSFER_OK ((uint8_t)0x00)
```

&

7.47.2 Function Documentation

7.47.2.1 BSP_SD_AbortCallback()

```
void BSP_SD_AbortCallback (
            void  )
```

These functions can be modified in case the current settings (e.g. DMA stream) need to be changed for specific application needs /n

Abort the callback

7.47.2.2 BSP_SD_Erase()

```
uint8_t BSP_SD_Erase (
            uint32_t StartAddr,
            uint32_t EndAddr )
```

Erase a section of memory

Parameters

StartAddr	Address to start erasing at
EndAddr	Address to stop erasing at

Returns

card state, ERROR, etc.

7.47.2.3 BSP_SD_GetCardInfo()

```
void BSP_SD_GetCardInfo (
            DSY_SD_CardInfoTypeDef * CardInfo )
```

Parameters

∗*CardInfo*	Pointer to write card info to

Parameters

CardInfo	&

7.47.2.4 BSP_SD_GetCardState()

```
uint8_t BSP_SD_GetCardState (
            void  )
```

Returns

card state, ERROR, etc.

7.47.2.5 BSP_SD_Init()

```
uint8_t BSP_SD_Init (
            void  )
```

Returns

card state, ERROR, etc.

7.47.2.6 BSP_SD_IsDetected()

```
uint8_t BSP_SD_IsDetected (
            void  )
```

Returns

Is card detected

7.47.2.7 BSP_SD_ITConfig()

```
uint8_t BSP_SD_ITConfig (
            void  )
```

Returns

card state, ERROR, etc.

7.47.2.8 BSP_SD_ReadBlocks()

```
uint8_t BSP_SD_ReadBlocks (
            uint32_t * pData,
            uint32_t ReadAddr,
            uint32_t NumOfBlocks,
            uint32_t Timeout )
```

Parameters

∗pData	&
ReadAddr	Address to read from
NumOfBlocks	Number of blocks to be read
Timeout	Timeout len in ms

Returns

OK ERROR, etc.

7.47.2.9 BSP_SD_ReadBlocks_DMA()

```
uint8_t BSP_SD_ReadBlocks_DMA (
            uint32_t * pData,
            uint32_t ReadAddr,
            uint32_t NumOfBlocks )
```

No timeout

Parameters

∗pData	&
ReadAddr	Address to read from
NumOfBlocks	Number of blocks to be read

Returns

card state, ERROR, etc.

7.47.2.10 BSP_SD_ReadCpltCallback()

```
void BSP_SD_ReadCpltCallback (
            void  )
```

Write complete callback

7.47.2.11 BSP_SD_WriteBlocks()

```
uint8_t BSP_SD_WriteBlocks (
            uint32_t * pData,
            uint32_t WriteAddr,
            uint32_t NumOfBlocks,
            uint32_t Timeout )
```

Parameters

∗pData	&
WriteAddr	Address to write to
NumOfBlocks	Number of blocks to be written
Timeout	Timeout len in ms

Returns

card state, ERROR, etc.

7.47.2.12 BSP_SD_WriteBlocks_DMA()

```
uint8_t BSP_SD_WriteBlocks_DMA (
             uint32_t * pData,
             uint32_t WriteAddr,
             uint32_t NumOfBlocks )
```

No timeout

Parameters

∗pData	&
WriteAddr	Address to write to
NumOfBlocks	Number of blocks to be read

Returns

card state, ERROR, etc.

7.47.2.13 BSP_SD_WriteCpltCallback()

```
void BSP_SD_WriteCpltCallback (
             void  )
```

Read complete callback

7.48 src/util_color.h File Reference

```
#include <stdint.h>
```

Classes

- class daisy::Color

Macros

- #define DSY_COLOR_H

7.48.1 Macro Definition Documentation

7.48.1.1 DSY_COLOR_H

```
#define DSY_COLOR_H
```

I'd like for it to be easy and not `Color::PresetColor::Foo`- There's no way to change a color once its been created (without unintuitively reiniting it).

7.49 src/util_hal_map.h File Reference

```
#include "stm32h7xx_hal.h"
#include "daisy_core.h"
#include "per_i2c.h"
```

Functions

- GPIO_TypeDef * dsy_hal_map_get_port (dsy_gpio_pin *p)
- uint16_t dsy_hal_map_get_pin (dsy_gpio_pin *p)
- I2C_HandleTypeDef * dsy_hal_map_get_i2c (dsy_i2c_handle *p)

Variables

- I2C_HandleTypeDef hi2c1
- I2C_HandleTypeDef hi2c2
- I2C_HandleTypeDef hi2c3
- I2C_HandleTypeDef hi2c4

7.49.1 Function Documentation

7.49.1.1 dsy_hal_map_get_i2c()

```
I2C_HandleTypeDef* dsy_hal_map_get_i2c (
            dsy_i2c_handle * p )
```

Parameters

*p	dsy_i2c_handle to get

Returns

The I2C_HandleTypeDef for the given *p

7.49.1.2 dsy_hal_map_get_pin()

```
uint16_t dsy_hal_map_get_pin (
            dsy_gpio_pin * p )
```

Parameters

*p	Pin pin to get

Returns

HAL GPIO Pin as used in the HAL from a dsy_gpio_pin input.

7.49.1.3 dsy_hal_map_get_port()

```
GPIO_TypeDef* dsy_hal_map_get_port (
            dsy_gpio_pin * p )
```

Parameters

*p	Pin pin to get

Returns

HAL GPIO_TypeDef as used in the HAL from a dsy_gpio_pin input.

7.49.2 Variable Documentation

7.49.2.1 hi2c1

```
I2C_HandleTypeDef hi2c1
```

global structs, and helper functions for interfacing with the stm32 HAL library while it remains a dependancy. This file should only be included from source files (c/cpp) Including it from a header within libdaisy would expose the entire HAL to the users. This should be an option for users, but should not be required.externs of HAL handles...

7.49.2.2 hi2c2

```
I2C_HandleTypeDef hi2c2
```

externs of HAL handles...

7.49.2.3 hi2c3

```
I2C_HandleTypeDef hi2c3
```

externs of HAL handles...

7.49.2.4 hi2c4

```
I2C_HandleTypeDef hi2c4
```

externs of HAL handles...

7.50 src/util_ringbuffer.h File Reference

```
#include <algorithm>
```

Classes

- class daisy::RingBuffer< T, size >
- class daisy::RingBuffer< T, 0 >

7.51 src/util_unique_id.h File Reference

```
#include "daisy_core.h"
```

Functions

- void dsy_get_unique_id (uint32_t *w0, uint32_t *w1, uint32_t *w2)

7.51.1 Function Documentation

7.51.1.1 dsy_get_unique_id()

```
void dsy_get_unique_id (
             uint32_t * w0,
             uint32_t * w1,
             uint32_t * w2 )
```

Returns 96-bit Unique ID of the MCU

Author

shensley

Date

May 2020 fills the three pointer arguments with the unique ID of the MCU.

Parameters

*w0	First pointer
*w1	Second pointer
*w2	Third pointer

7.52 src/util_wav_format.h File Reference

```
#include <stdint.h>
```

Classes

- struct WAV_FormatTypeDef

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