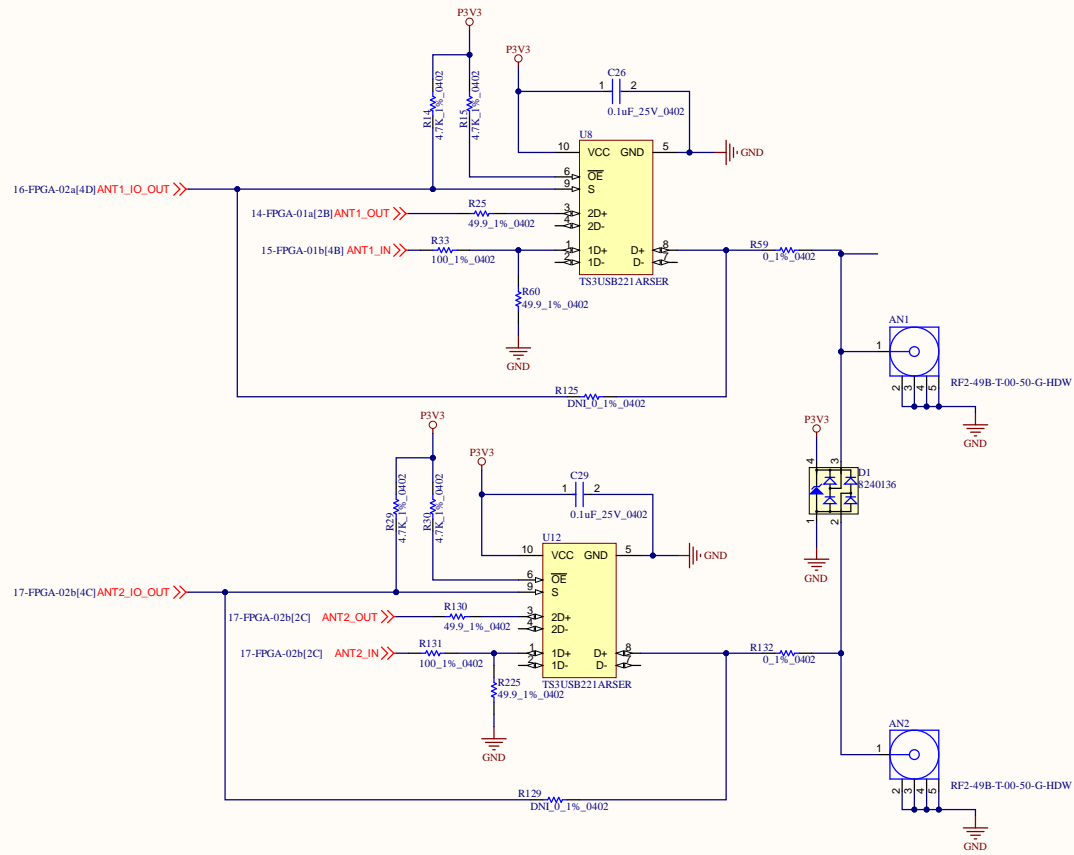


Cannot open file
C:\Users\beta\Desktop\T
imecard
Logo\TIMECARD.jpg.
File does not exist.

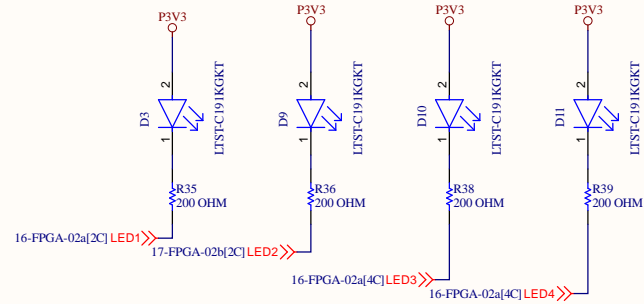
REV **P3** DATE 09/14/21
timingcard.com

SHEET **1** OF **22**

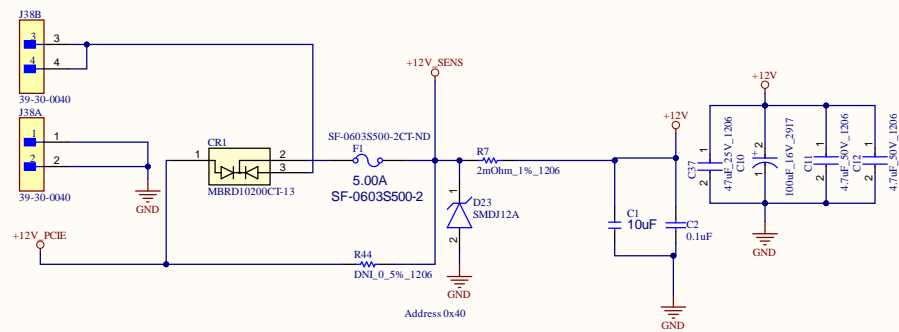
INPUT/OUTPUT SMA

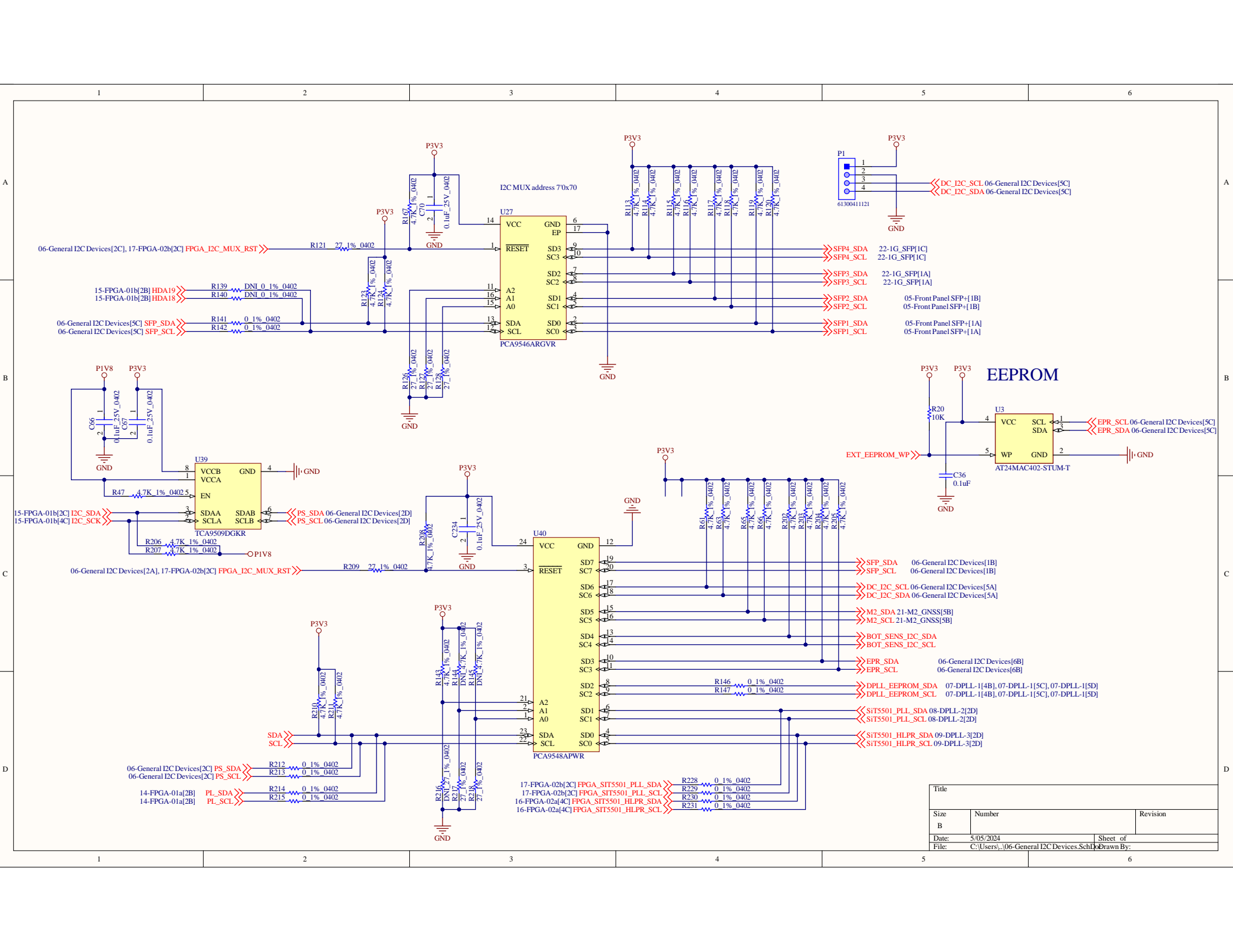


SYSTEM STATUS LEDS



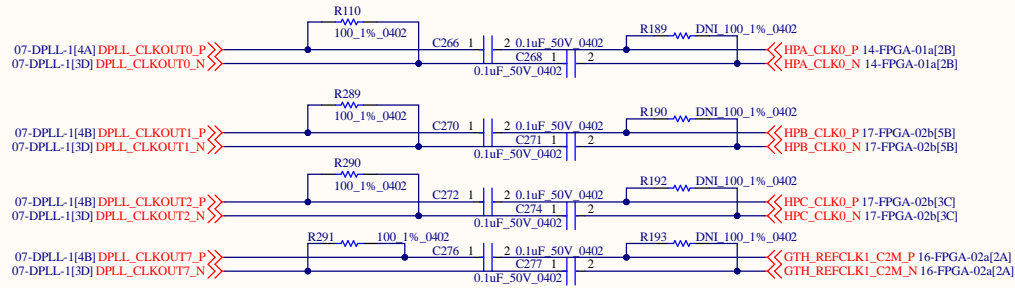
Title		
Size B	Number	Revision
Date:	5/05/2024	Sheet of
File:	C:\Users\103-USER\IO_STATUS.Sch	Drawn By:



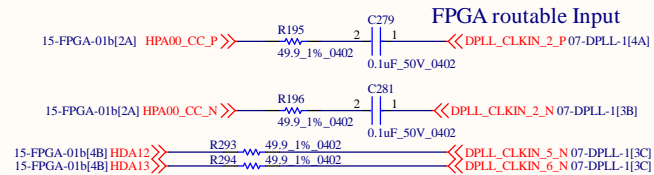


Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\...\06-General I2C Devices.SchDoc	Drawn By:

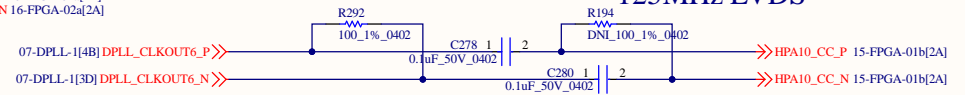
AC Coupled LVDS Outputs



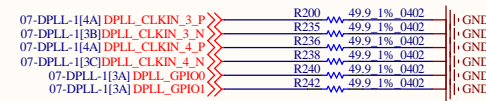
FPGA routable Input



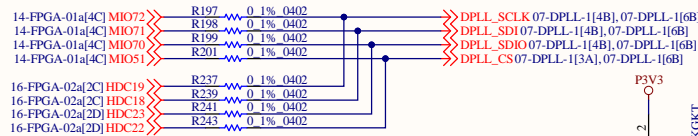
125MHz LVDS



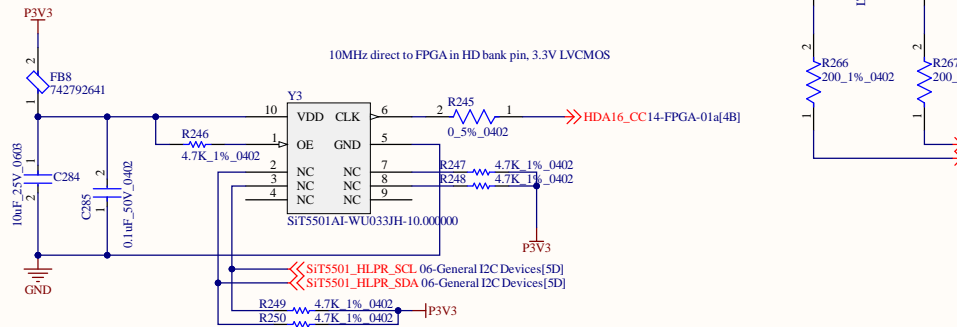
Expose these



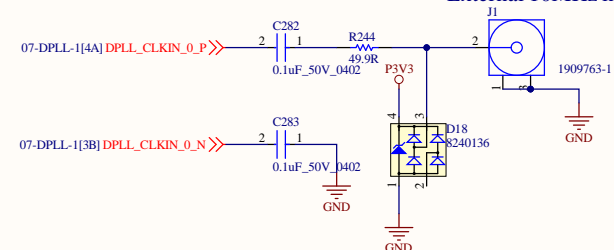
Allow MIO and HDIO to control, MIO default



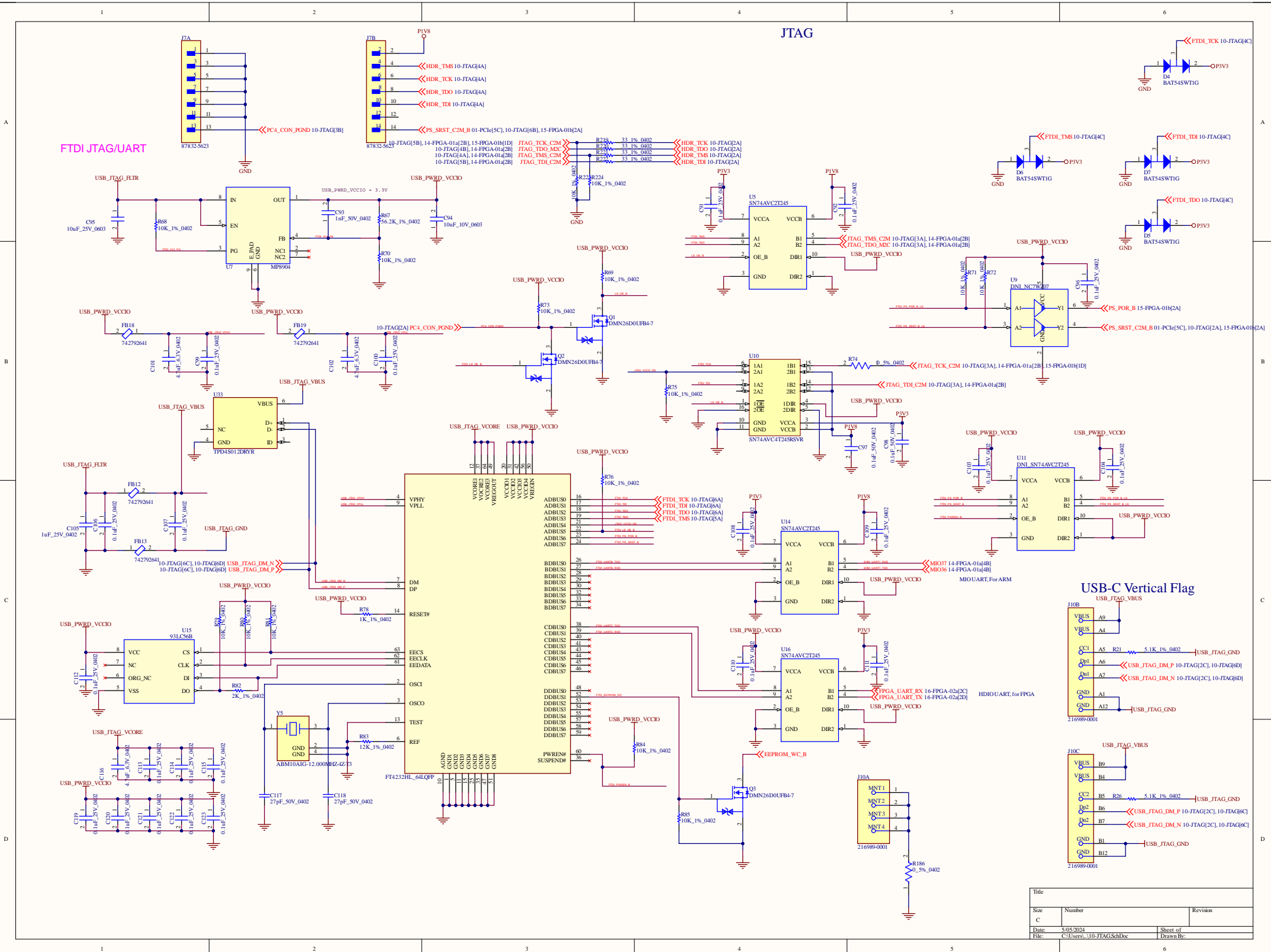
10MHz direct to FPGA in HD bank pin, 3.3V LVCMOS



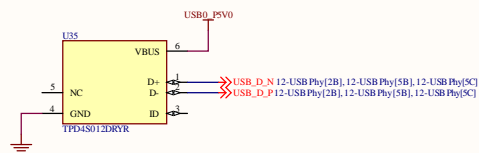
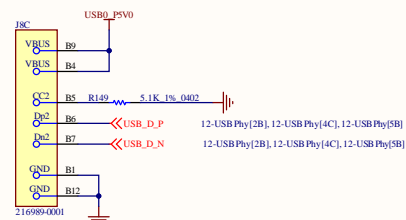
External 10MHz input



Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\...\09-DPLL-3.SchDoc	Drawn By:

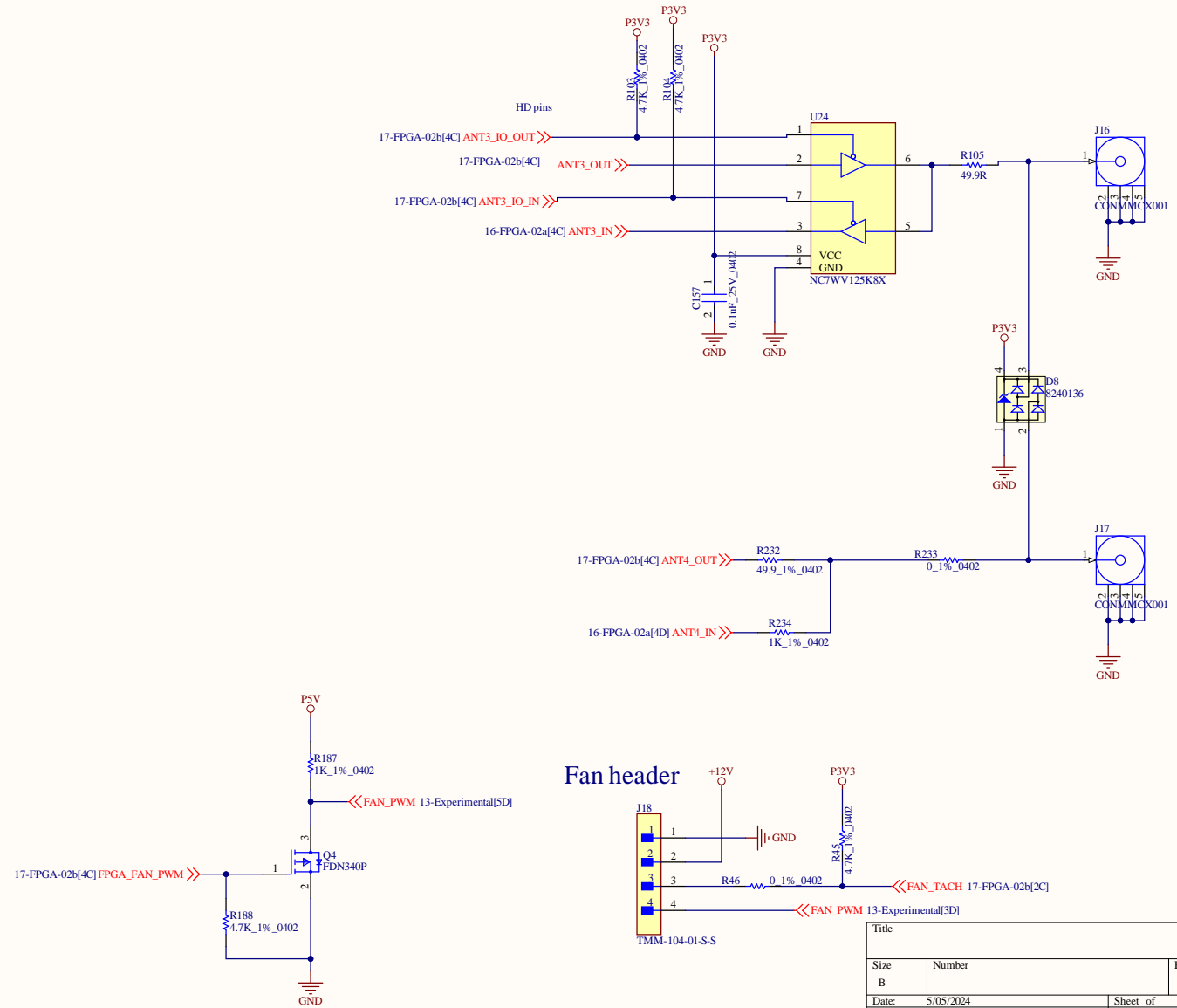


Title		
Size	Number	Revision
C		
Date:	5/05/2024	Sheet of
File:	C:\Users\...110-JTAG\SchDoc	Drawn By:



Title		
Size C	Number	Revision
Date: 5/05/2024	File: C:\Users\...12-USB Phys.Sch.Doc	Sheet of Drawn By:

Experiment: Reserve UFL on board



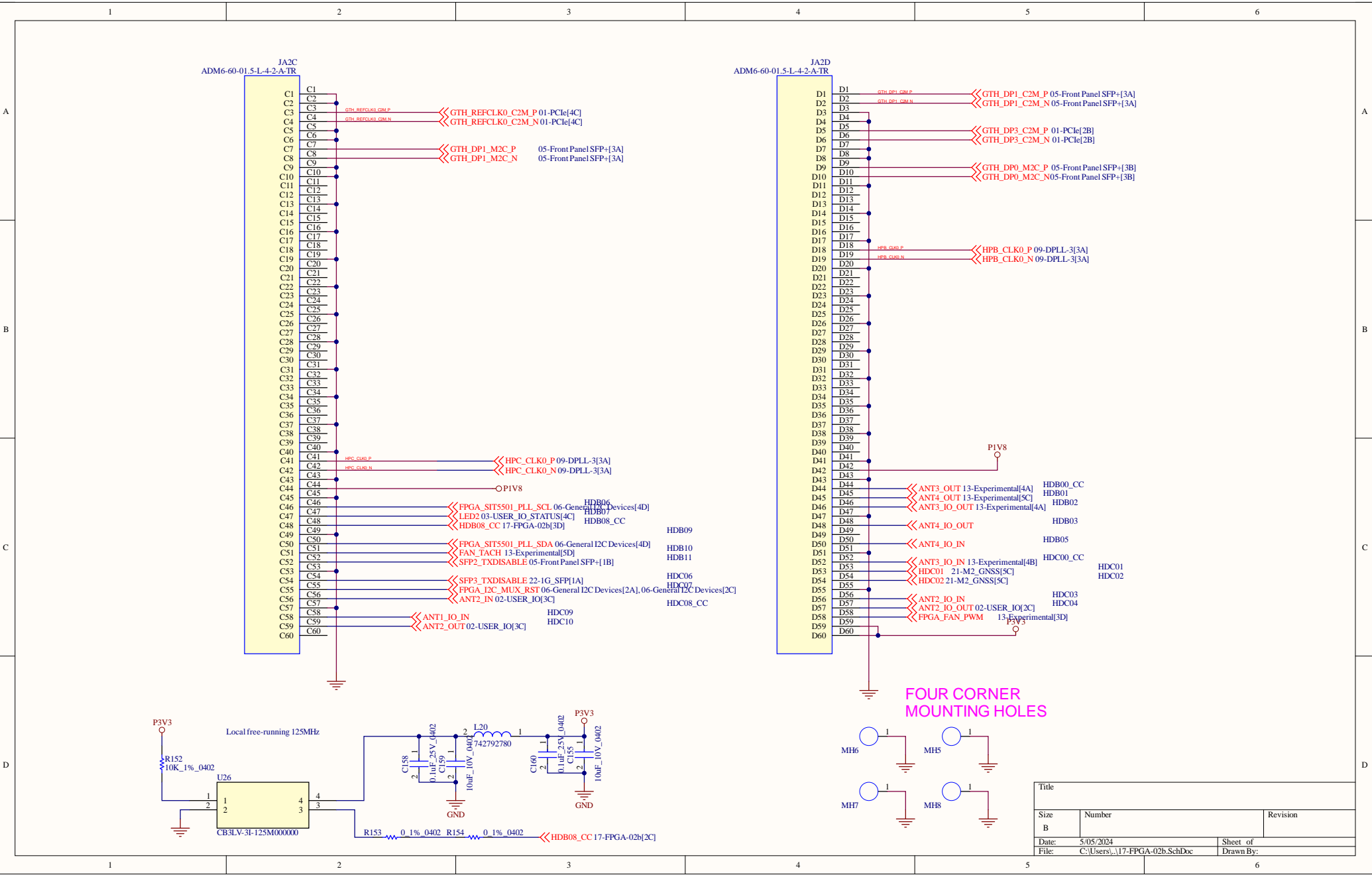
Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\113-Experimental\SchDoc	Drawn By:

SOM240_2 CONNECTOR

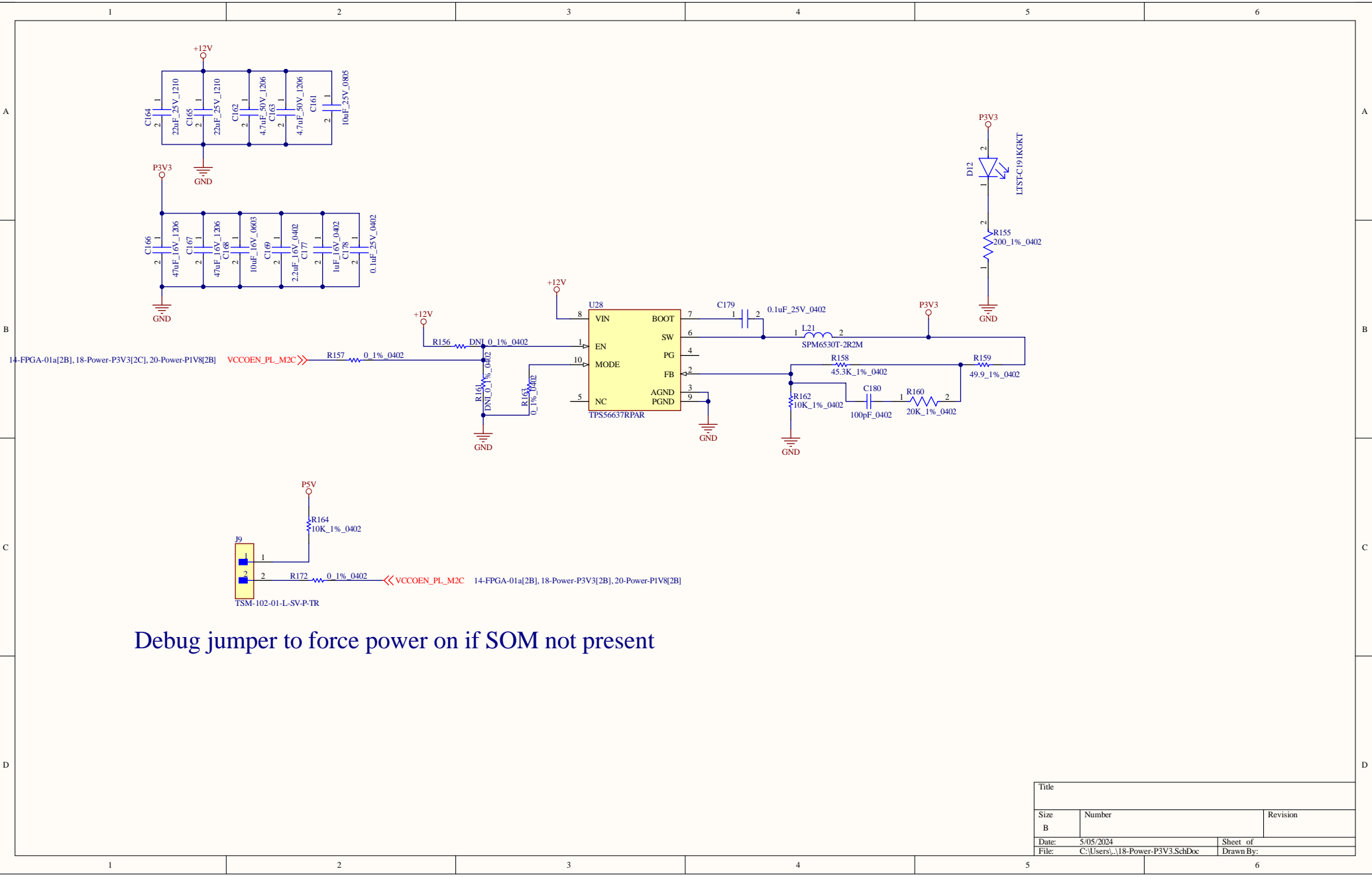
612-ADM6-60-01.5-L-4-2-A-TR-ND
JA2A
ADM6-60-01.5-L-4-2-A-TR

JA2B
ADM6-60-01.5-L-4-2-A-TR

Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\...\16-FPGA-02a.SchDoc	Drawn By:

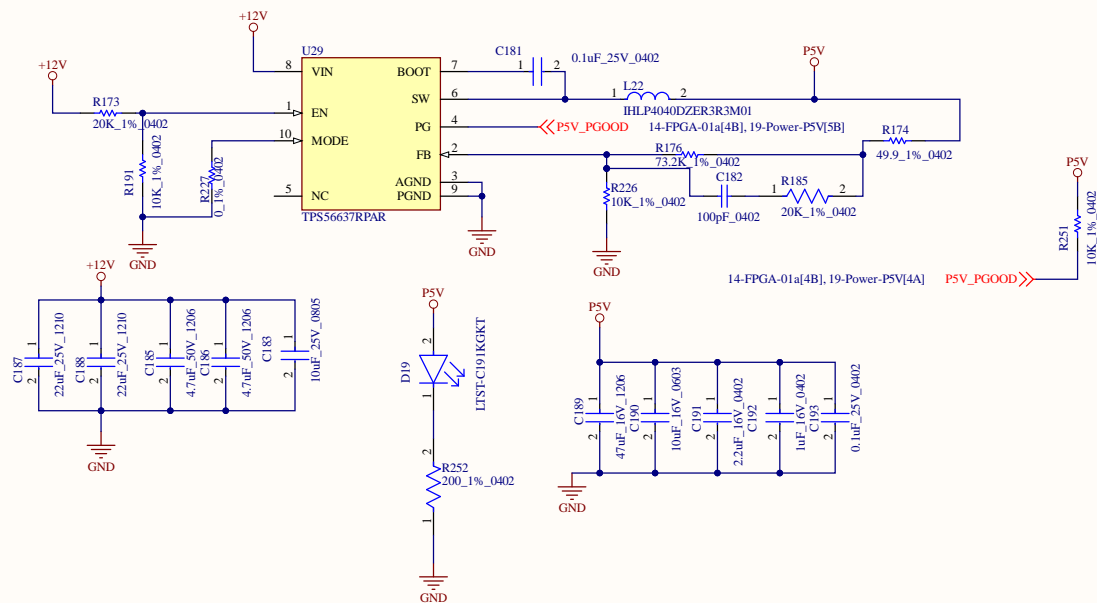


Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\...\17-FPGA-02b.SchDoc	Drawn By:

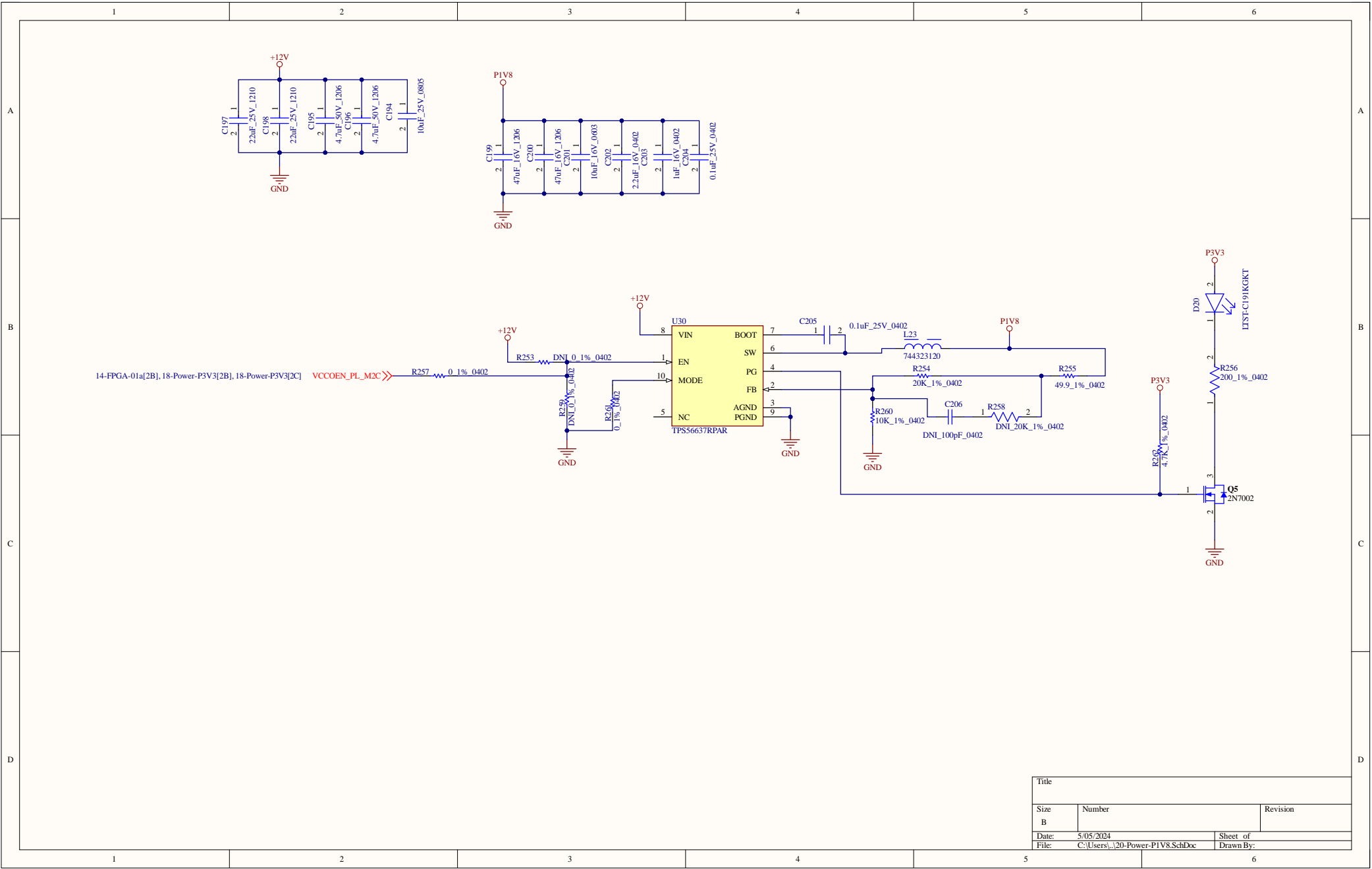


Debug jumper to force power on if SOM not present

Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\18-Power-P3V3\SchDoc	Drawn By:

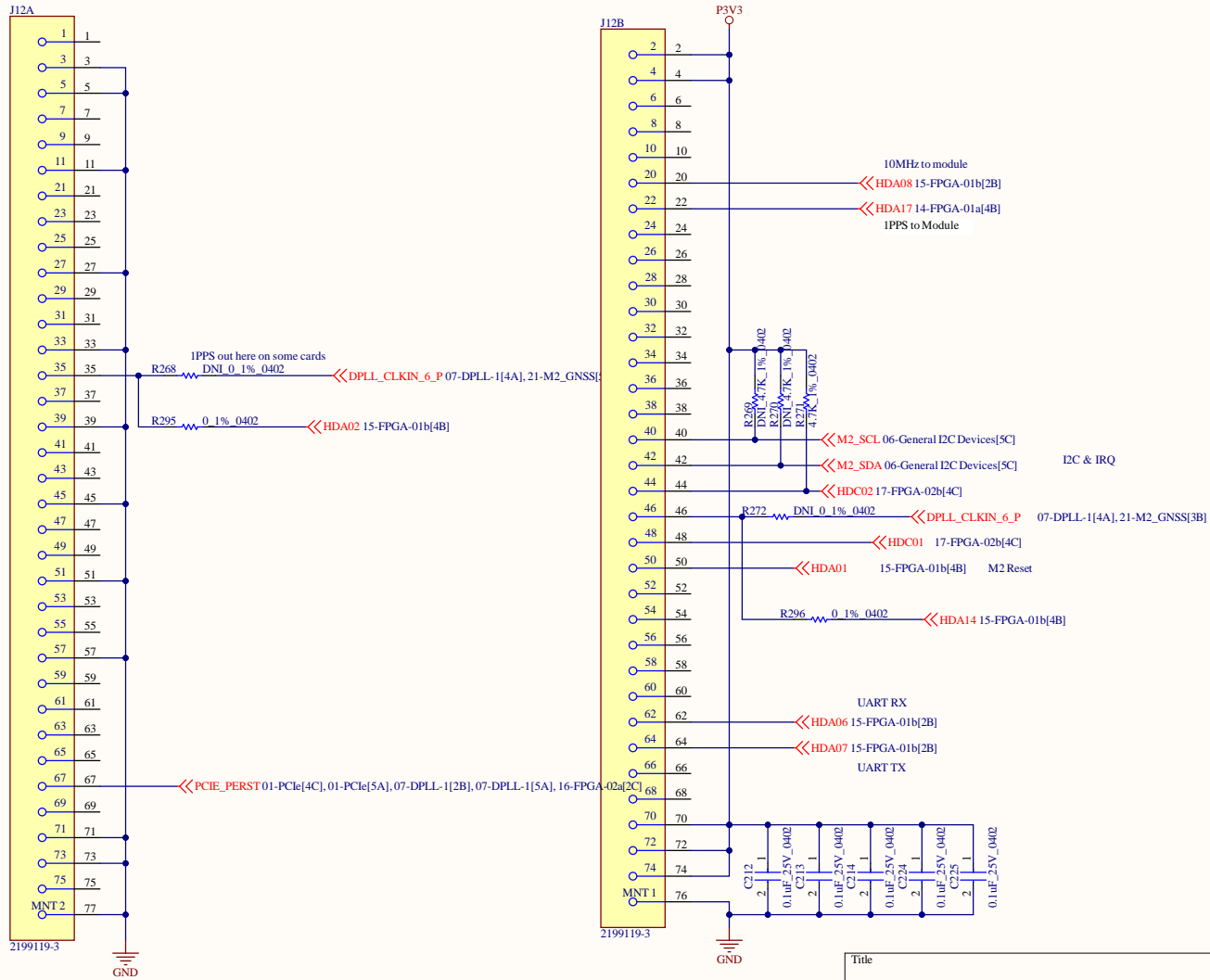


Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\...\19-Power-PSV.SchDoc	Drawn By:



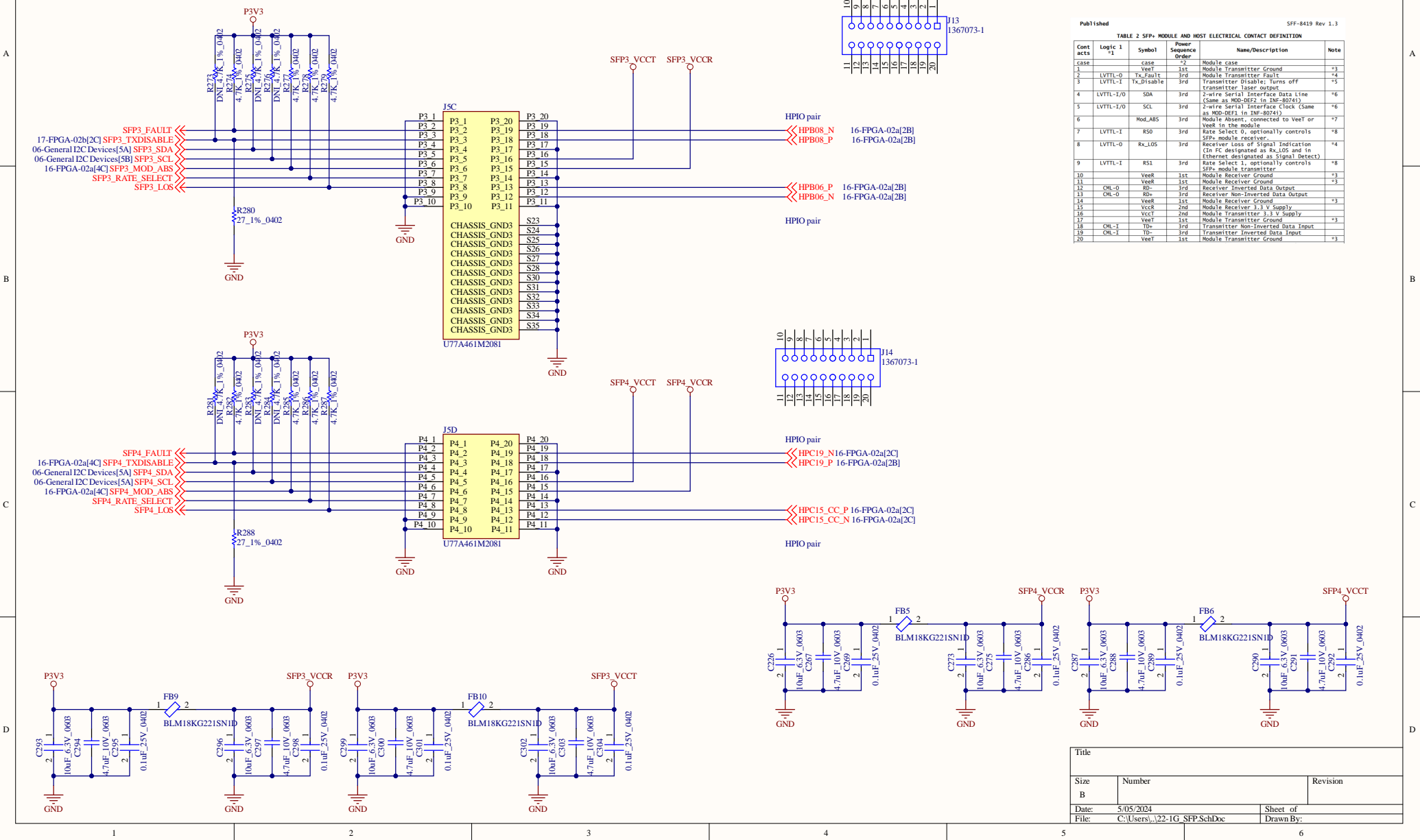
Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\j20-Power-P1V8\SchDoc	Drawn By:

Socket 2 Key B PCIe-based WWAN Adapter Pinout , table 3-21 in M.2 spec



Title		
Size B	Number	Revision
Date: 5/05/2024	Sheet of	
File: C:\Users\j21-M2_GNSS.SchDoc	Drawn By:	

Experiment: 1G SFP driven from HPIO instead of GTH



Title		
Size	Number	Revision
B		
Date:	5/05/2024	Sheet of
File:	C:\Users\122-1G_SFP.SchDoc	Drawn By: