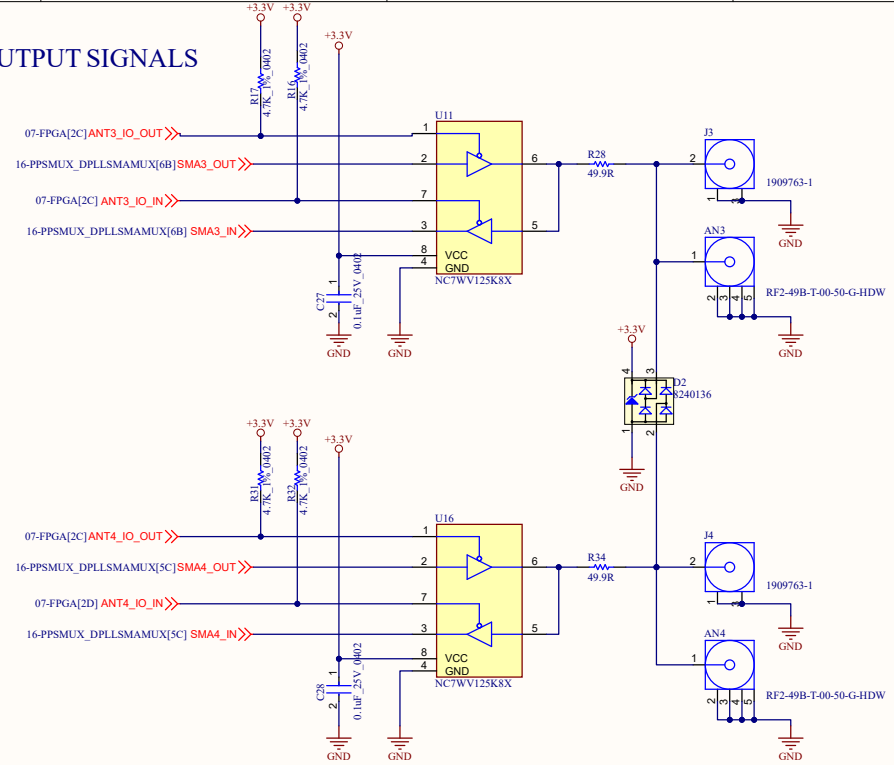
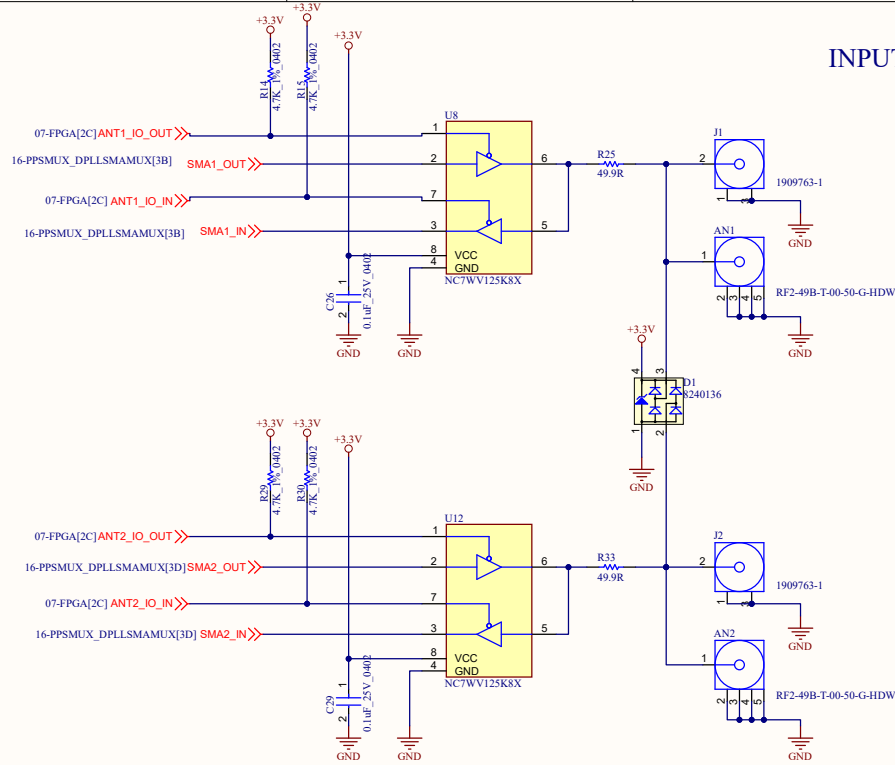
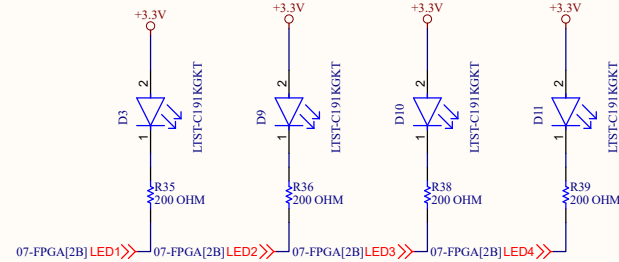


# INPUT/OUTPUT SIGNALS

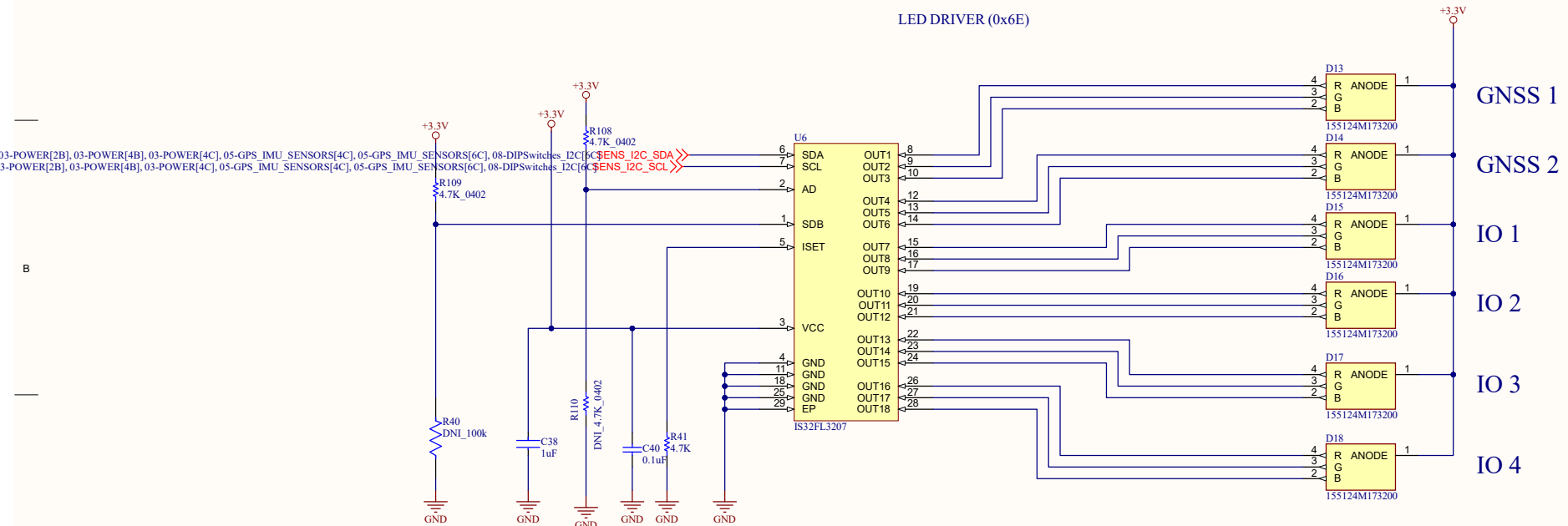


## SYSTEM STATUS LEDs

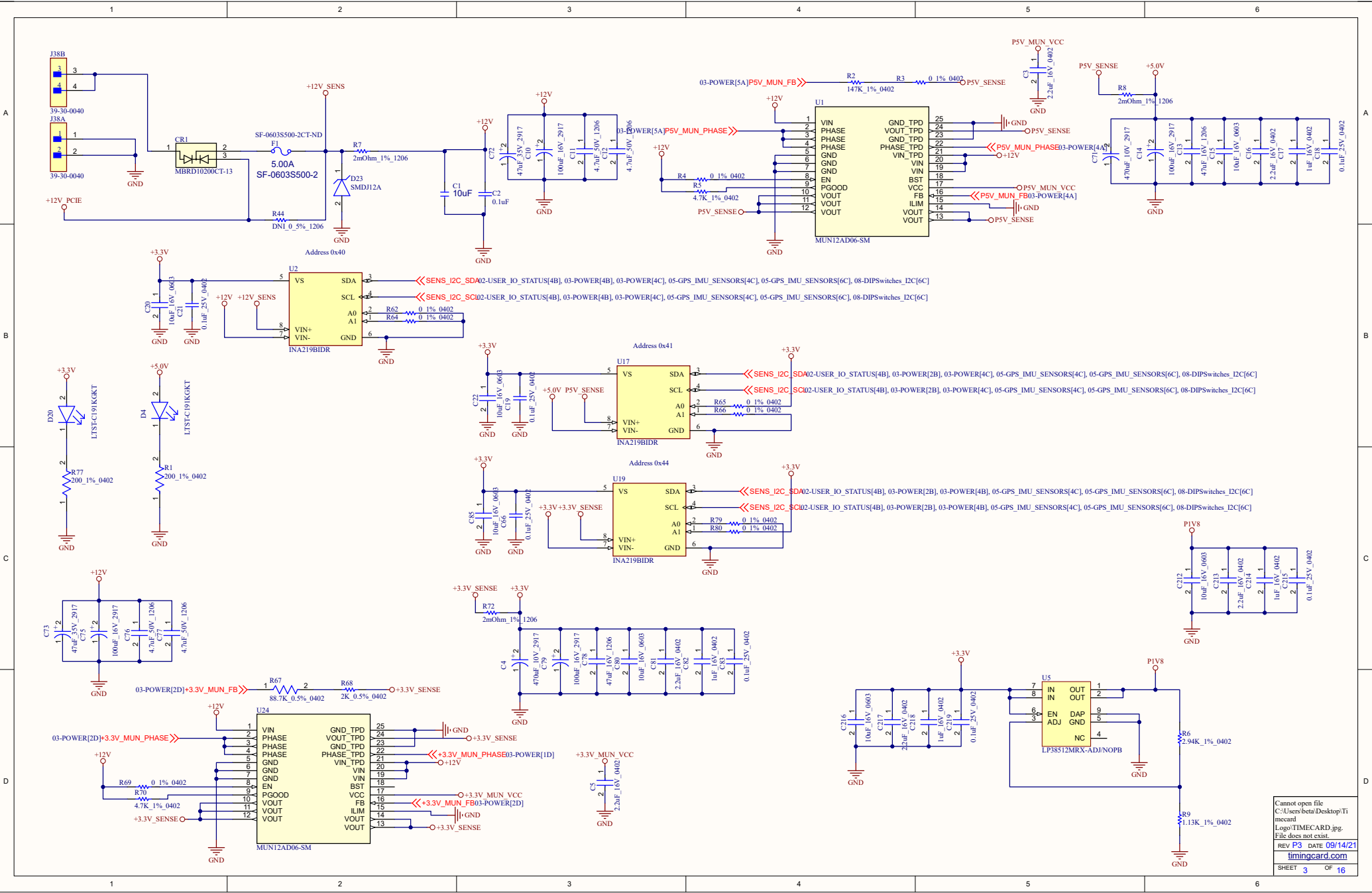


## IO STATUS LEDs

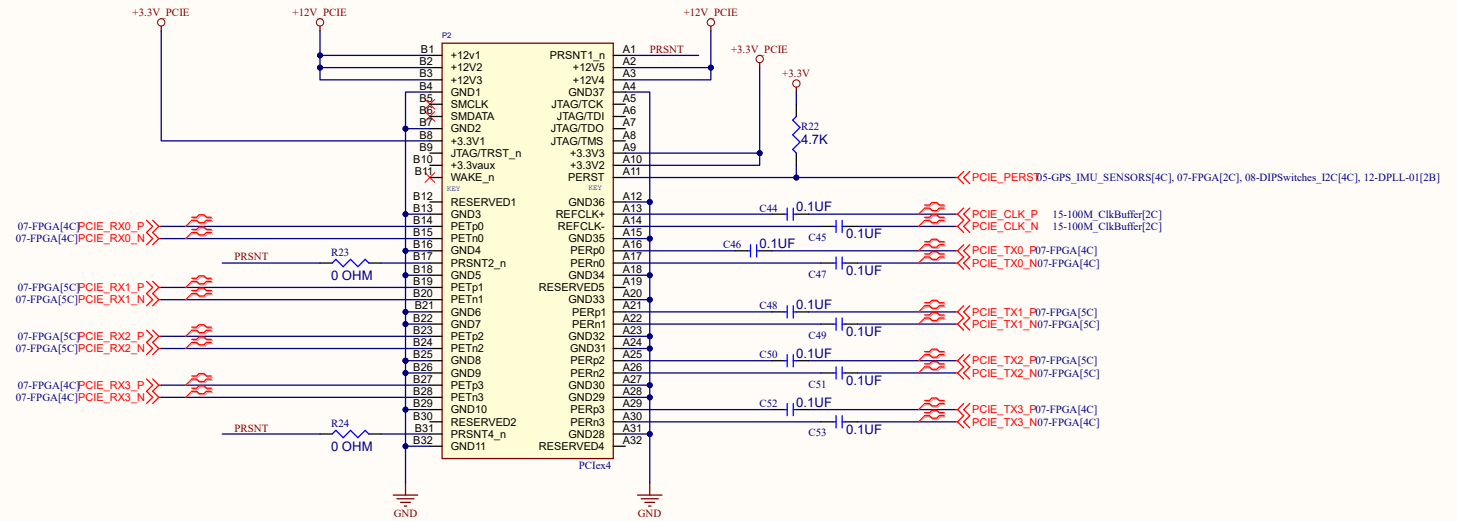
LED DRIVER (0x6E)



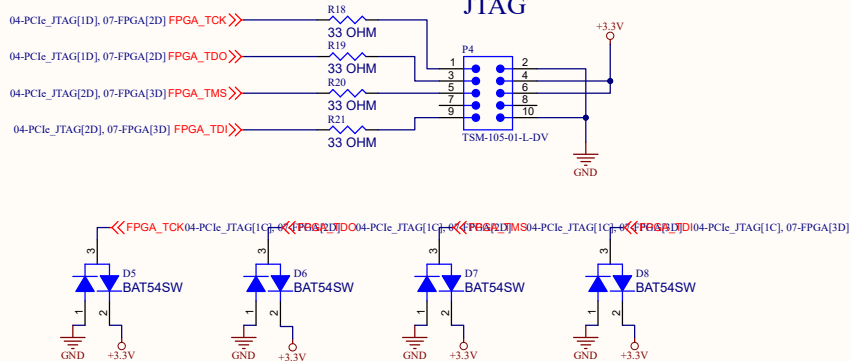
Title		
Size B	Number	Revision
Date:	9/07/2023	Sheet of
File:	C:\Users\102-USER\IO_STATUS_Sch	Drawn By:

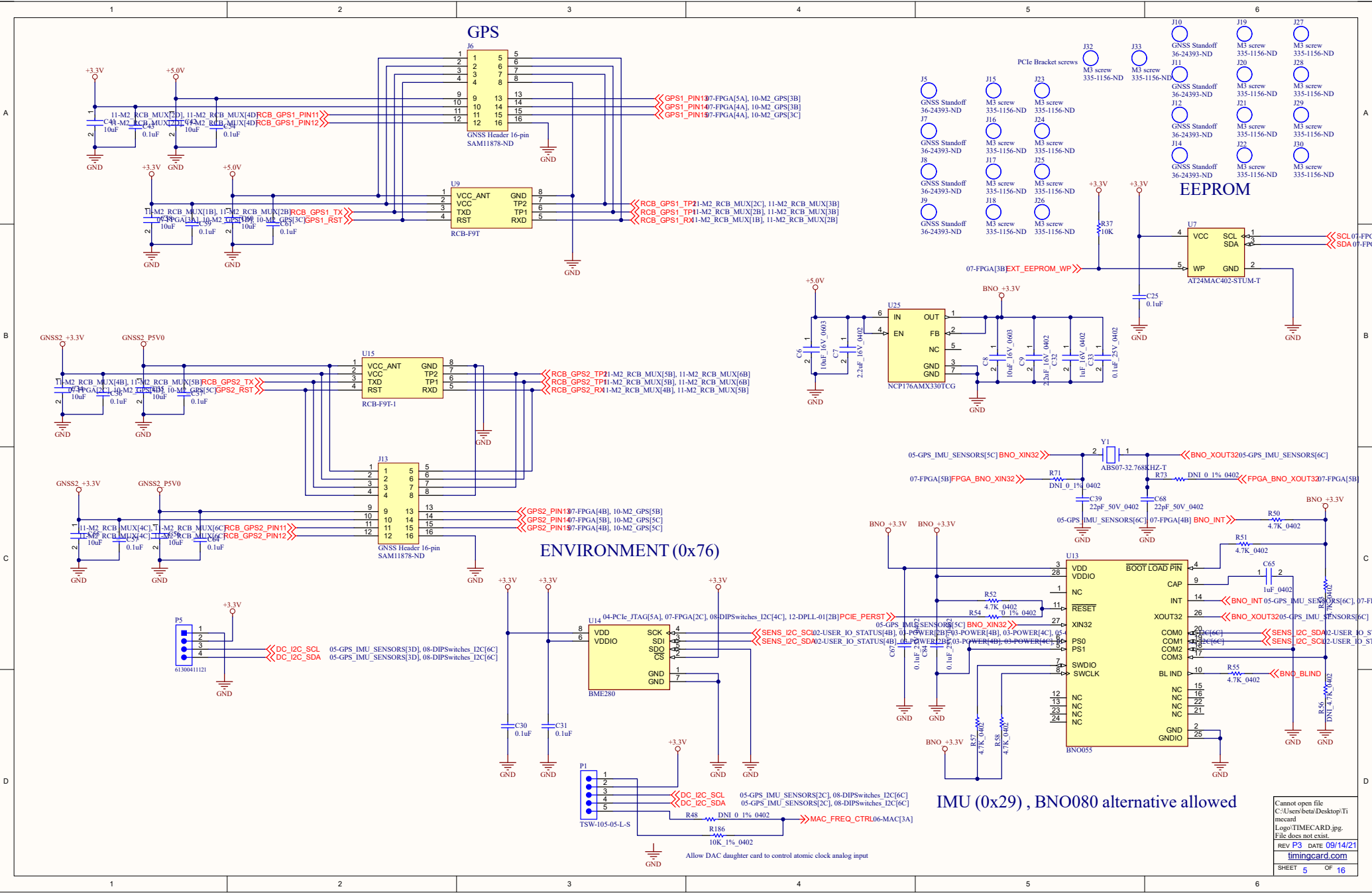


## PCIe Connector

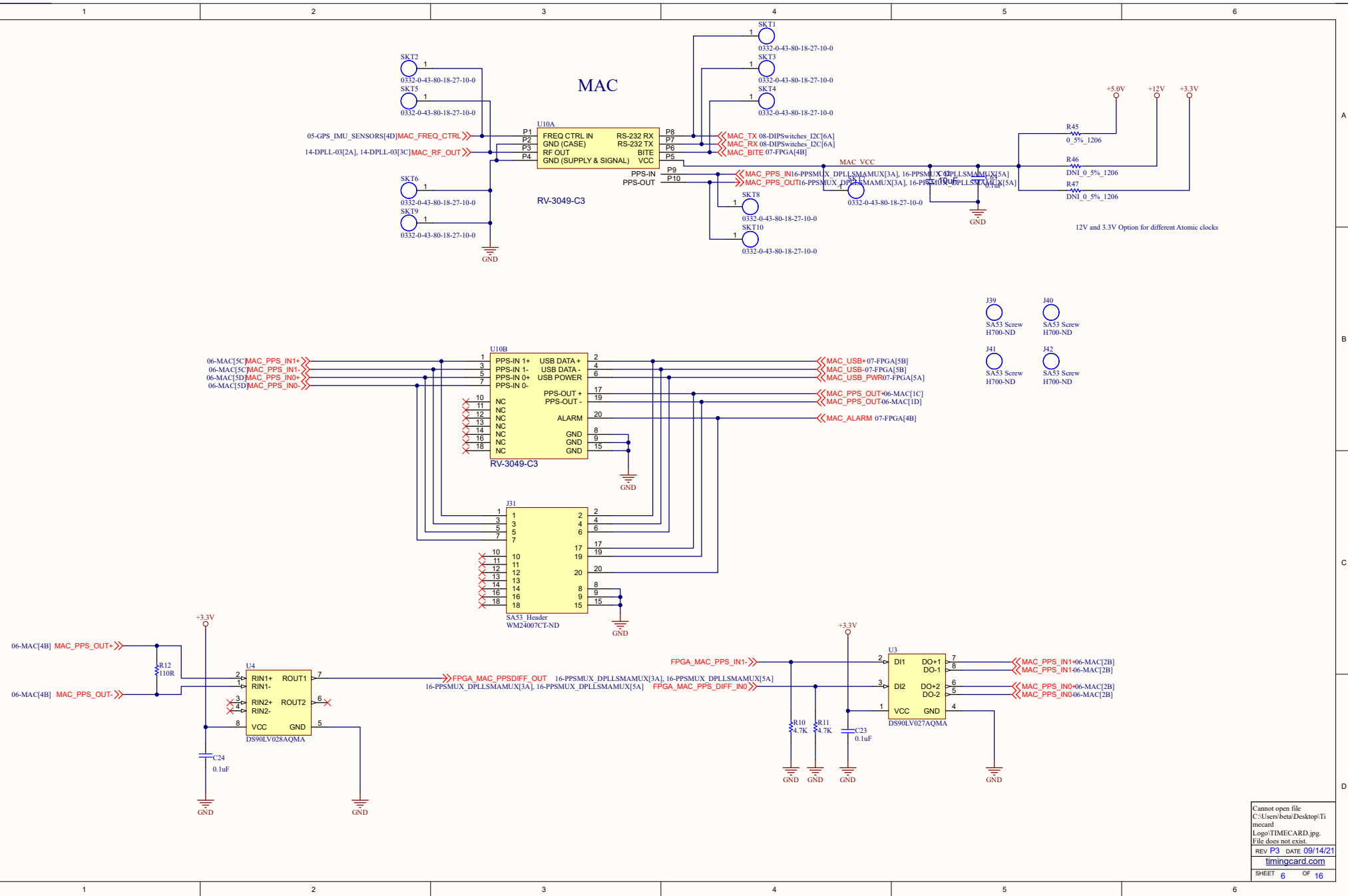


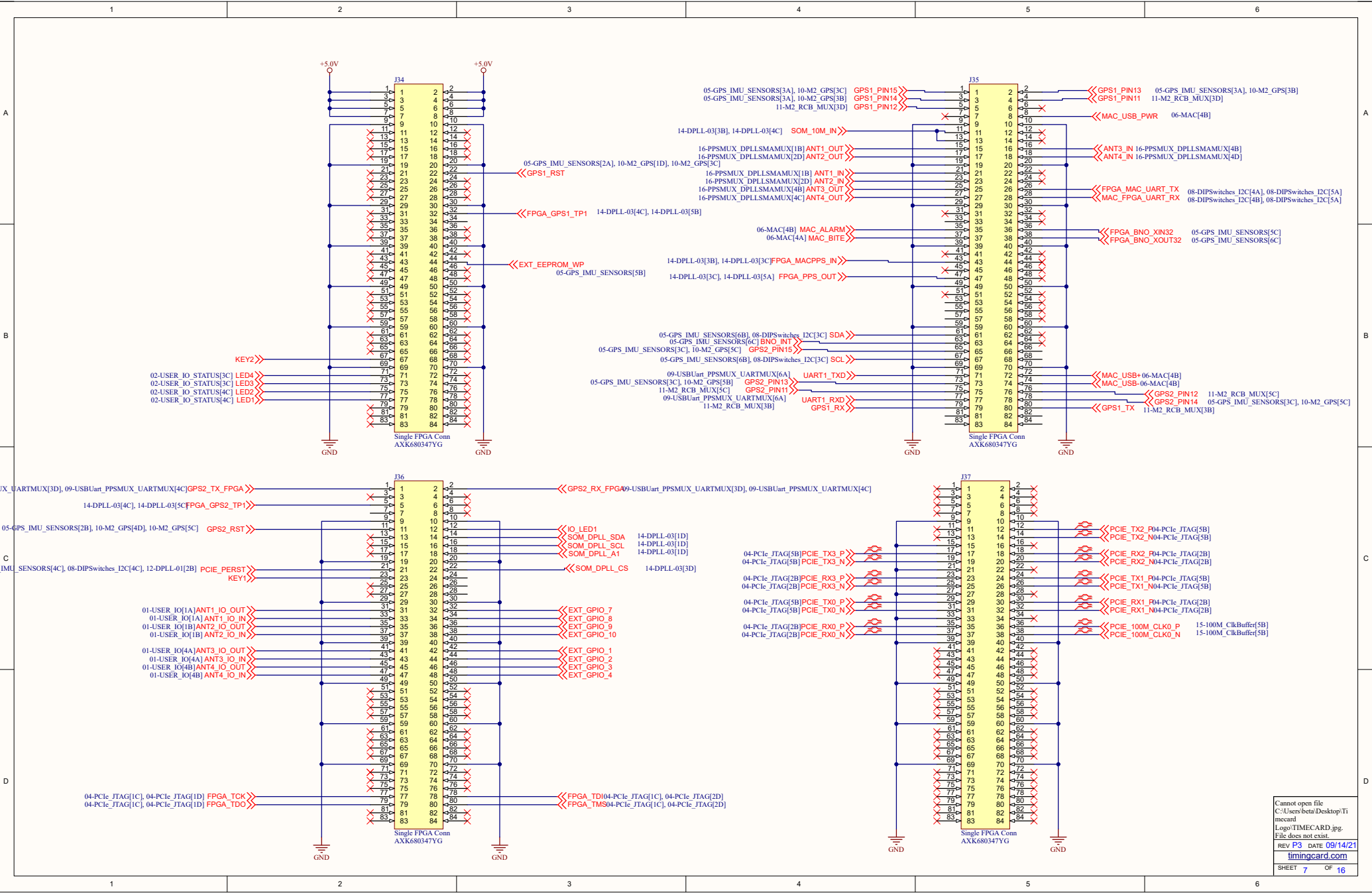
## JTAG

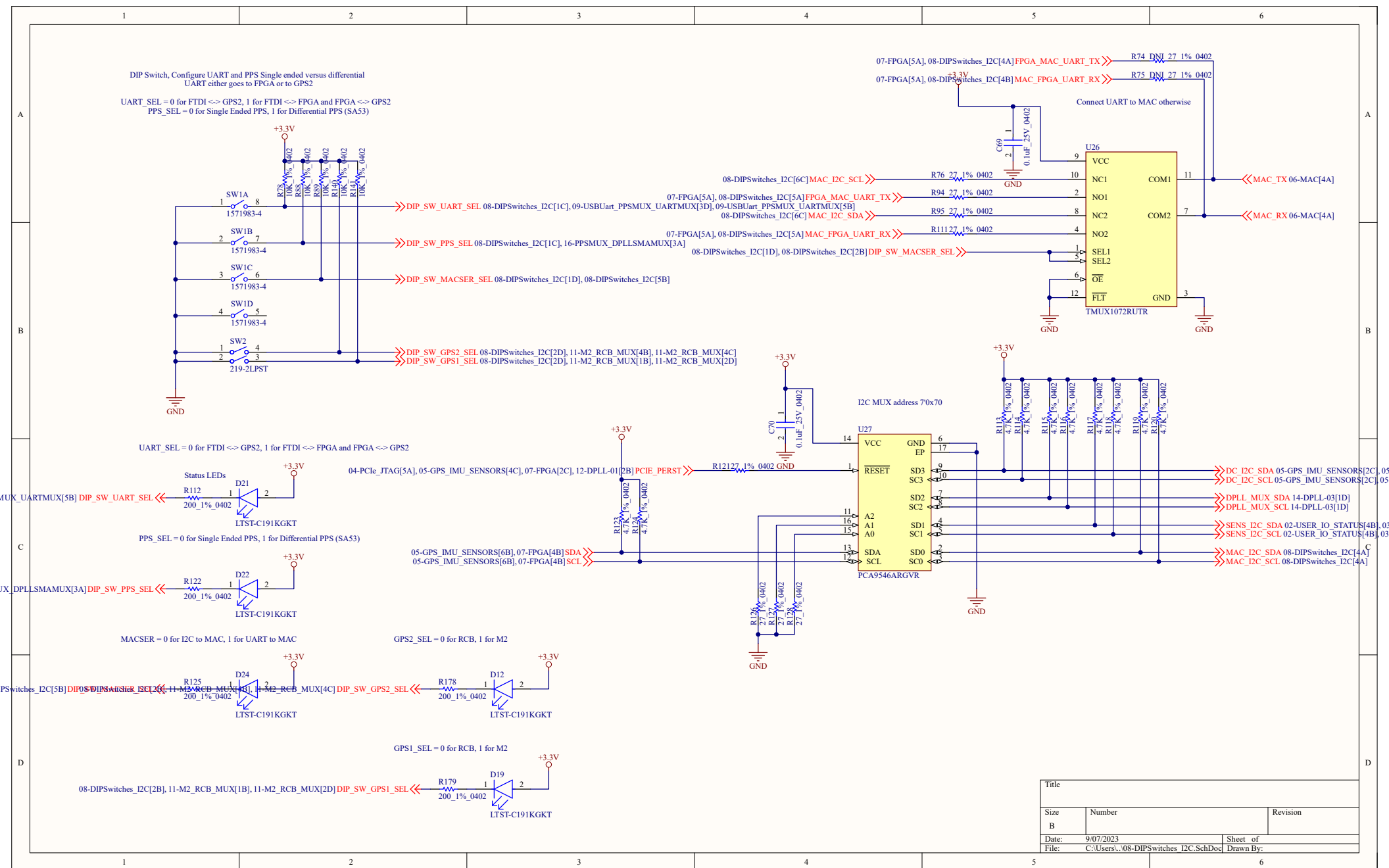




Cannot open file  
C:\Users\beta\Desktop\Ti  
meCard  
Logo\TIMECARD.jpg  
File does not exist.  
REV P3 DATE 09/14/21  
timingcard.com  
SHEET 5 OF 16



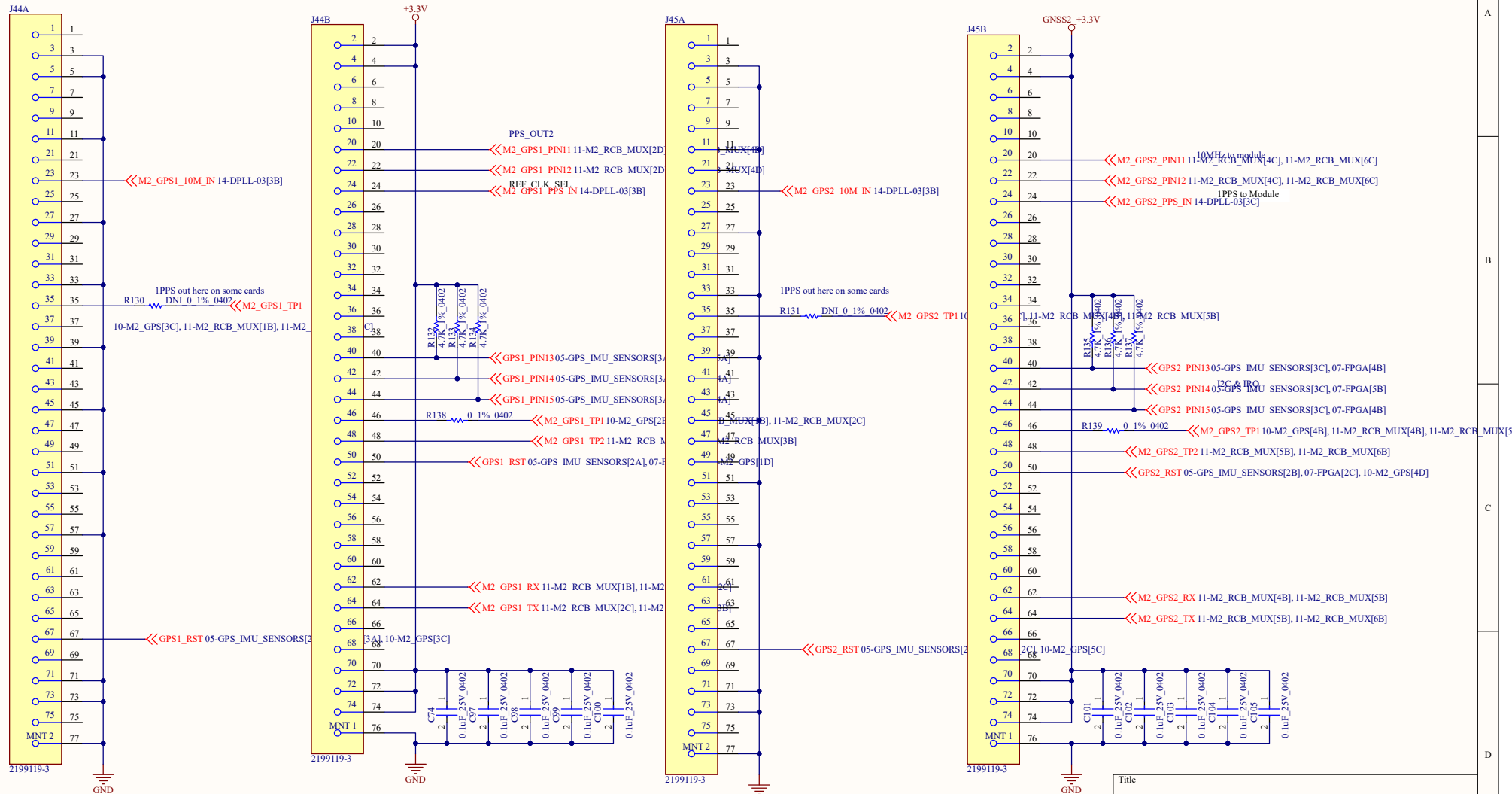




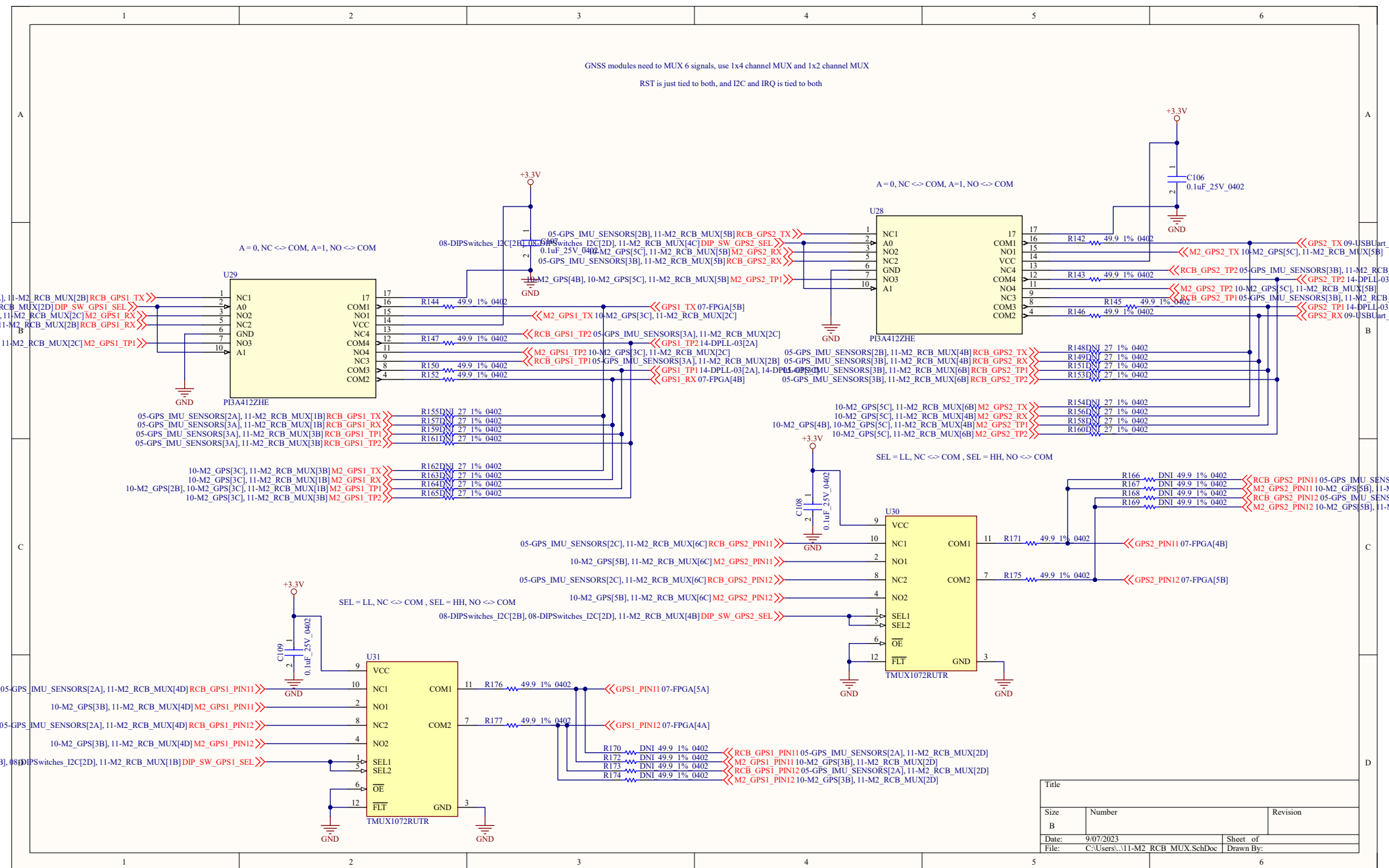




# Socket 2 Key B PCIe-based WWAN Adapter Pinout , table 3-21 in M.2 spec



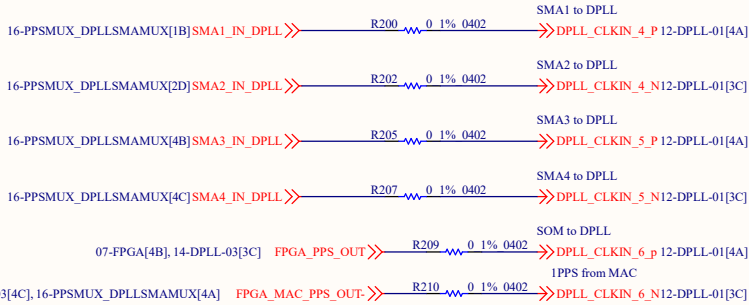
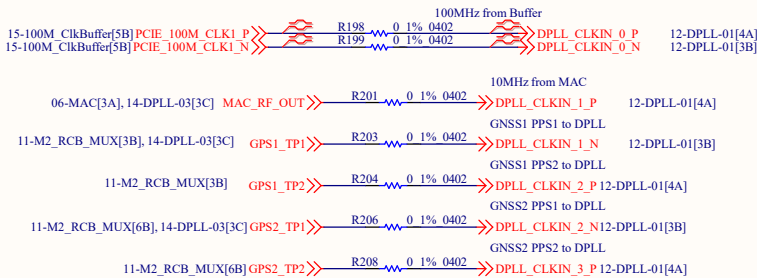
Title		
Size B	Number	Revision
Date: 9/07/2023	Sheet of	
File: C:\Users\110-M2_GPS\SchDoc	Drawn By:	



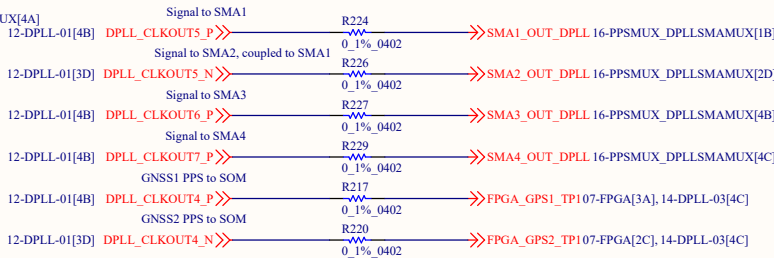
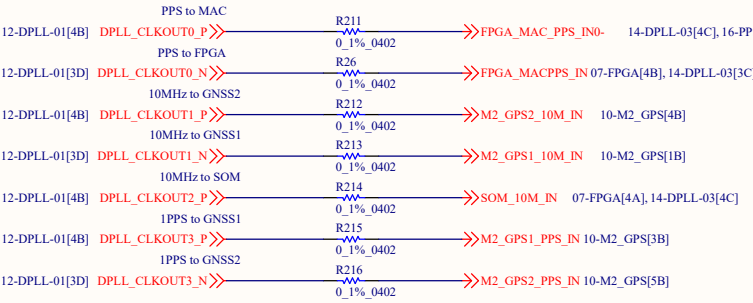




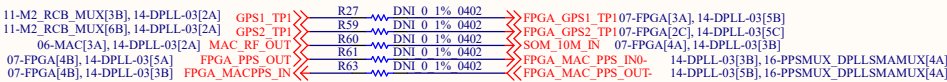
DPLL Clock Inputs



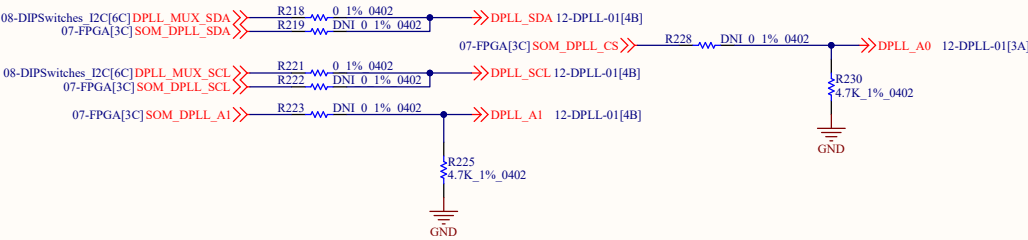
DPLL Clock Outputs



DPLL Bypass options, if want to DNP DPLL and use Time Card as before



Options for SPI connections, and for direct i2c



Title		
Size B	Number	Revision
Date:	9/07/2023	Sheet of
File:	C:\Users\114-DPLL-03.SchDoc	Drawn By:

