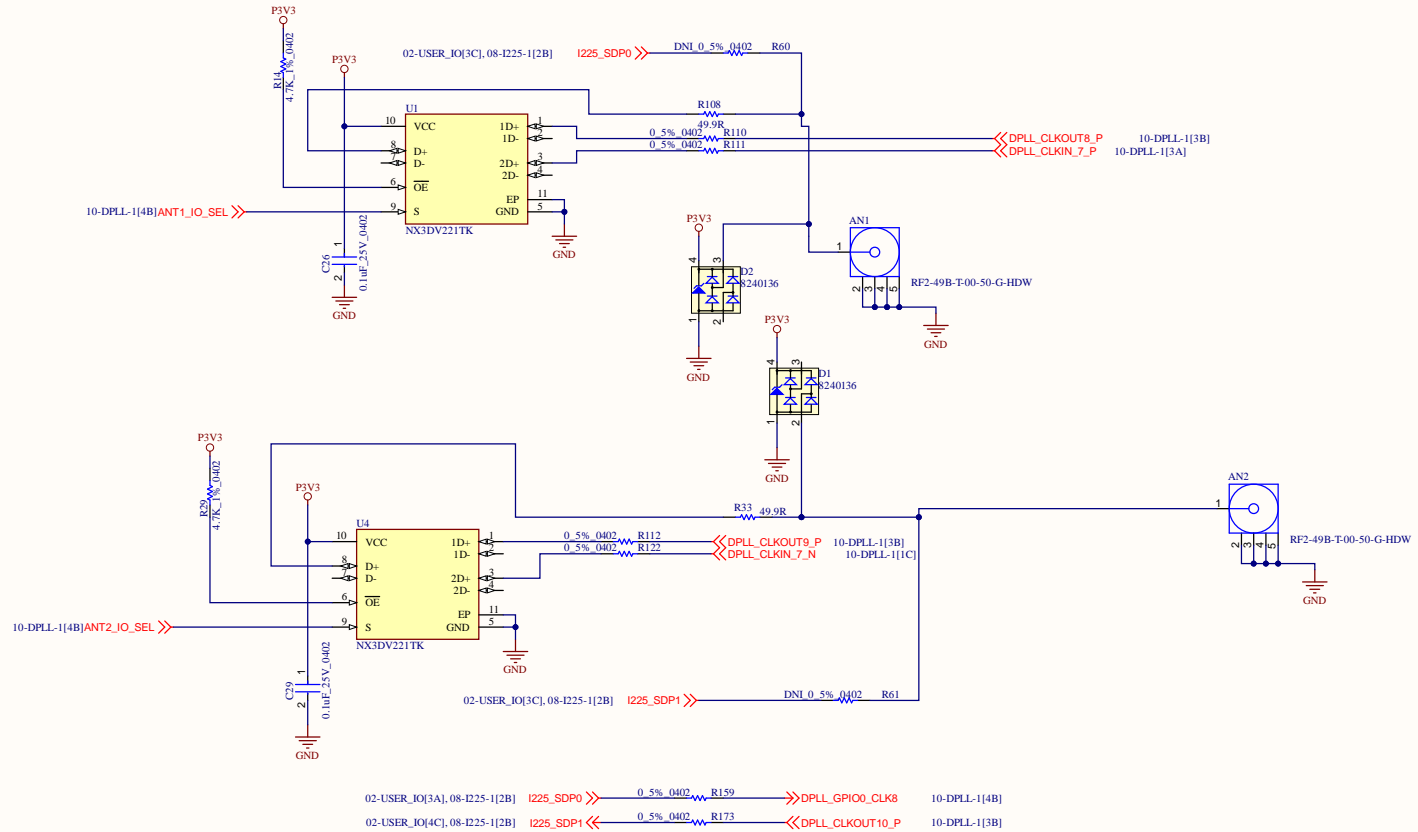


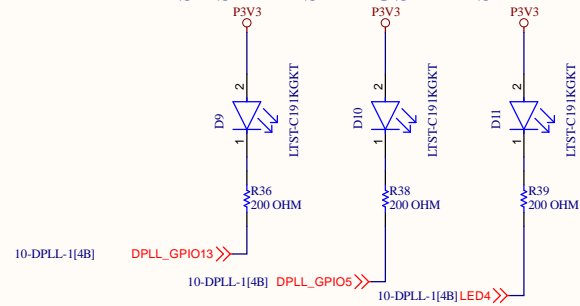
INPUT/OUTPUT SMA

Using USB MUX To select Input vs Output, cheap, lots of options, low prop delay

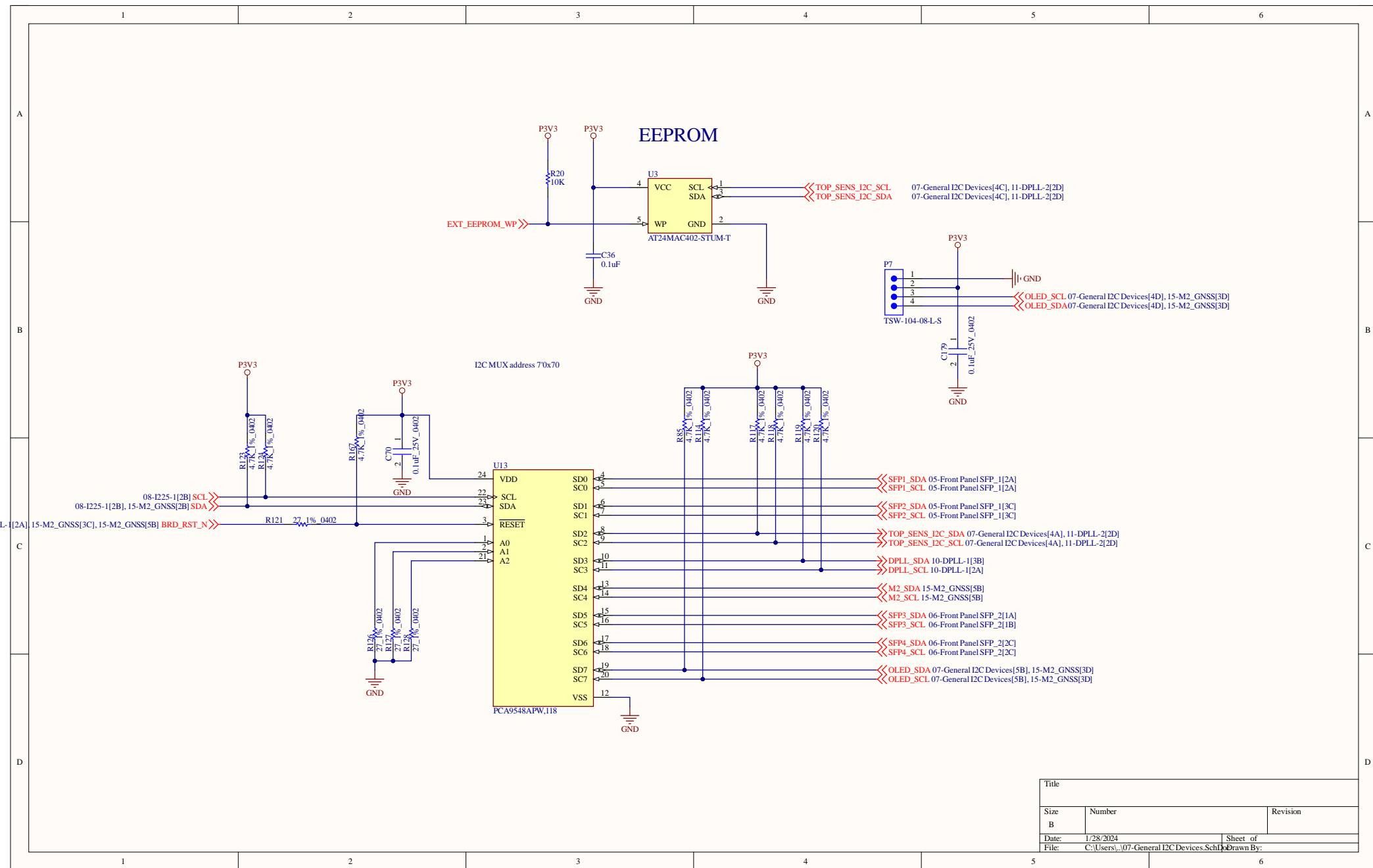
BOM Option to connect I225 directly
Also BOM options for how DPLL connects to I225 SDP0 and SDP1

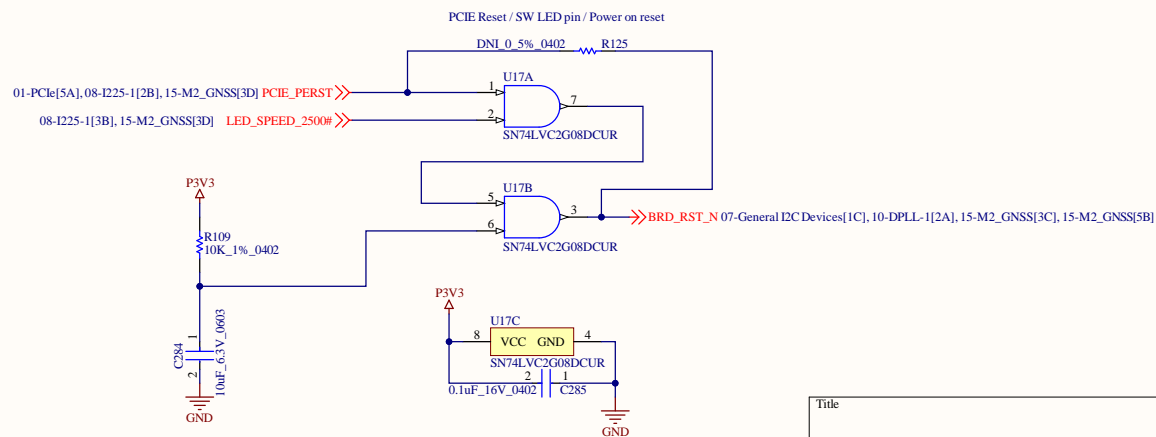
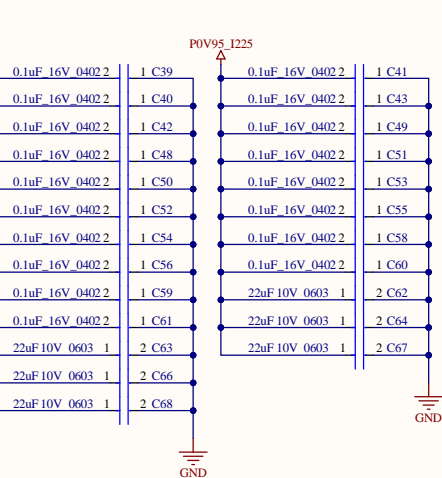


SYSTEM STATUS LEDS



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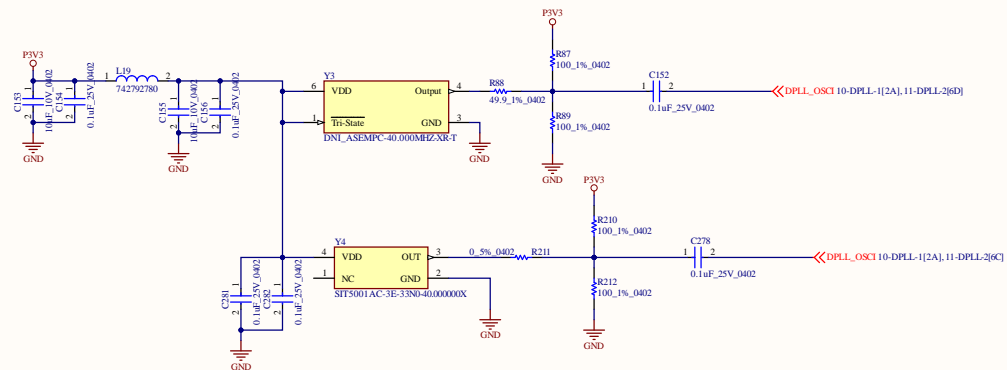
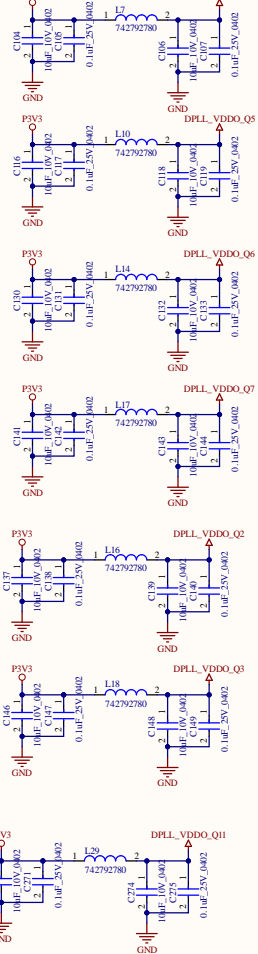




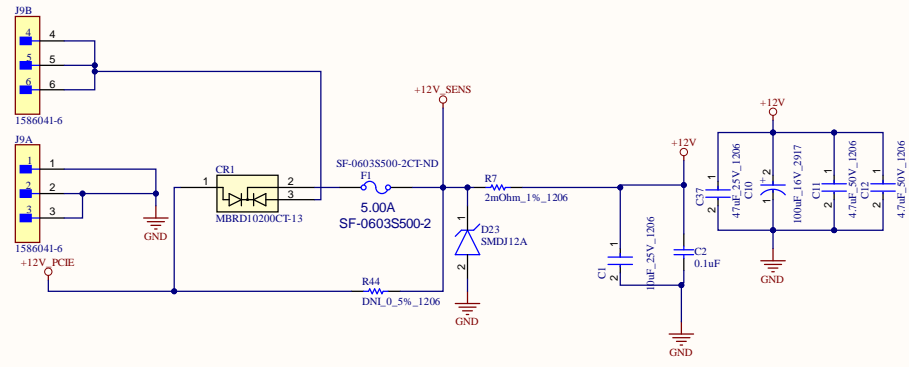
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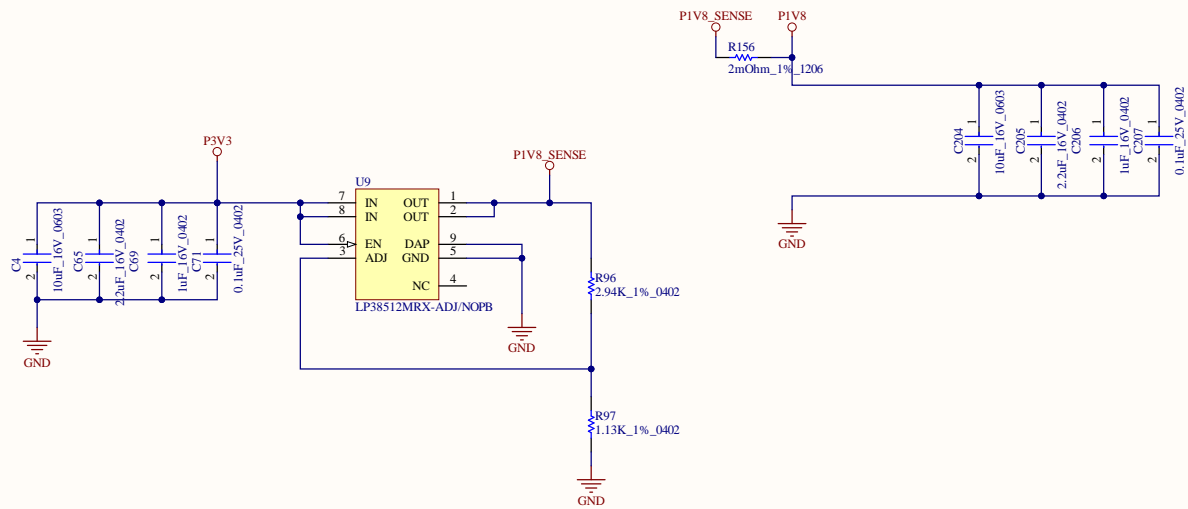


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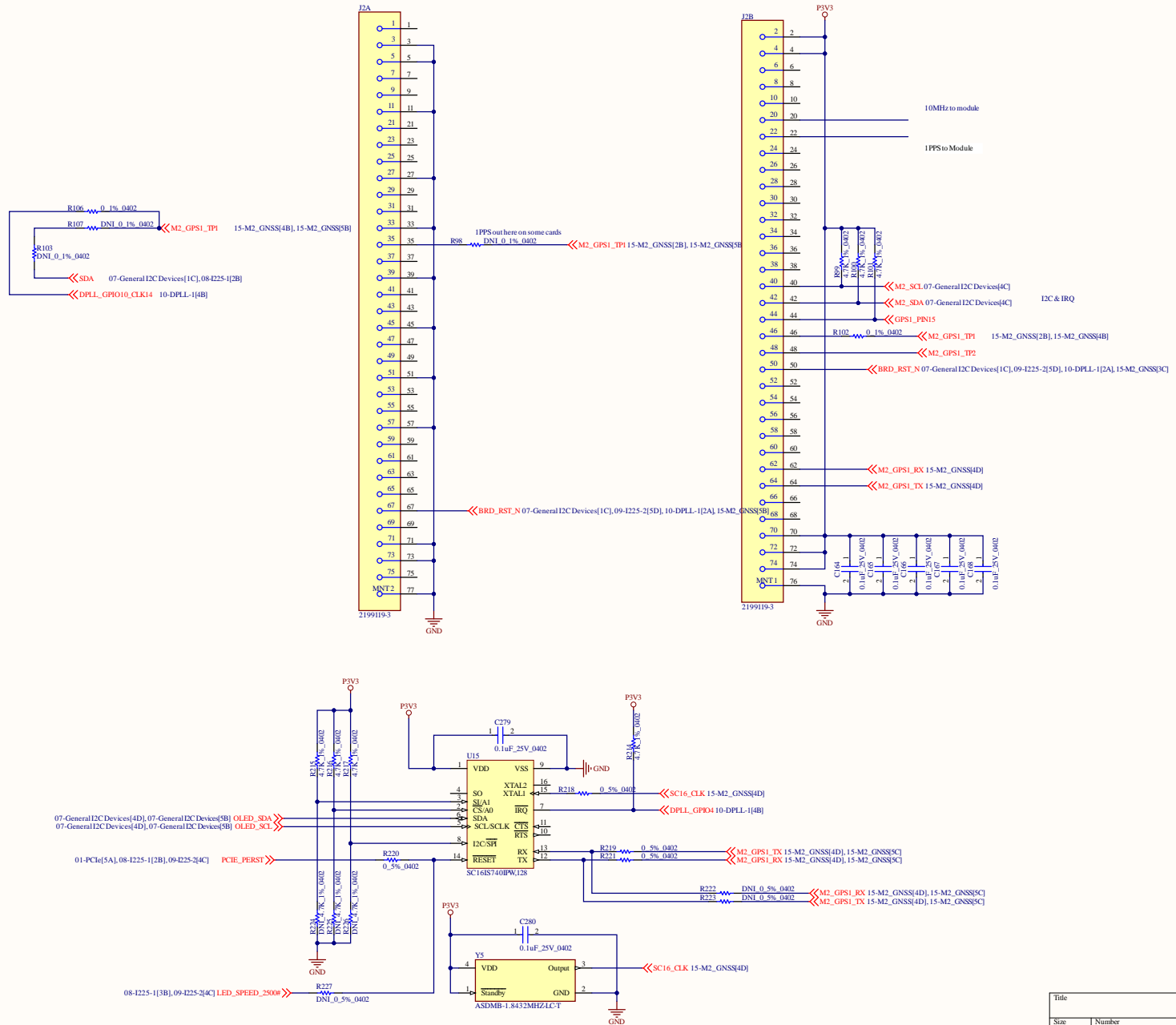
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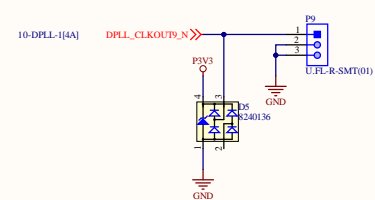
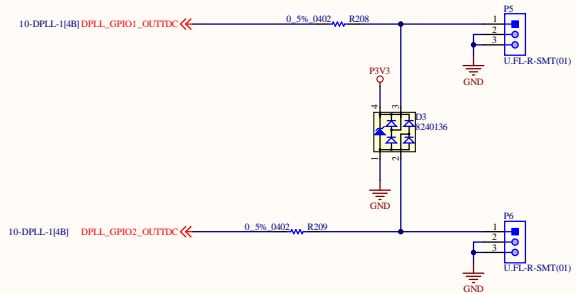


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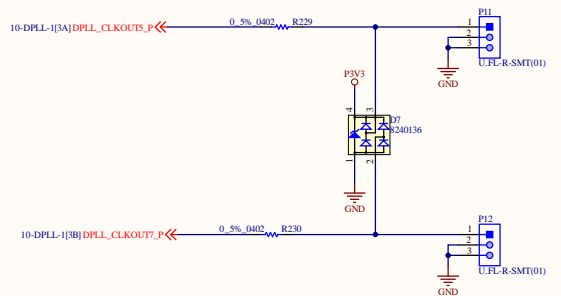
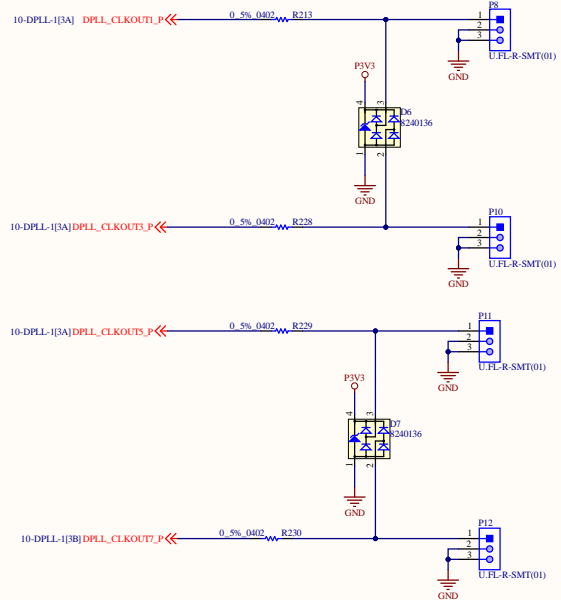
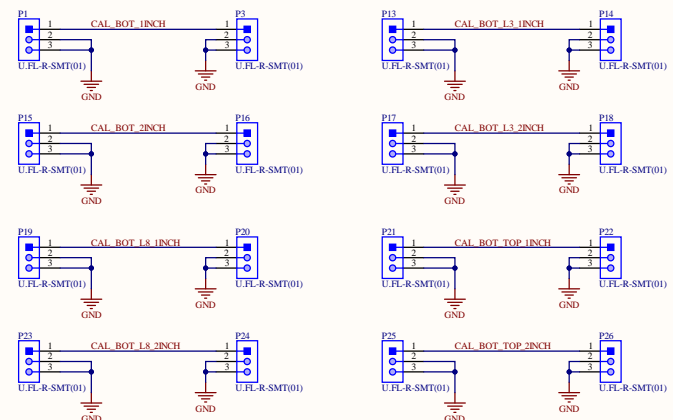
Socket 2 Key B PCIe-based WWAN Adapter Pinout , table 3-21 in M.2 spec



Title		
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Calibration traces



Title		
Size	Number	Revision
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Date:	1/28/2024	Sheet of
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