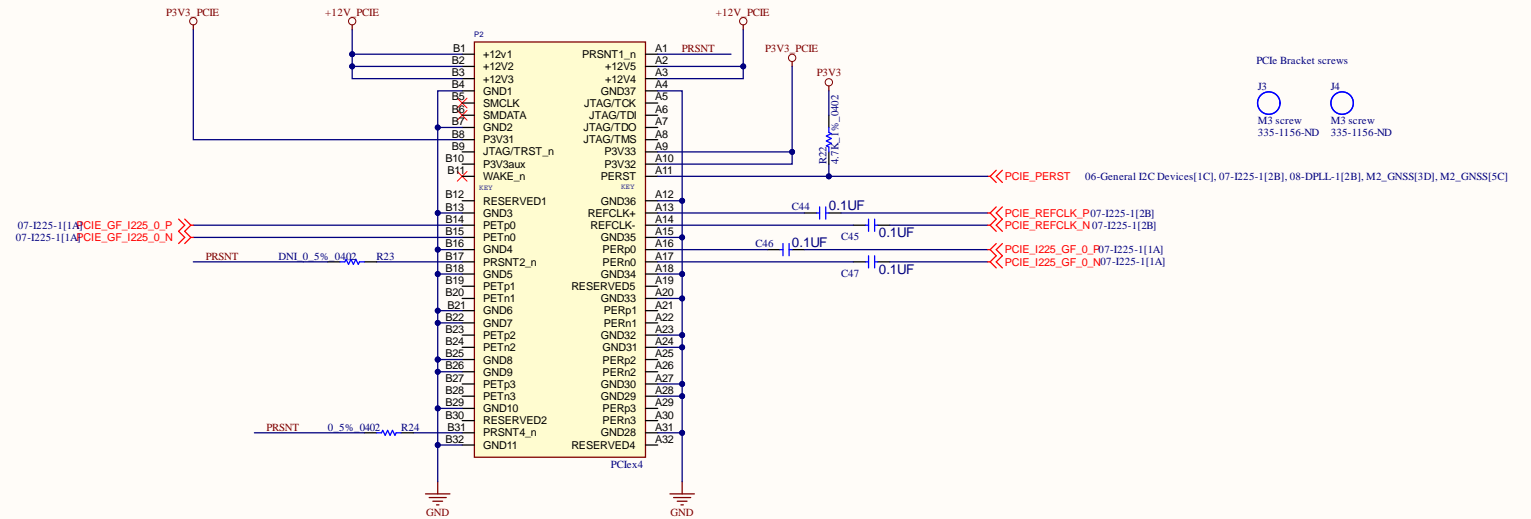


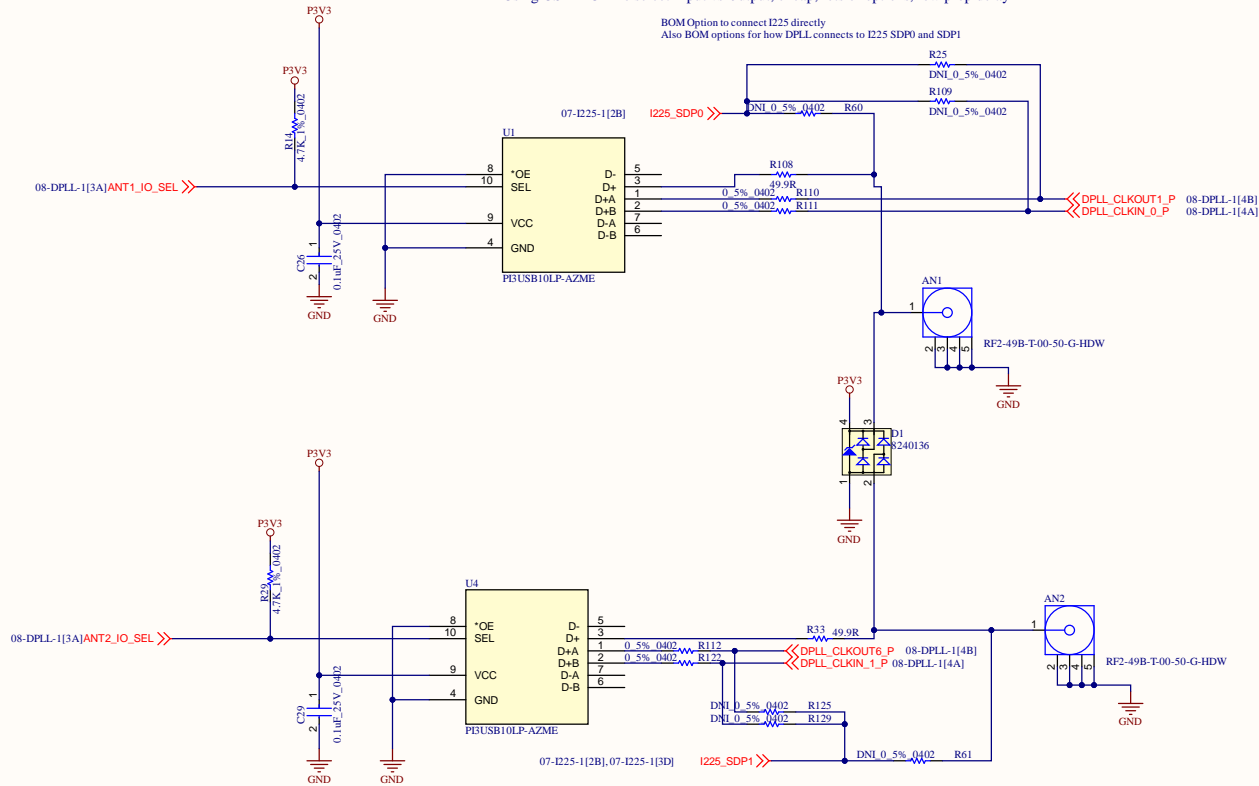
PCIe Connector



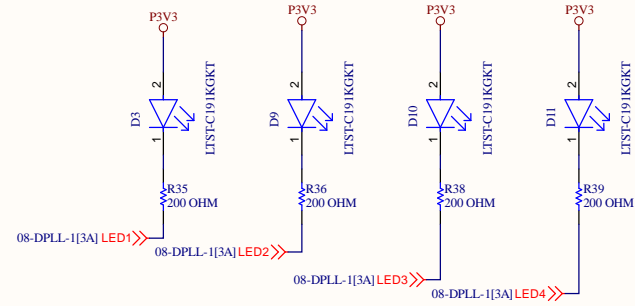
INPUT/OUTPUT SMA

Using USB MUX To select Input vs Output, cheap, lots of options, low prop delay

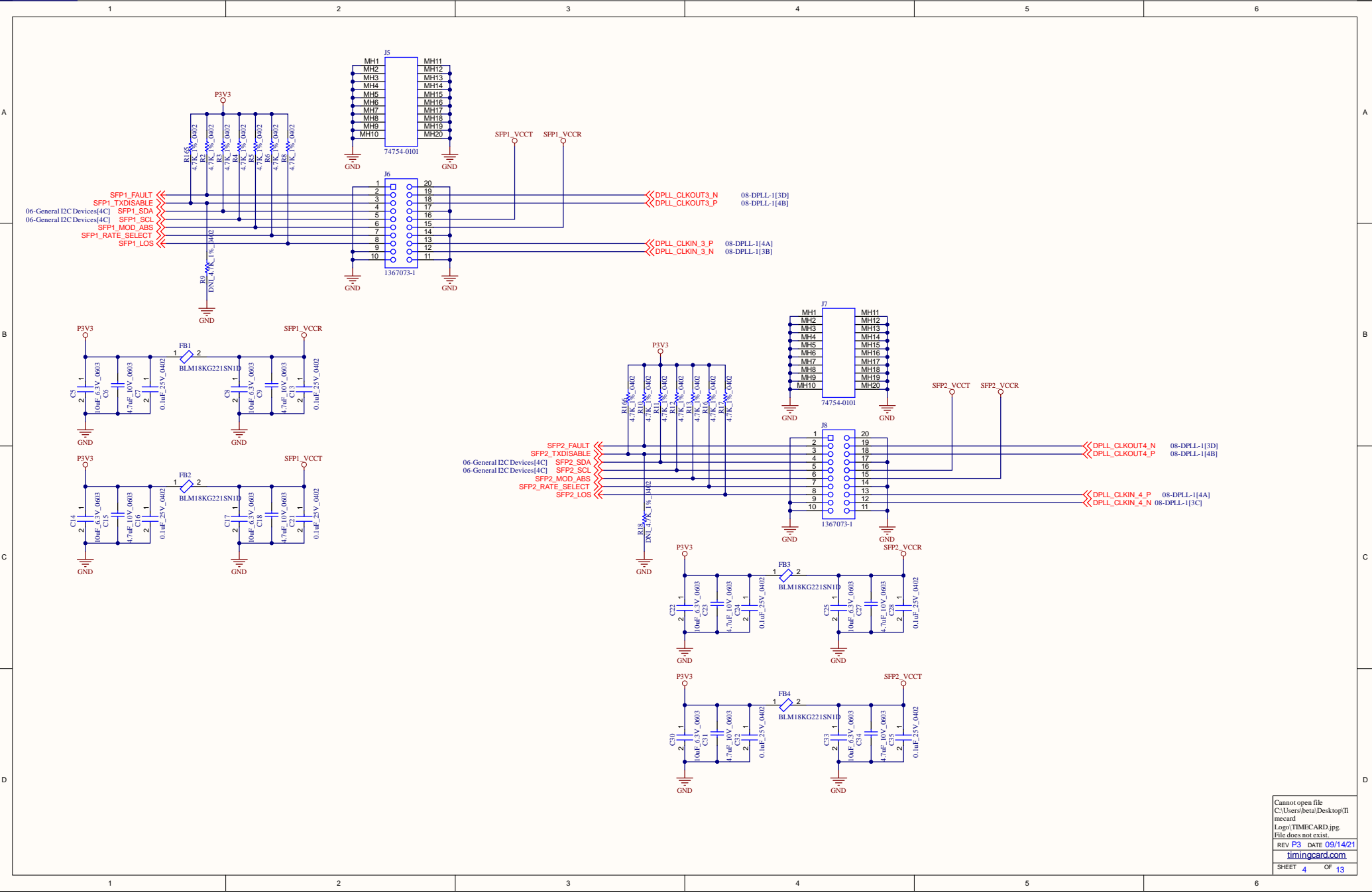
BOM Option to connect I225 directly
Also BOM options for how DPLL connects to I225 SDP0 and SDP1

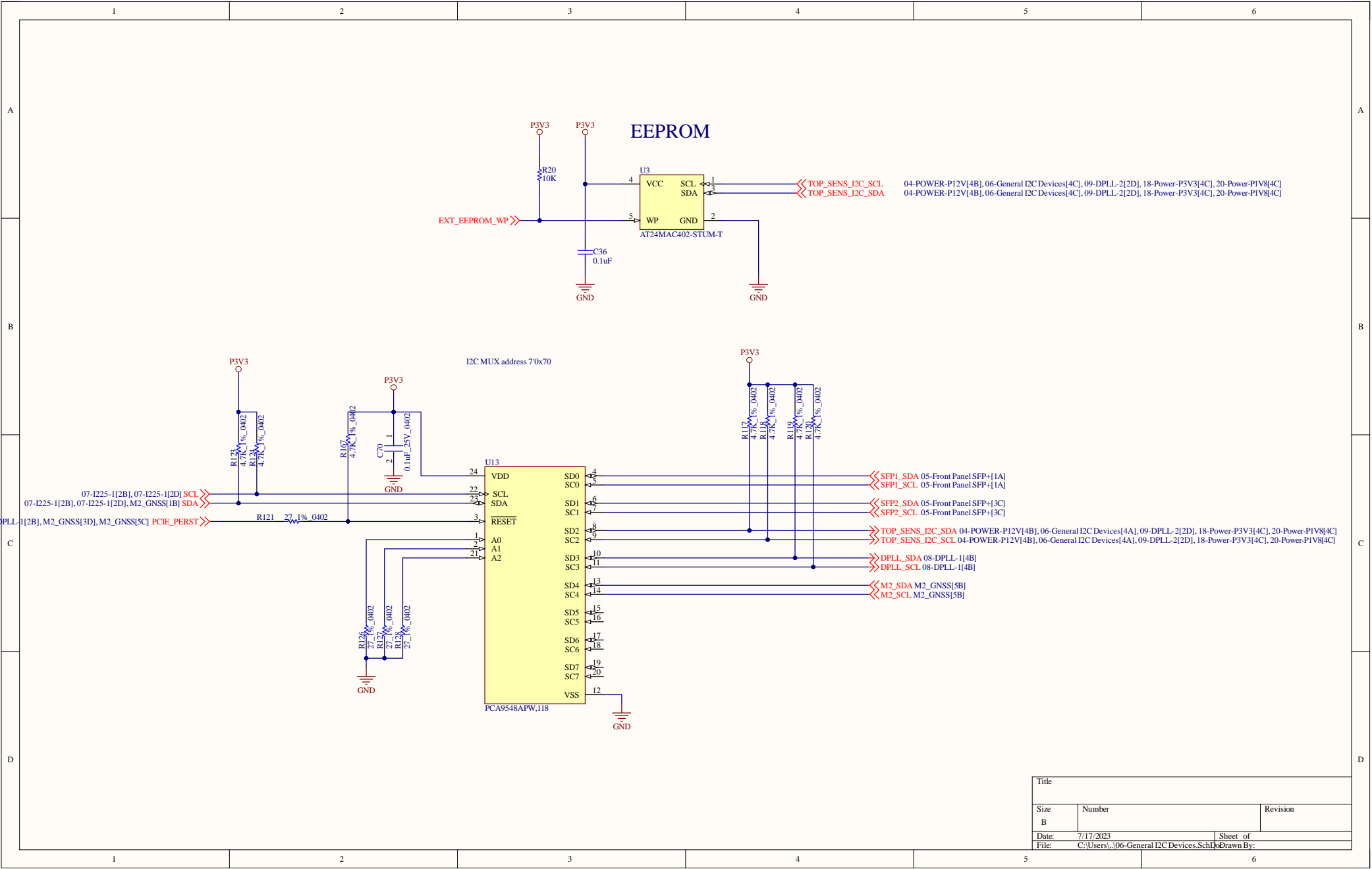


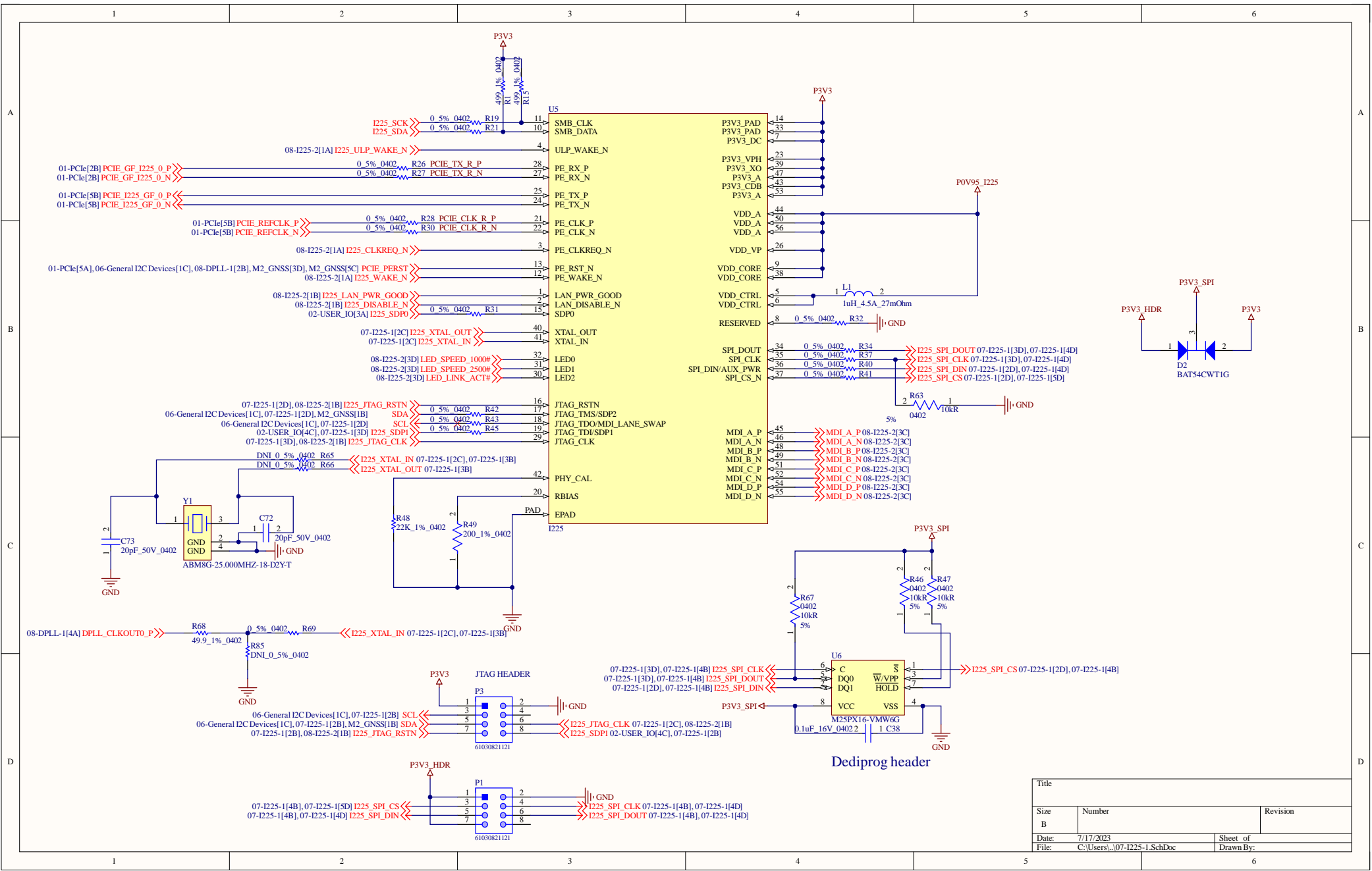
SYSTEM STATUS LEDS



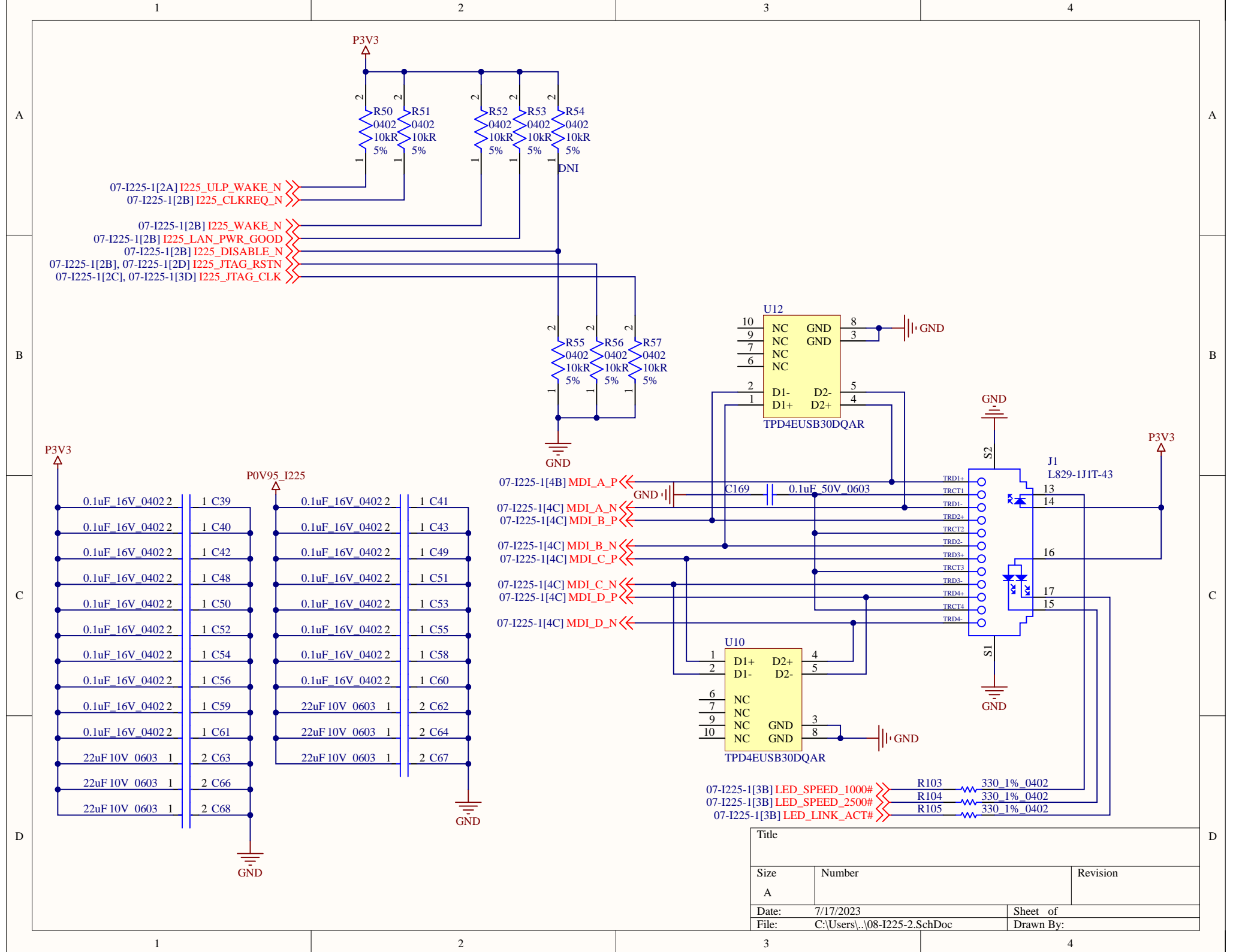
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Size B	Number	Revision
Date:	7/17/2023	Sheet of
File:	C:\Users\103-USER\IO_STATUS.Sch	Drawn By:

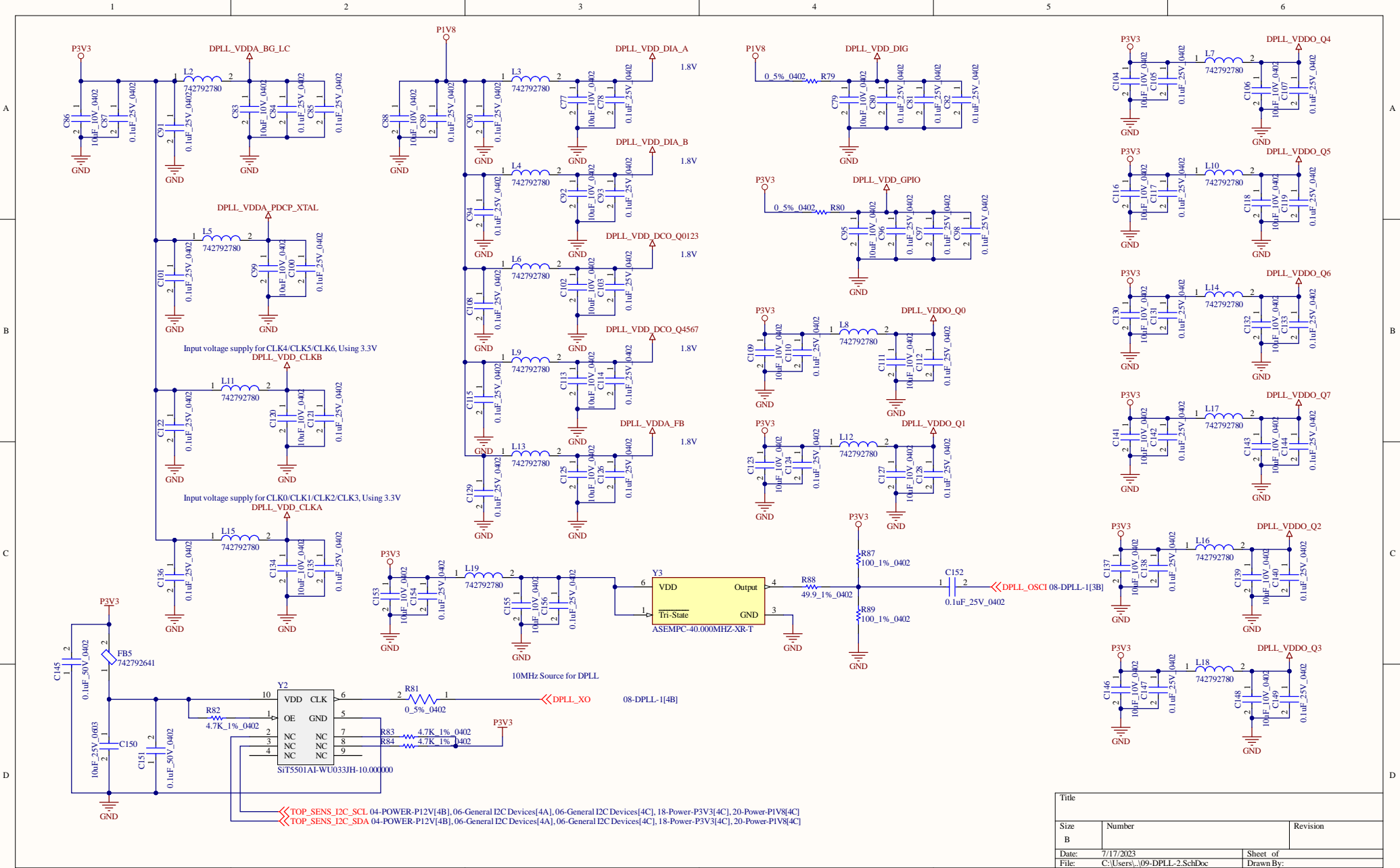






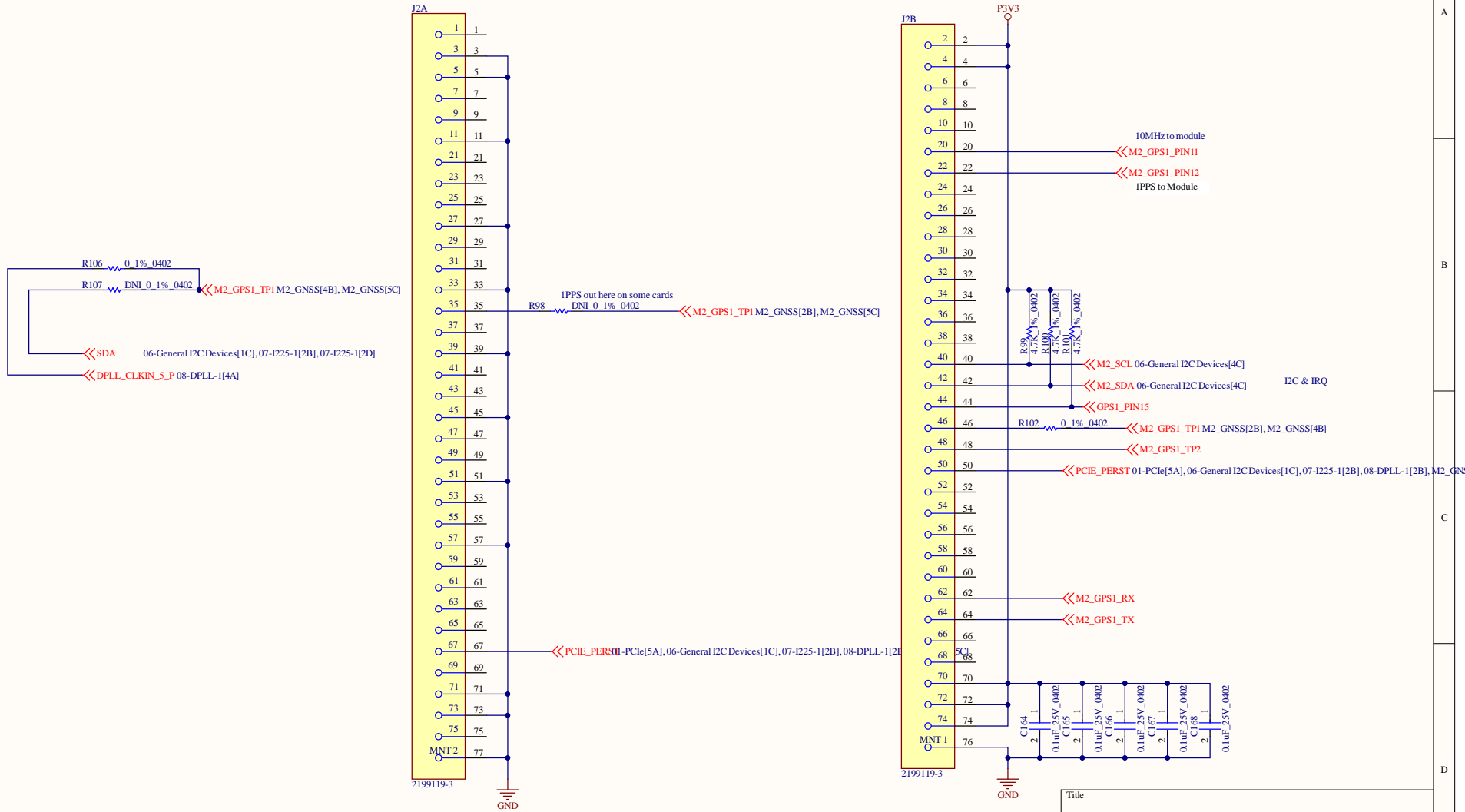
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Title		
Size	Number	Revision
B		
Date:	7/17/2023	Sheet of
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Socket 2 Key B PCIe-based WWAN Adapter Pinout , table 3-21 in M.2 spec



Title		
Size	Number	Revision
B		
Date:	7/17/2023	Sheet of
File:	C:\Users\...\M2_GNSS.SchDoc	Drawn By: