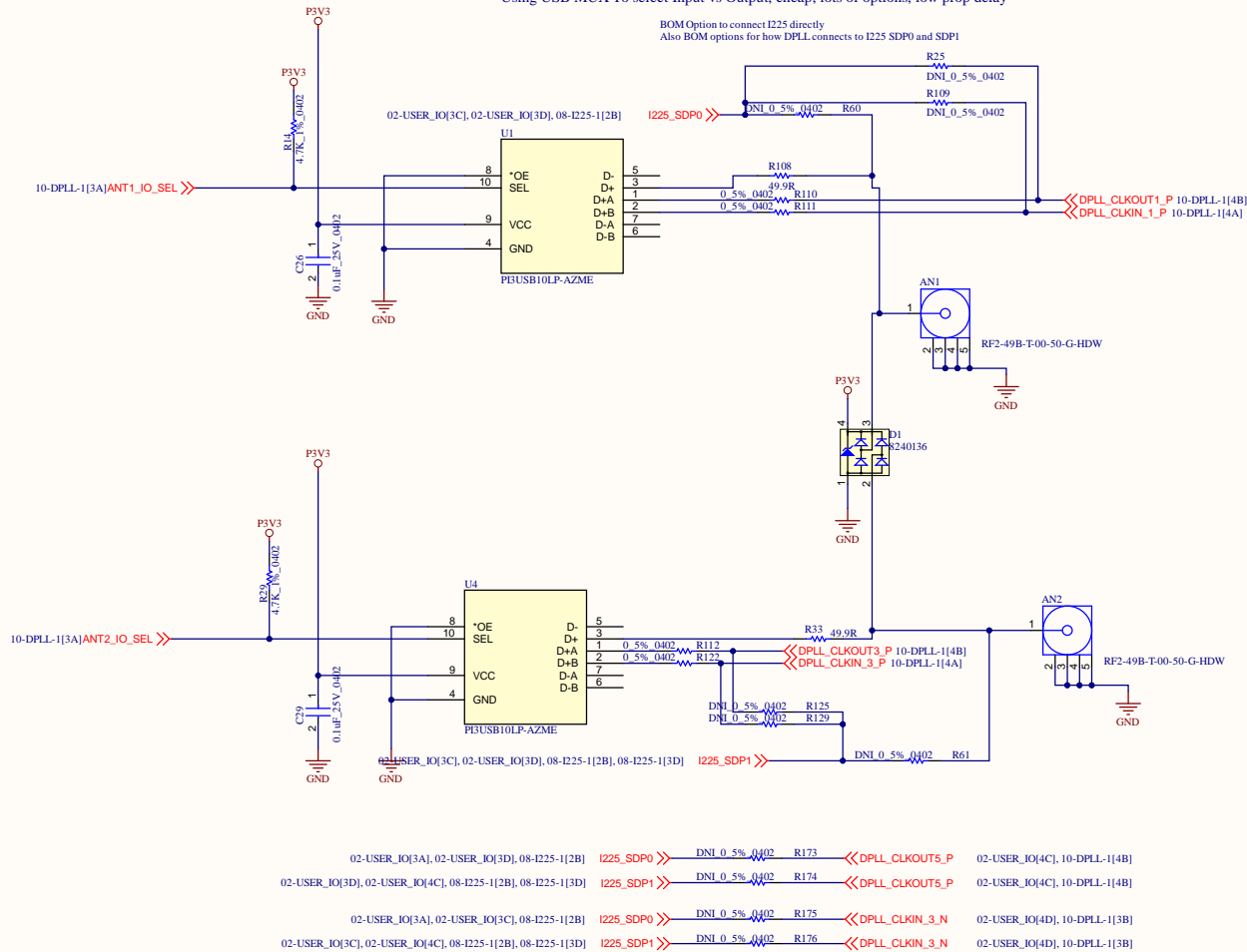




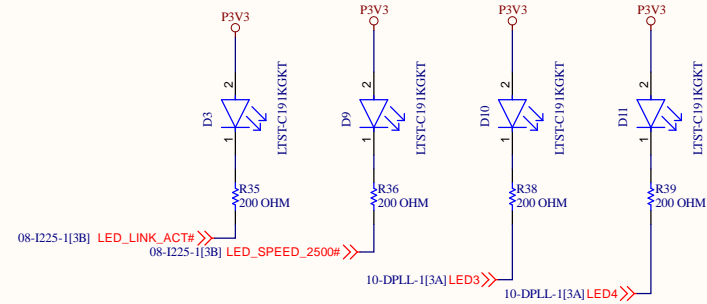
# INPUT/OUTPUT SMA

Using USB MUX To select Input vs Output, cheap, lots of options, low prop delay

BOM Option to connect I225 directly  
Also BOM options for how DPLL connects to I225 SDP0 and SDP1

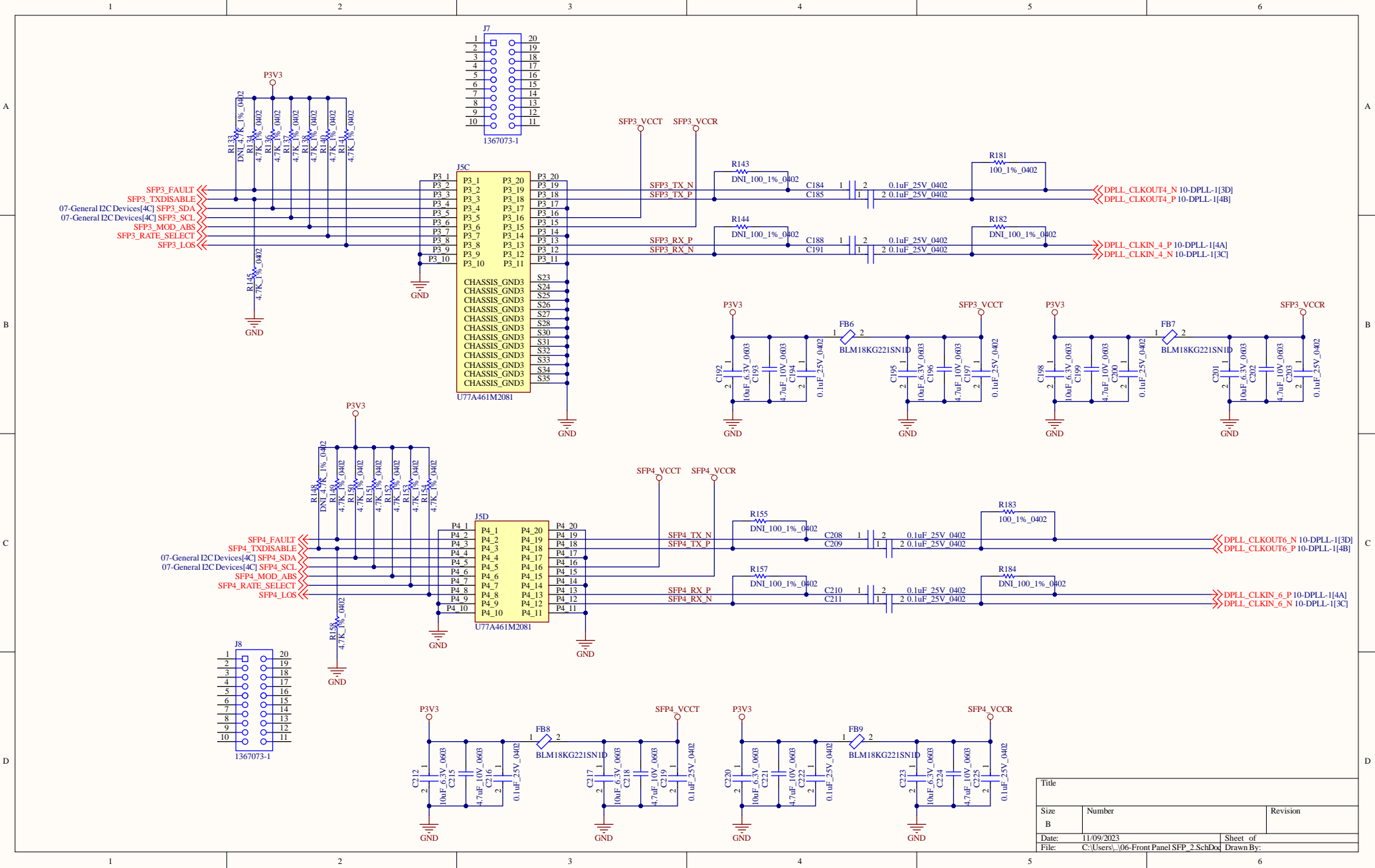


# SYSTEM STATUS LEDS

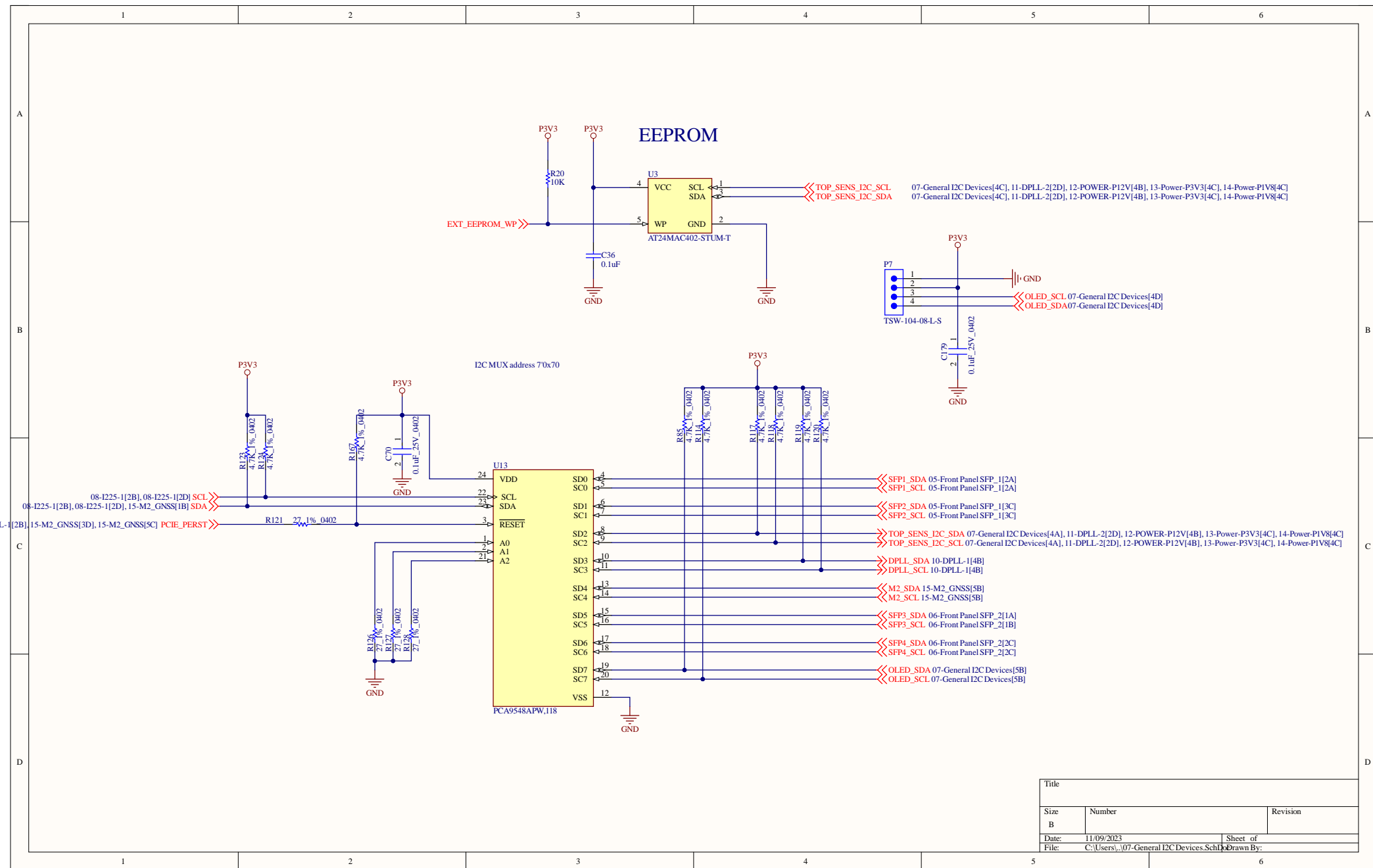


Title		
Size B	Number	Revision
Date:	11/09/2023	Sheet of
File:	C:\Users\W3-USER\IO_STATUS.Sch	Drawn By:

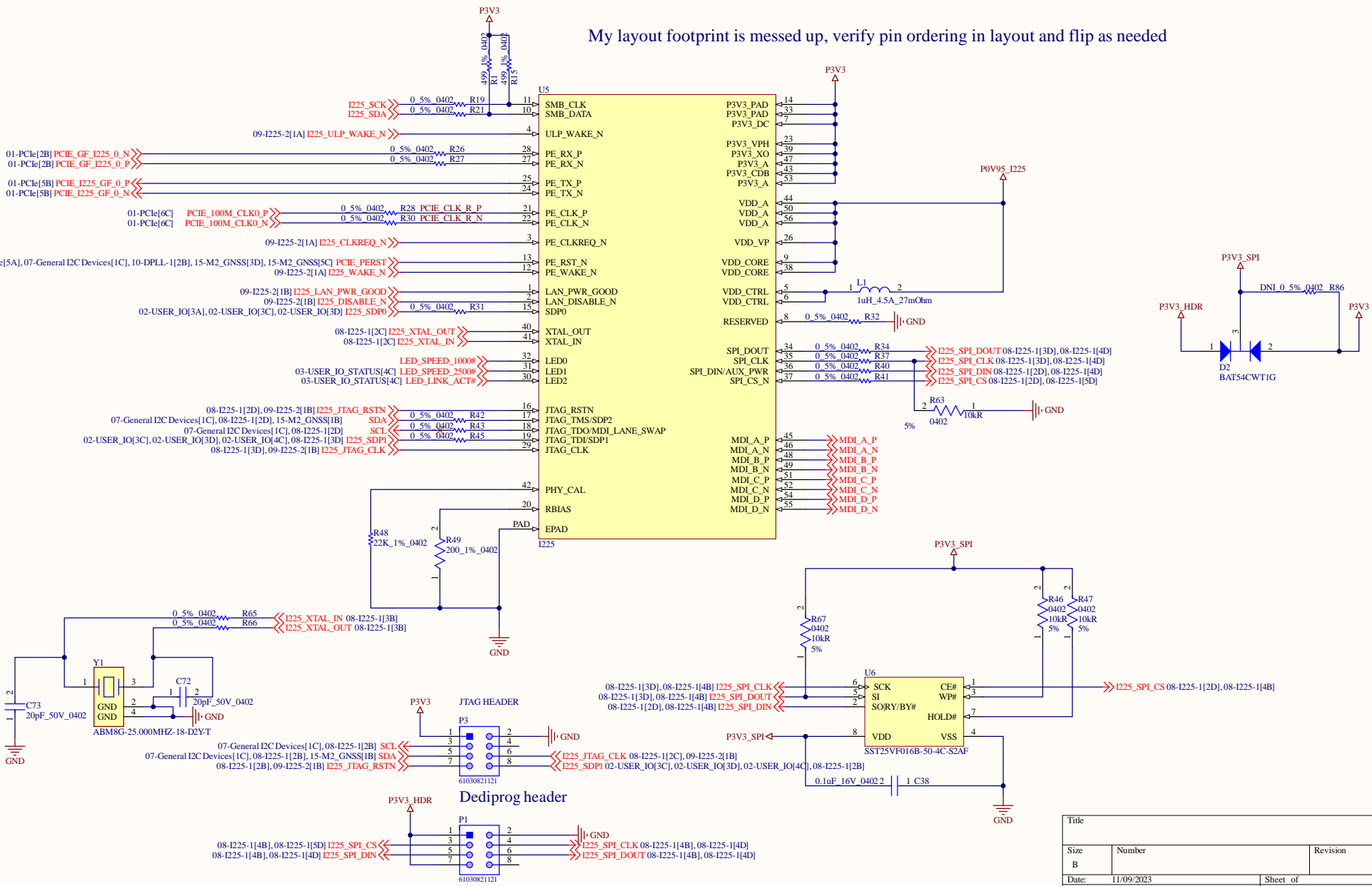




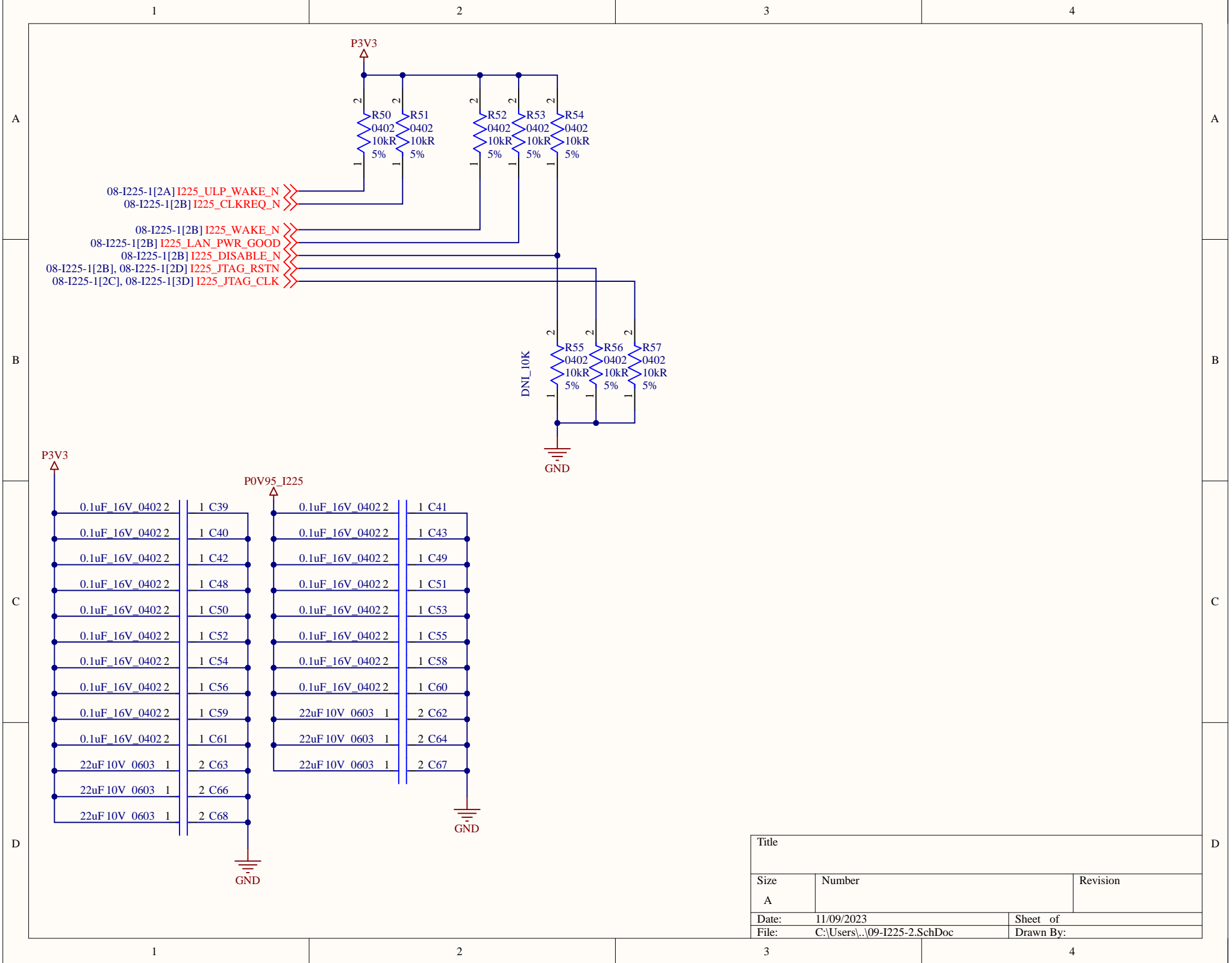
Title		
Size	Number	Revision
B		
Date:	11/09/2023	Sheet of
File:	C:\Users\...\06-Front Panel SFP_2.SchDoc	Drawn By:



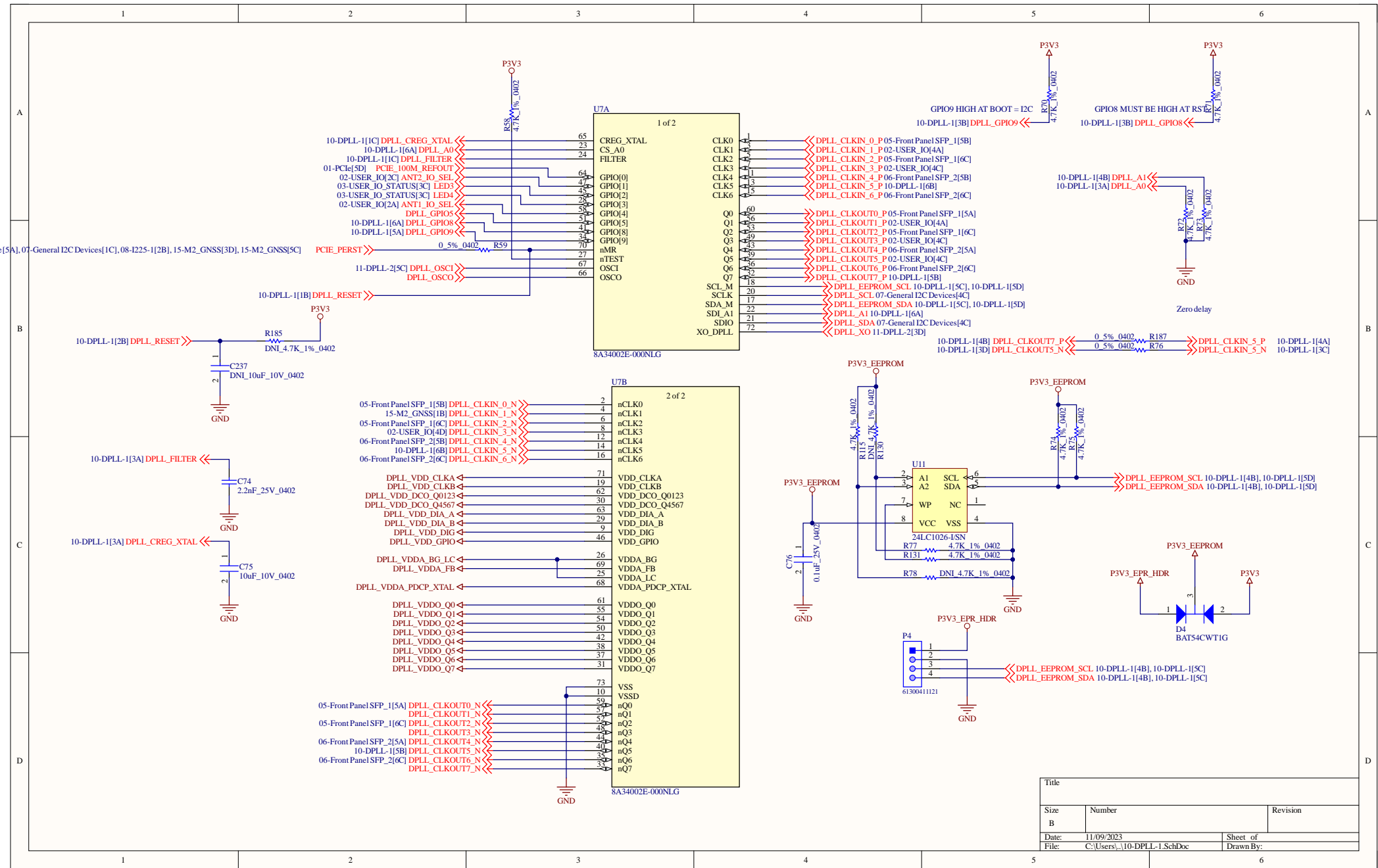
My layout footprint is messed up, verify pin ordering in layout and flip as needed



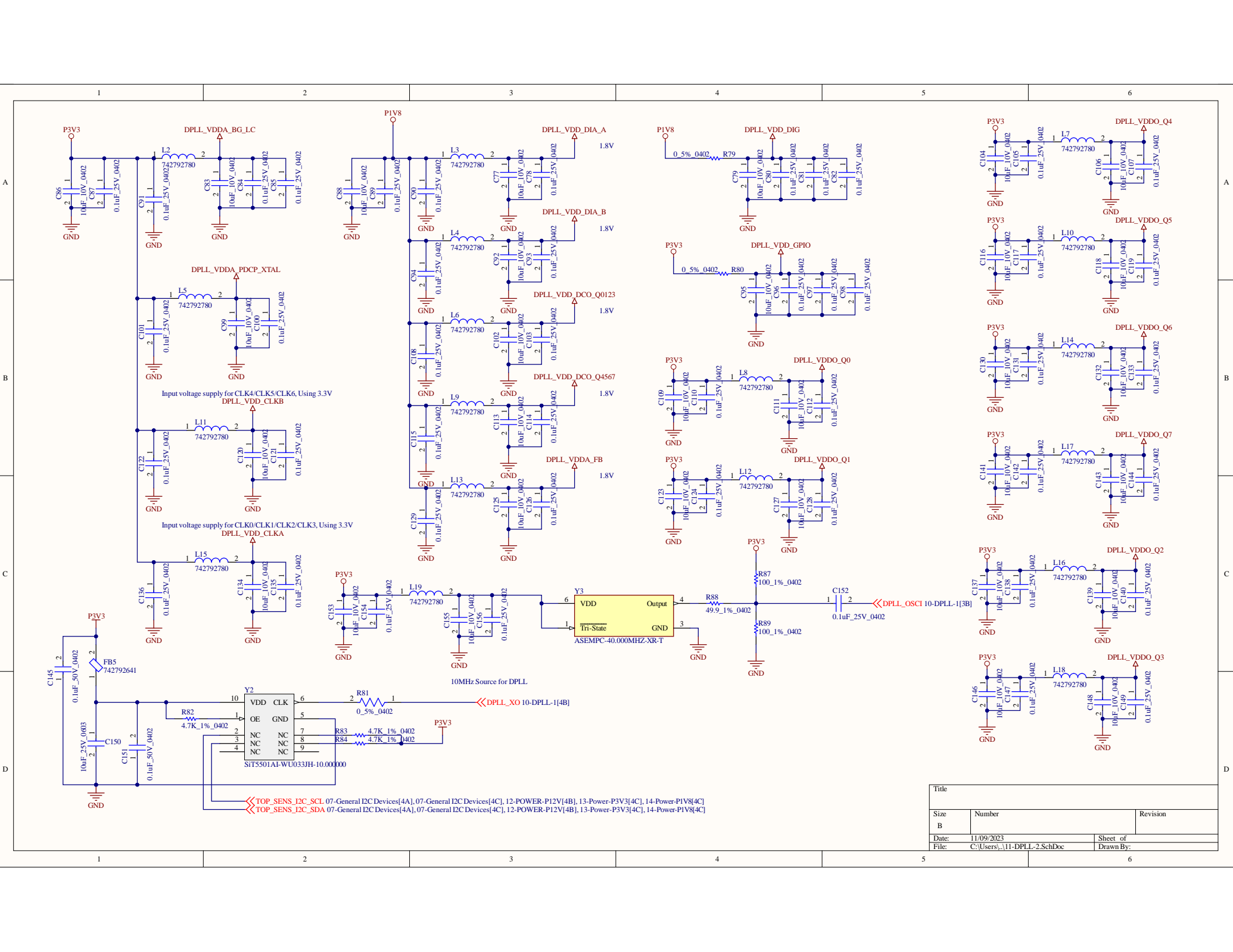
Title		
Size	Number	Revision
B		
Date:	11/09/2023	Sheet of
File:	C:\Users\...\08-I225-1.SchDoc	Drawn By:





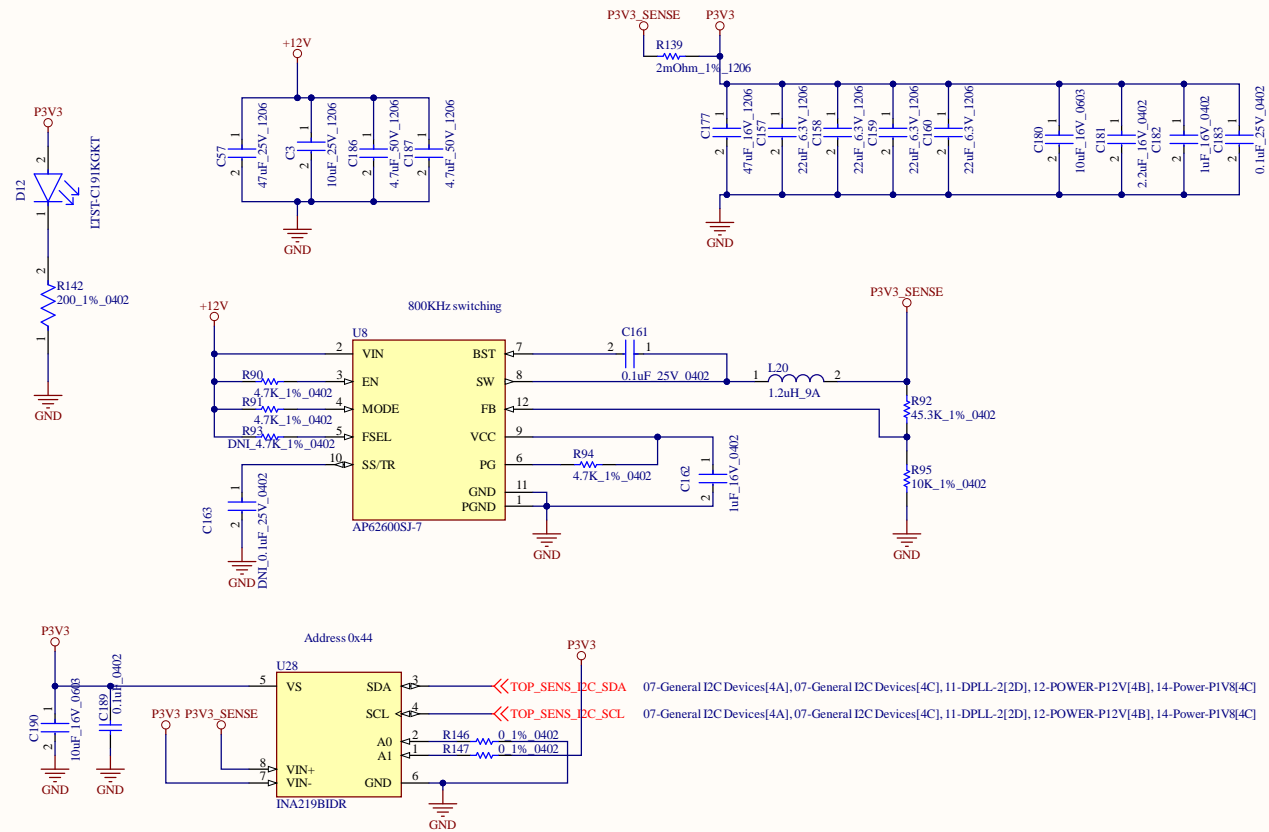


Title		
Size	Number	Revision
B		
Date:	11/09/2023	Sheet of
File:	C:\Users\10-DPLL-1\SchDoc	Drawn By:

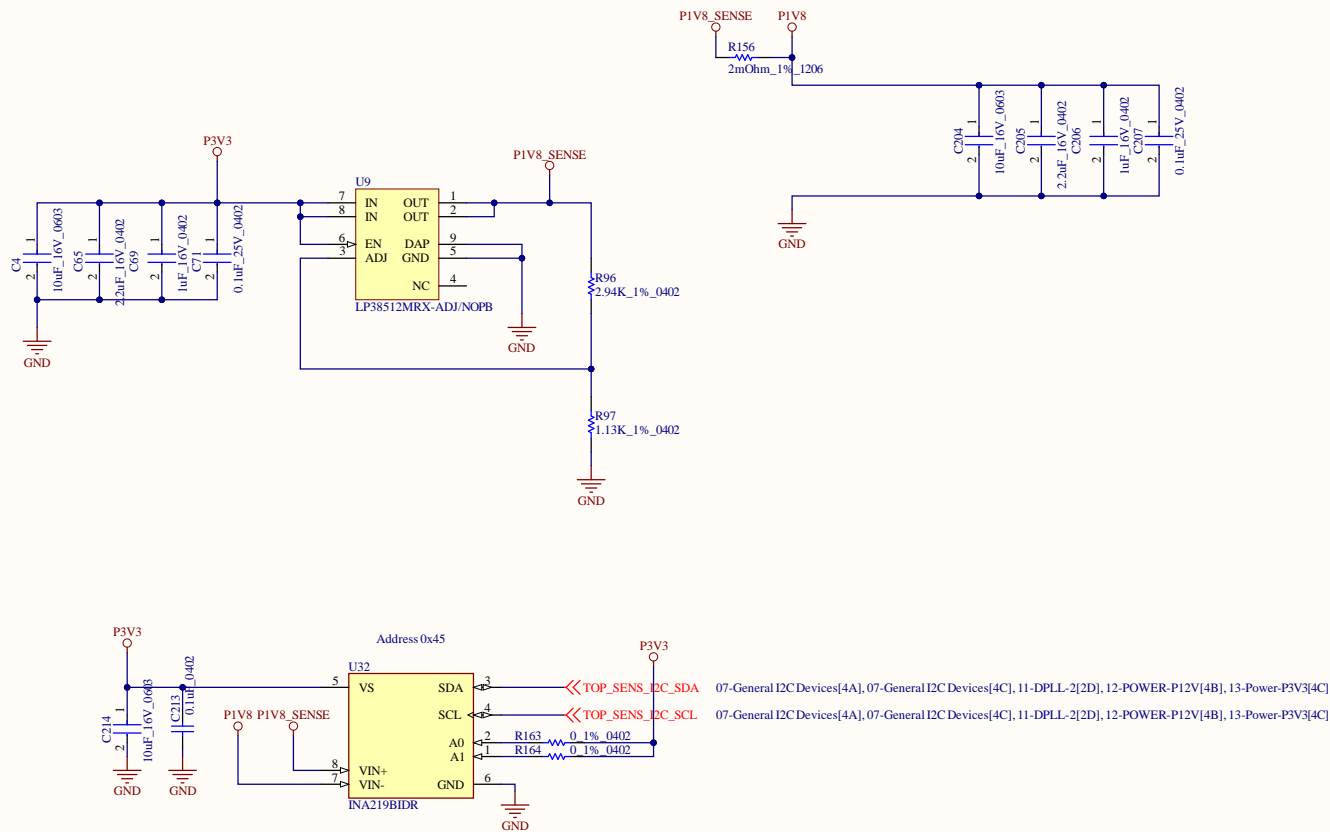


Title		
Size	Number	Revision
B		
Date:	11/09/2023	Sheet of
File:	C:\Users\11-DPLL-2.SchDoc	Drawn By:



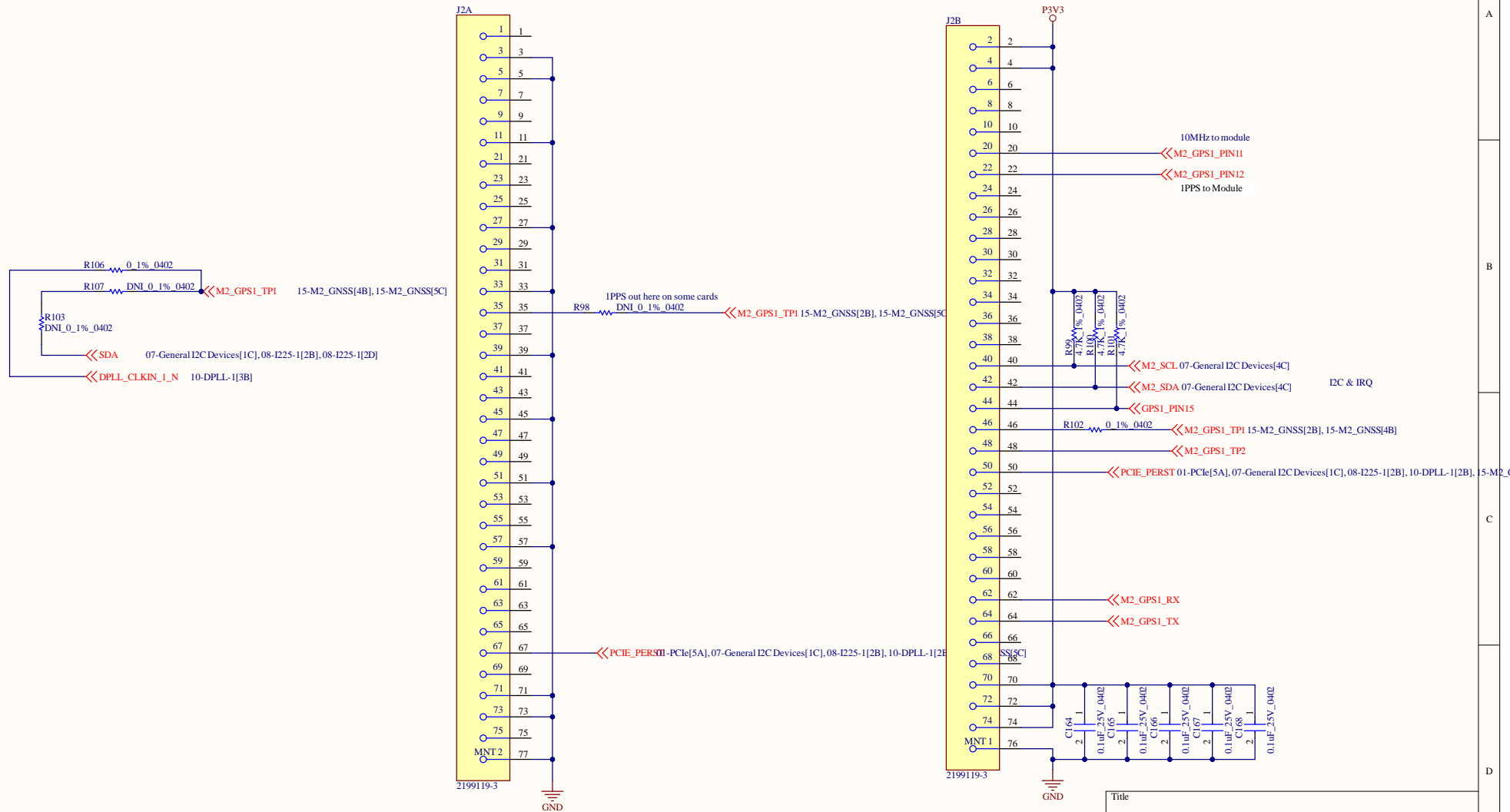


Title		
Size	Number	Revision
B		
Date:	11/09/2023	Sheet of
File:	C:\Users\...\13-Power-P3V3.SchDoc	Drawn By:



Title		
Size	Number	Revision
B		
Date:	11/09/2023	Sheet of
File:	C:\Users\...\14-Power-P1V8.SchDoc	Drawn By:

# Socket 2 Key B PCIe-based WWAN Adapter Pinout , table 3-21 in M.2 spec



Title		
Size B	Number	Revision
Date: 11/09/2023	Sheet of	
File: C:\Users\...\15-M2_GNSS.SchDoc	Drawn By:	