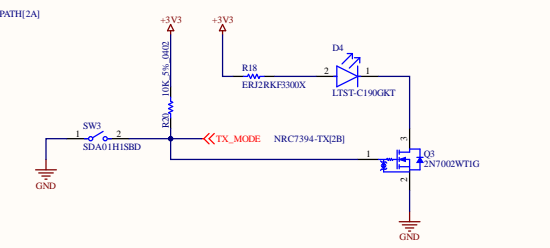
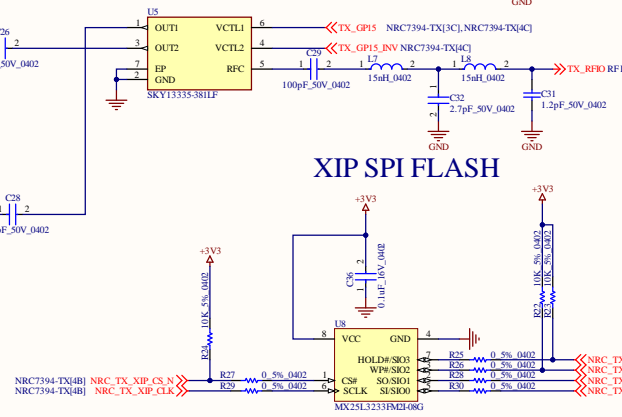
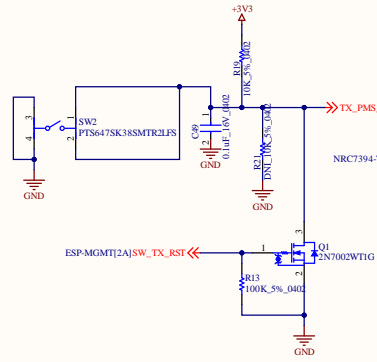
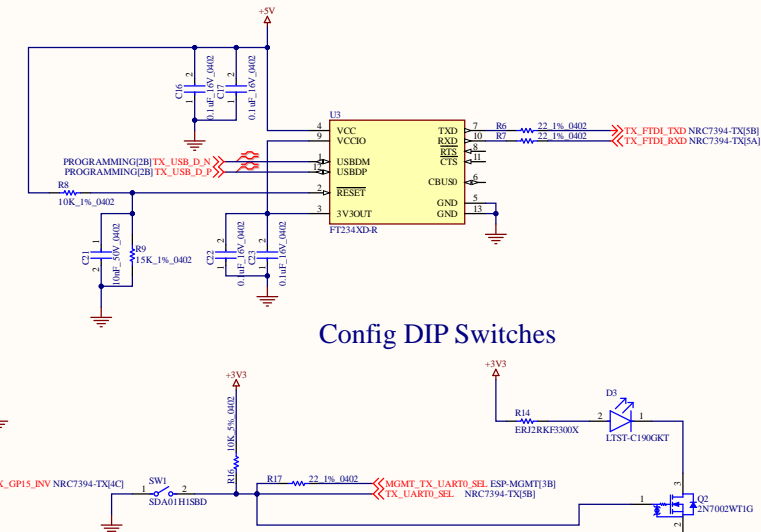
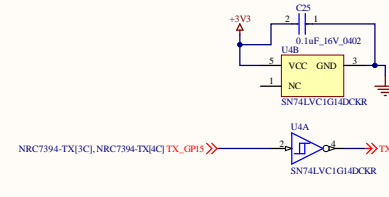
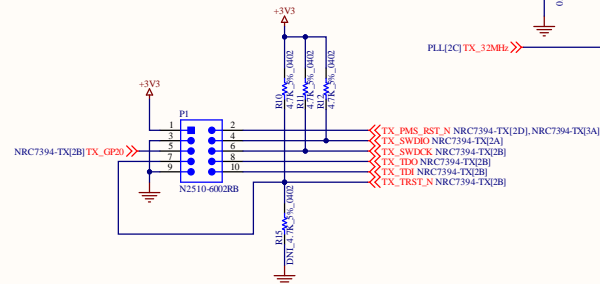
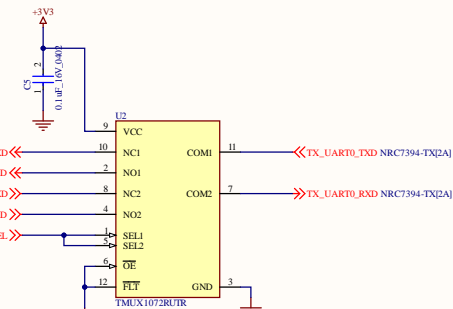
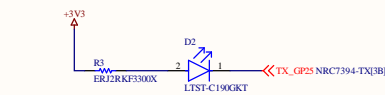
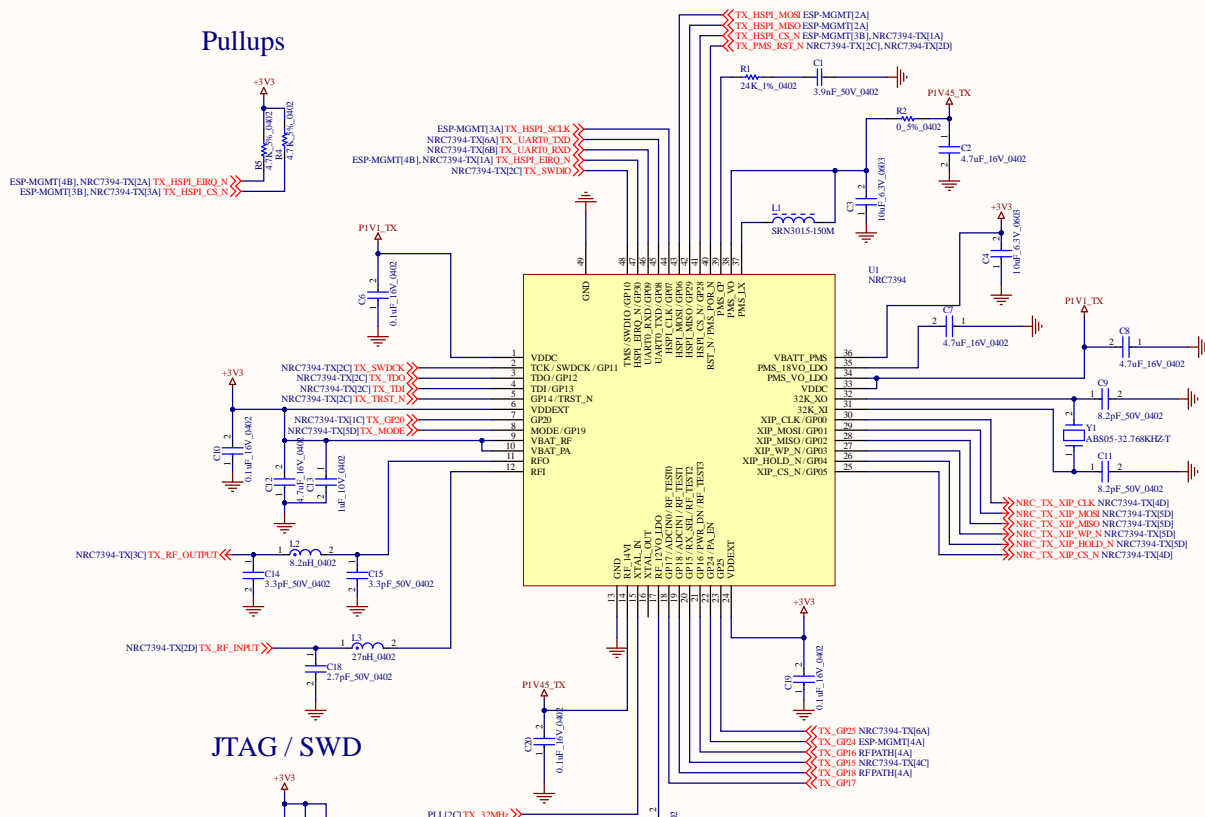


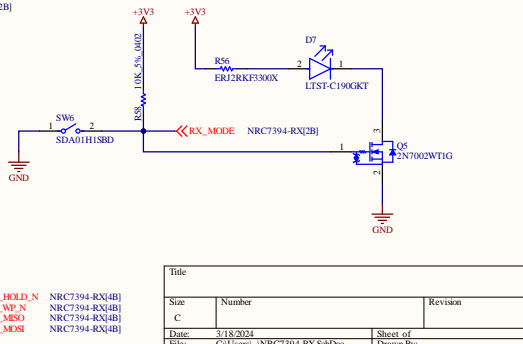
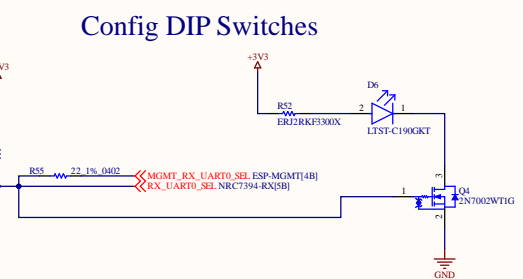
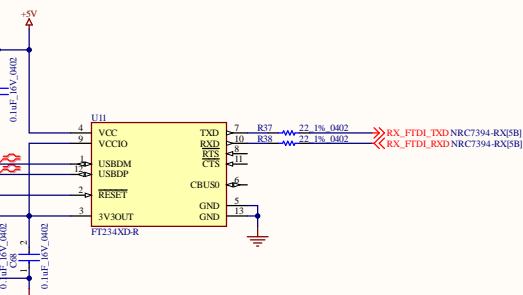
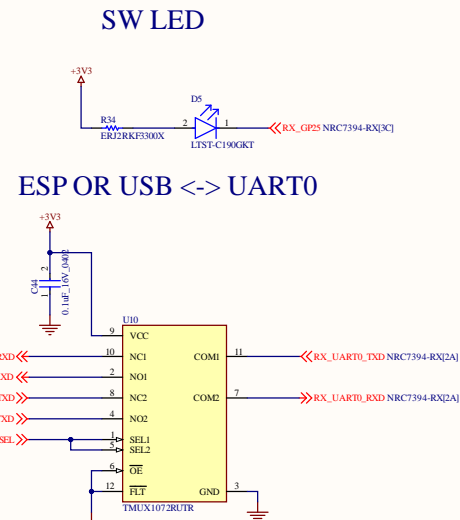
Newracom-WiWi

- [1] TITLE PAGE.SchDoc
- [2] NRC7394-TX.SchDoc
- [3] NRC7394-RX.SchDoc
- [4] ESP-MGMT.SchDoc
- [5] RF PATH.SchDoc
- [6] PROGRAMMING.SchDoc
- [7] POWER.SchDoc
- [8] DCTCXO.SchDoc
- [9] PLL.SchDoc

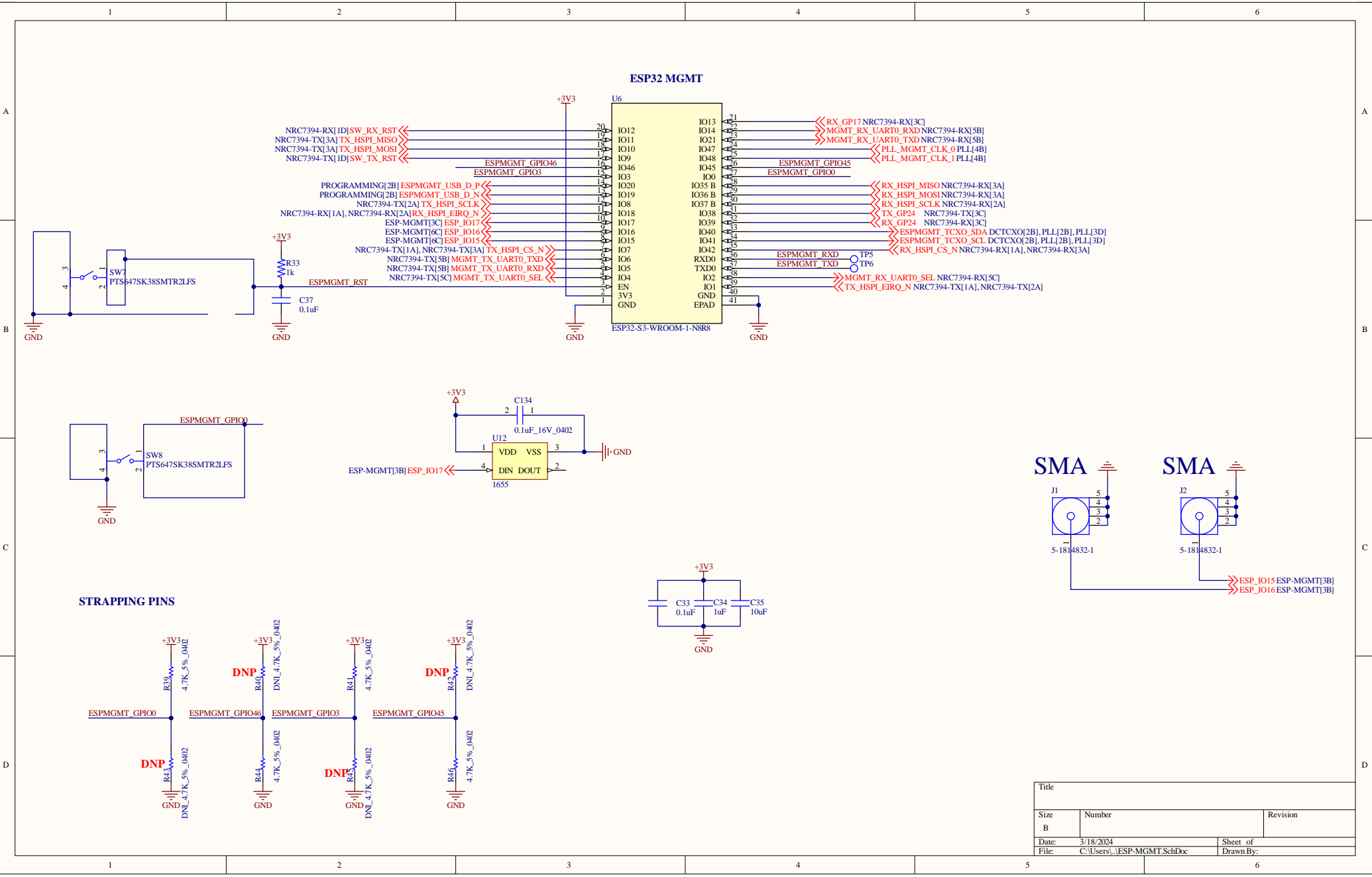
Title			TITLE PAGE	
Size	Number		Revision	
A			V1.1	
Date:	3/18/2024		Sheet 1 of 9	
File:	C:\Users\...\TITLE PAGE.SchDoc		Drawn By: Armando Pinales	



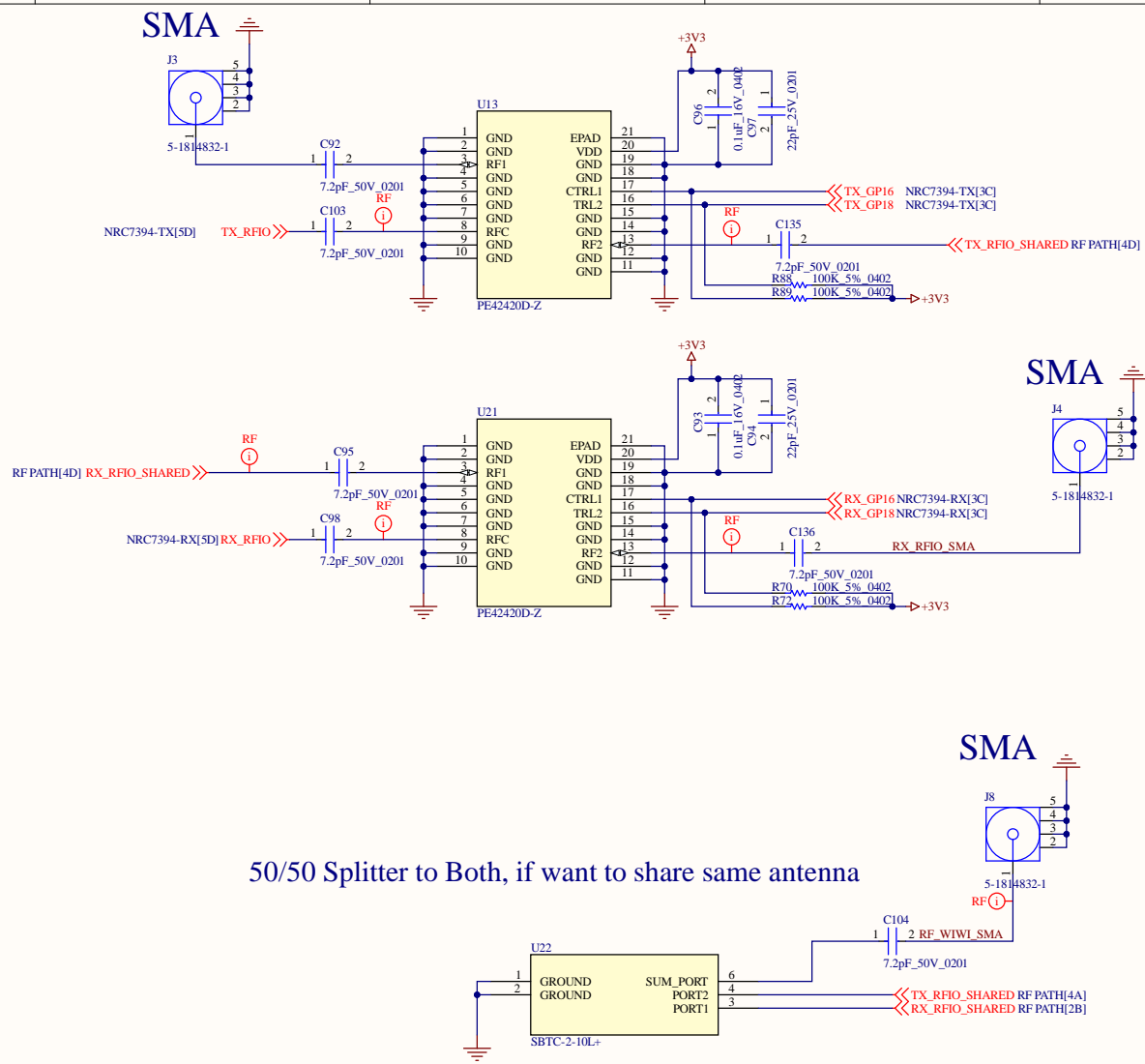
Title					
Size C	Number				Revision
Date:	3/18/2024				Sheet of _____
Drawing Name: C:\Users\jmb\Desktop\4790 Sub-Proc\Drawings\4790 Sub-Proc.dwg					



Title		
Size C	Number	Revision
Date: 3/18/2024	Sheet of	
Edw. Collins, UNIC/77304 DVC E&E Pro	Drawing B...	

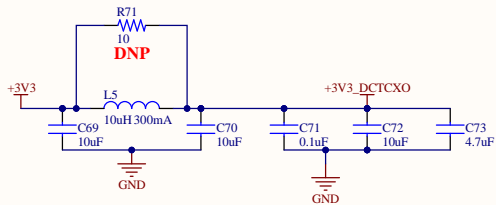


Title		
Size	Number	Revision
B		
Date:	3/18/2024	Sheet of
File:	C:\Users\...\ESP-MGMT.SchDoc	Drawn By:

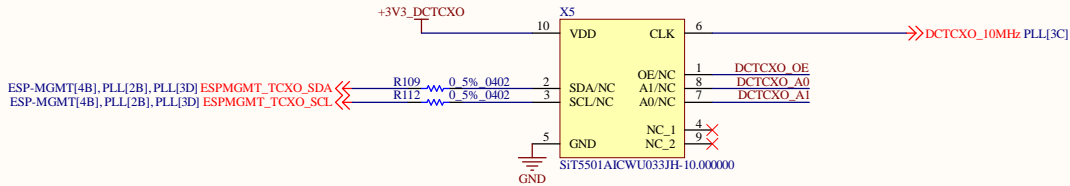


Title		
RF PATH		
Size	Number	Revision
B		V1.1
Date:	3/18/2024	Sheet 5 of 9
File:	C:\Users\...\RF PATH.SchDoc	Drawn By: Armando Pinales

MGMT I2C PULL-UPS



DCTCXO



I2C ADDR: 0x6A

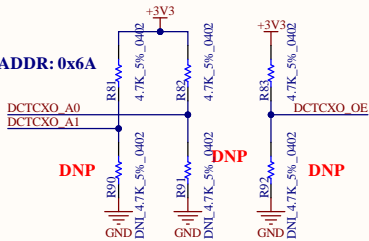


Table 14. Pin Selectable I ² C Address Control ⁽¹⁾		
A0 Pin 7	A1 Pin 8	I ² C Address
0	0	1100000
0	1	1100010
1	0	1101000
1	1	1101010 (Default)

Title DCTCXO		
Size B	Number	Revision
		V1.1
Date:	3/18/2024	Sheet 8 of 9
File:	C:\Users\...\DCTCXO.SchDoc	Drawn By: Armando Pinales

