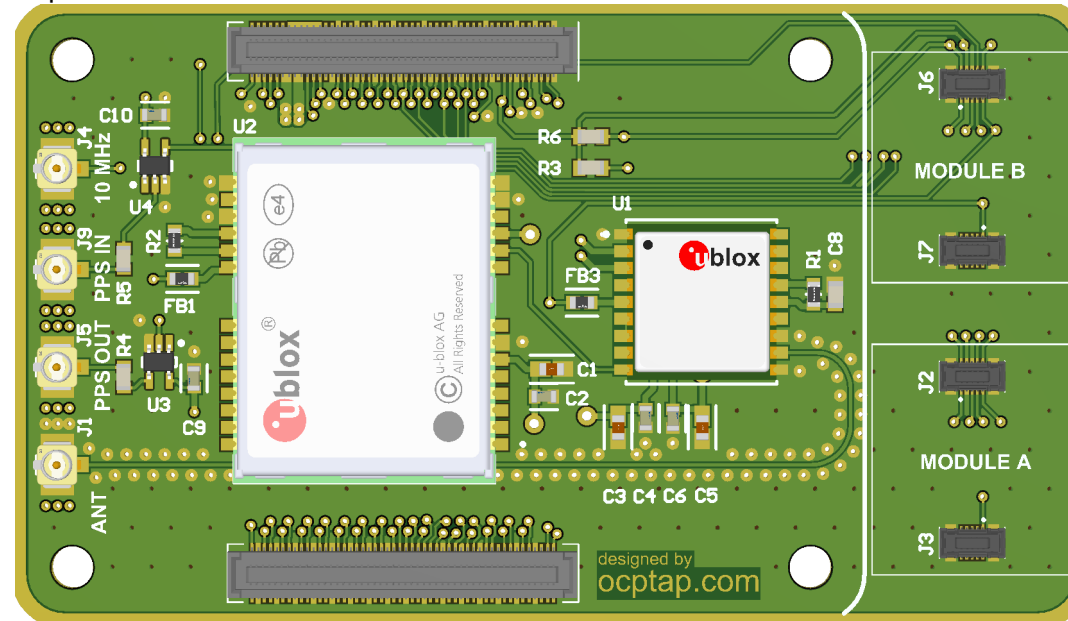
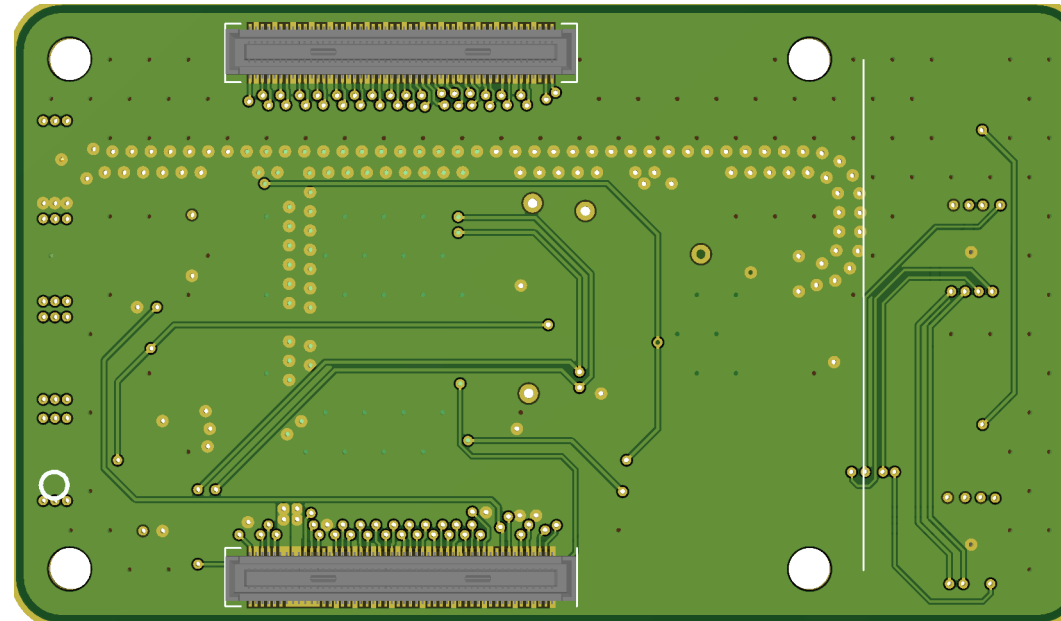


Top



Bottom



**Time4CM4**  
**V1 - ocptap.com**

NOTES: UNITS IN MILLIMETERS UNLESS OTHERWISE SPECIFIED

1. INTERPRET THIS DRAWING IN ACCORDANCE WITH IPC-D-325A
2. BOARD FABRICATION AND QUALITY PER IPC-6012, CLASS 2, EXCEPT SPECIFIED HEREIN
3. MUST COMPLY WITH EUROPEAN DIRECTIVE 2002/95/EC (RoHS)
4. DIMENSIONAL LIMITS APPLY AFTER PLATING OR COATING
5. BOW AND TWIST MAXIMUM IS 0.75%
6. MATERIAL: LAMINATE AND PREPREG SHALL BE IN ACCORDANCE WITH IPC-4101/21. 170 DEGREES CELSIUS MINIMUM Tg, UL 94V-0
7. STACKUP SUMMARY:
  - A. NUMBER OF COPPER LAYERS: 4
  - B. BOARD THICKNESS SHALL BE 62mil +/- 10%
  - C. COPPER: See Layer Stack
  - D. DEFAULT TRACE/SPACE: 6mil / 6mil
  - E. CONDUCTOR WIDTH TOLERANCE = +/- 0.01mm
8. SURFACE FINISH/PLATING:
  - A. BOARD SHALL BE IMMERSION GOLD PLATED (ENIG) ACCORDING TO IPC-4552. THICKNESS SHALL BE A MINIMUM OF 0.05µm GOLD OVER 3-6µm NICKEL
9. SOLDERMASK WITH LIQUID PHOTO IMAGEABLE (LPI) PER IPC-SM-840C, CLASS T. COLOR: MATTE BLACK
10. SILKSCREEN PER SUPPLIED ARTWORK WITH ORGANIC, NON-CONDUCTIVE, EPOXY INK. SILKSCREEN MAY BE TRIMMED OFF ANY SOLDERABLE ENTITY. COLOR: WHITE
11. 100% BARE BOARD ELECTRICAL TEST TO BE DONE WITH REFERENCE TO SUPPLIED NETLIST
12. LOCATE MANUFACTURER'S IDENTIFICATION AND LOT CODE ON PRIMARY SIDE FREE FROM ALL METAL ENTITY RENDERED IN SILKSCREEN.
13. DETAILS NOT SPECIFIED ARE AT MANUFACTURER'S OPTION BUT FINAL APPROVAL MUST BE OBTAINED

Layer Stack Legend

| Layer                 | Thickness | Type           | Gerber |
|-----------------------|-----------|----------------|--------|
| Top Overlay           |           | Legend         | GTO    |
| Top Solder            | 0.03mm    | Solder Mask    | GTS    |
| Top Surface Finish    | 0.02mm    | Surface Finish |        |
| Top Layer             | 0.04mm    | Signal         | GTL    |
|                       | 0.06mm    | Dielectric     |        |
|                       | 0.06mm    | Dielectric     |        |
| Int1 (GND)            | 0.04mm    | Signal         | G1     |
|                       | 1.20mm    | Dielectric     |        |
| Int2 (PWR)            | 0.04mm    | Signal         | G2     |
|                       | 0.06mm    | Dielectric     |        |
|                       | 0.06mm    | Dielectric     |        |
| Bottom Layer          | 0.04mm    | Signal         | GBL    |
| Bottom Surface Finish | 0.02mm    | Surface Finish |        |
| Bottom Solder         | 0.03mm    | Solder Mask    | GBS    |
| Bottom Overlay        |           | Legend         | GBO    |

Total thickness: 1.6mm

A

B

C

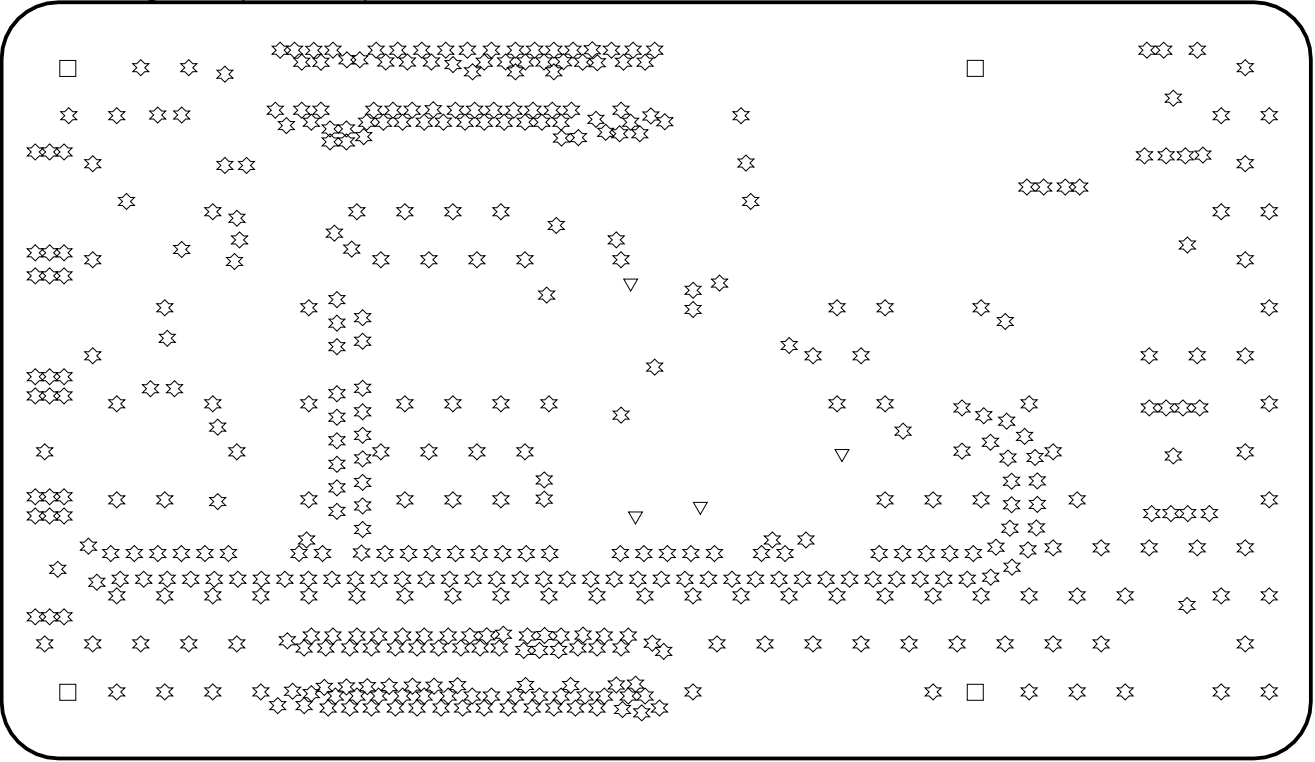
D

E

1

1

Drill Drawing View (Scale 5:2)



2

2

3

3

Drill Table

| Symbol | Count     | Hole Size | Plated | Hole Tolerance |
|--------|-----------|-----------|--------|----------------|
| ☆      | 469       | 0.30mm    | Plated |                |
| ▽      | 4         | 0.61mm    | Plated |                |
| □      | 4         | 2.70mm    | Plated |                |
|        | 477 Total |           |        |                |

4

4

A

B

C

D

E