



Transmission Line Structure Table

Impedance Id	Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Wide Trace Width	Narrow Trace Width	Gap	Reference layers	Substack	Clearance	Target Tolerance
1	Edge-Coupled Coated Microstrip	90	88.55	Top Layer	0.17mm	0.17mm	0.10mm	Int1 (GND)	Board Layer Stack	0.13mm	10%
2	Edge-Coupled Coated Microstrip	100	98.05	Top Layer	0.12mm	0.12mm	0.10mm	Int1 (GND)	Board Layer Stack	0.13mm	10%
3	Coated Microstrip	50	49.93	Top Layer	0.35mm	0.35mm		Int1 (GND)	Board Layer Stack	0.13mm	10%
4	Offset Stripline	50	49.52	Int1 (GND)	0.28mm	0.28mm		Top Layer,Int2 (PWR)	Board Layer Stack	0.13mm	10%
5	Offset Stripline	50	49.52	Int2 (PWR)	0.28mm	0.28mm		Int1 (GND),Bottom Layer	Board Layer Stack	0.13mm	10%
6	Edge-Coupled Coated Microstrip	90	88.55	Bottom Layer	0.17mm	0.17mm	0.10mm	Int2 (PWR)	Board Layer Stack	0.13mm	10%
7	Edge-Coupled Coated Microstrip	100	98.05	Bottom Layer	0.12mm	0.12mm	0.10mm	Int2 (PWR)	Board Layer Stack	0.13mm	10%
8	Coated Microstrip	50	49.93	Bottom Layer	0.35mm	0.35mm		Int2 (PWR)	Board Layer Stack	0.13mm	10%

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
	Surface Material	0.03mm	SM-001	Solder Mask	GTS
PbSn	Top Surface Finish	0.02mm		Surface Finish	
CF-004	Top Layer	0.04mm		Signal	GTL
Prepreg		0.21mm	PP-017	Dielectric	
CF-004	Int1 (GND)	0.02mm		Signal	G1
Core		1.06mm	Core-039	Dielectric	
CF-004	Int2 (PWR)	0.02mm		Signal	G2
Prepreg		0.21mm	PP-017	Dielectric	
CF-004	Bottom Layer	0.04mm		Signal	GBL
PbSn	Bottom Surface Finish	0.02mm		Surface Finish	
Surface Material	Bottom Solder	0.03mm	SM-001	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
Total thickness: 1.68mm					

Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
▽	28	0.15mm	Plated	
▣	2	0.20mm	Plated	
⊙	87	0.30mm	Plated	
◇	2	0.95mm	Plated	
▽	10	0.97mm	Plated	
✱	4	1.10mm	Plated	
⊗	2	1.25mm	Non-Plated	
○	1	1.57mm	Plated	
✱	2	1.65mm	Plated	
▣	4	1.70mm	Plated	
⊕	2	3.25mm	Non-Plated	
144 Total				