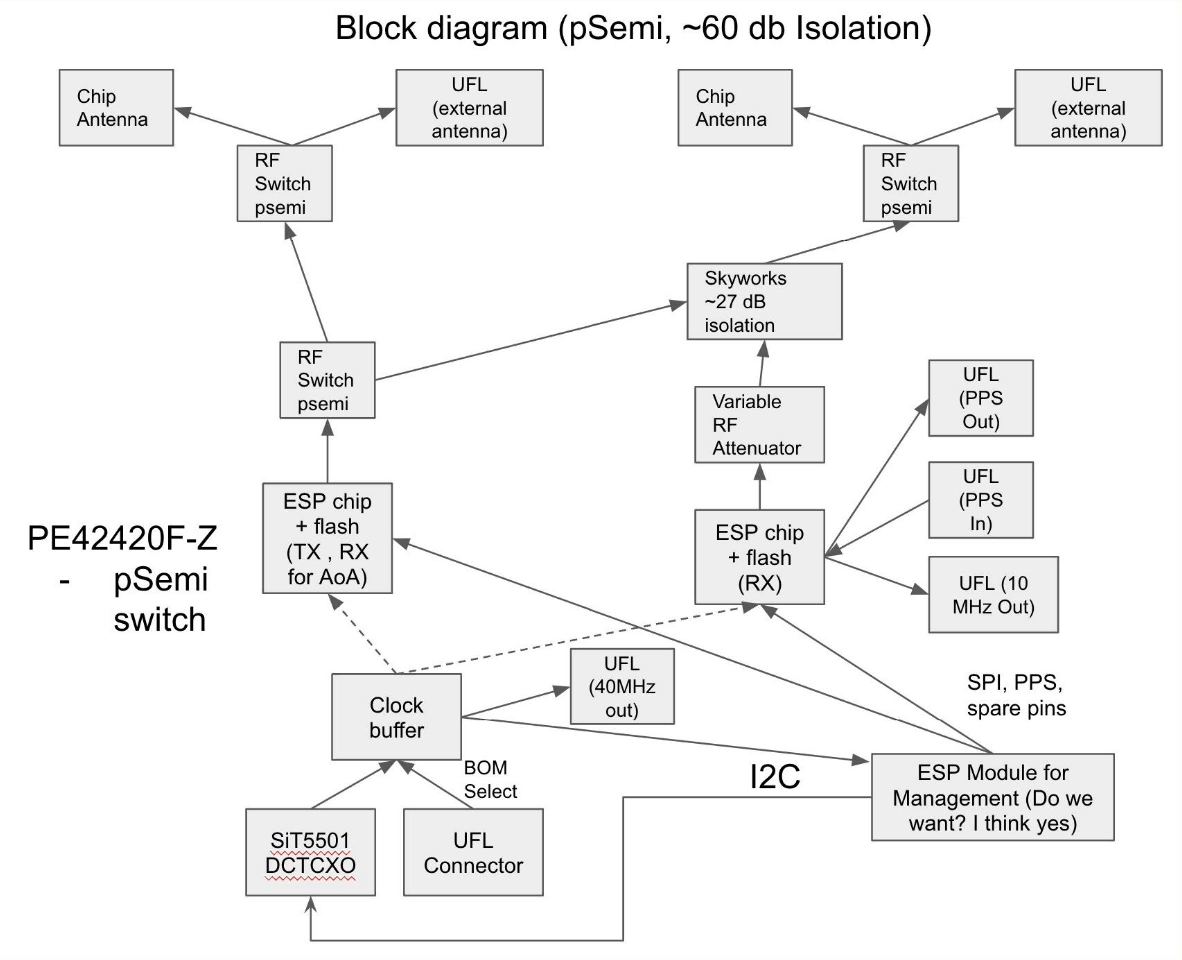
ESP-WiWi



Title
TITLE PAGE

Size: A
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File: X:\My Drive\..\TITLE PAGE.SchDoc
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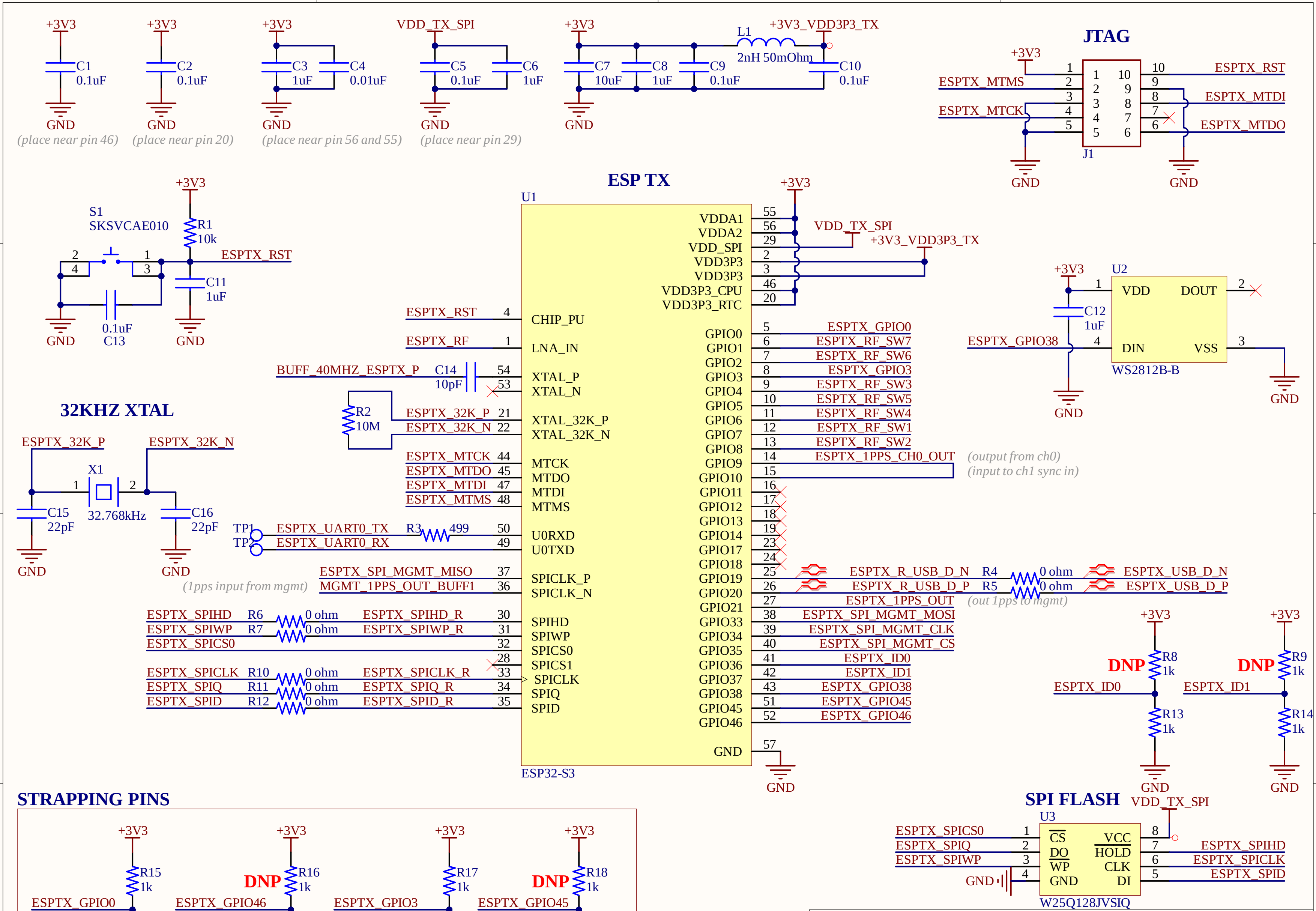

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ESP-TX

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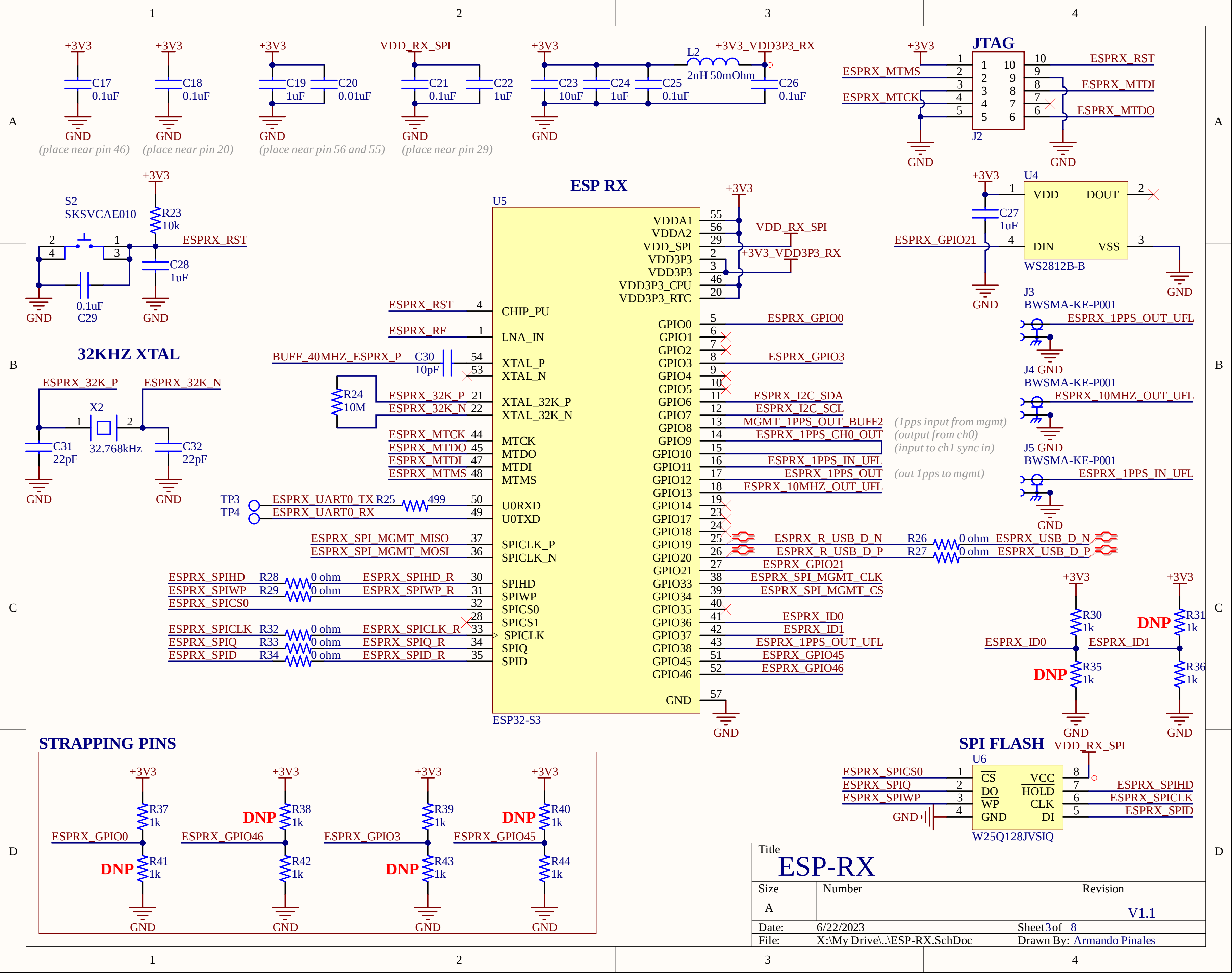

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ESP-RX

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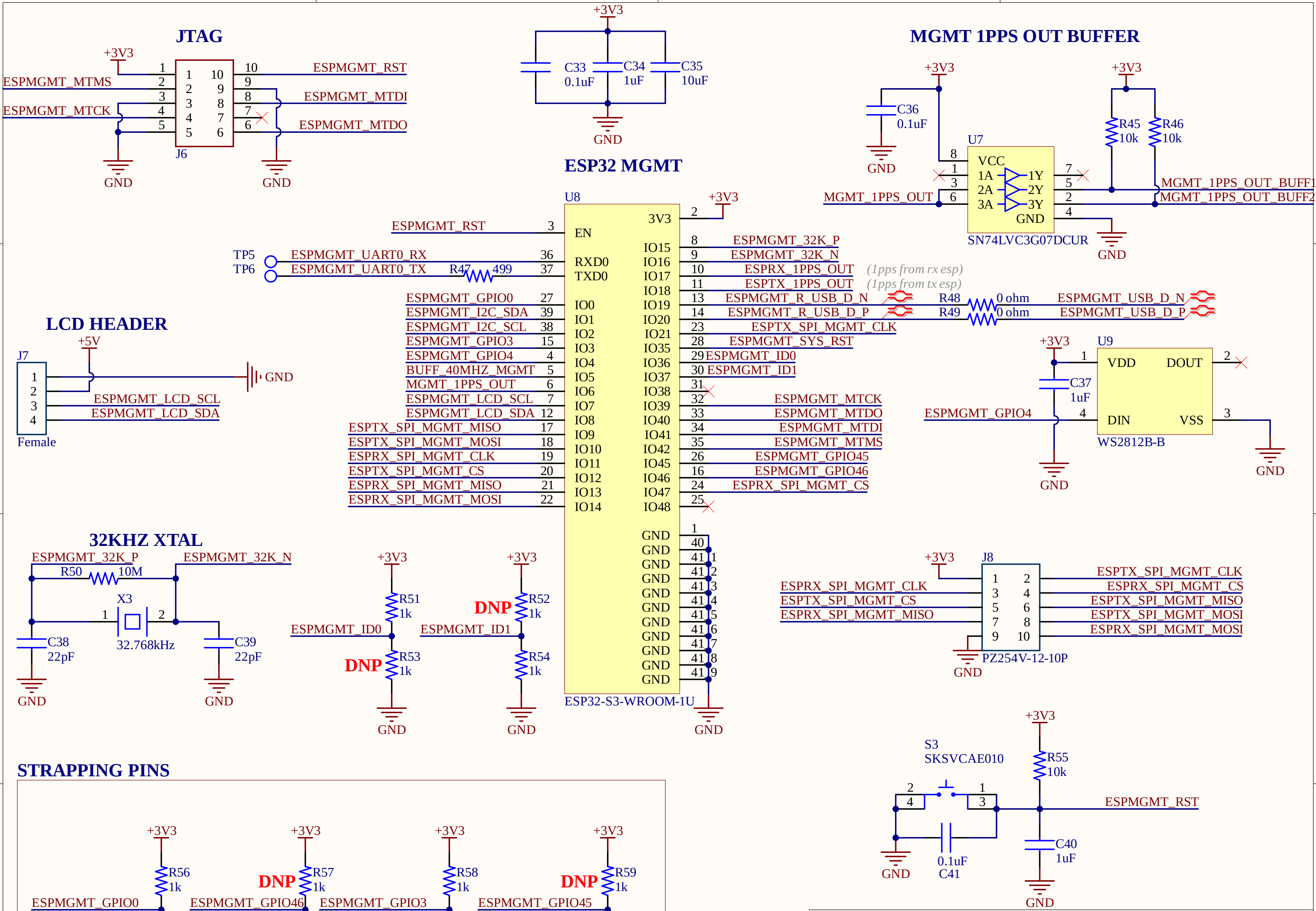
File: X:\My Drive\..\ESP-RX.SchDoc Drawn By: Armando Pinales



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ESP-MGMT		
Size A	Number	Revision V1.1
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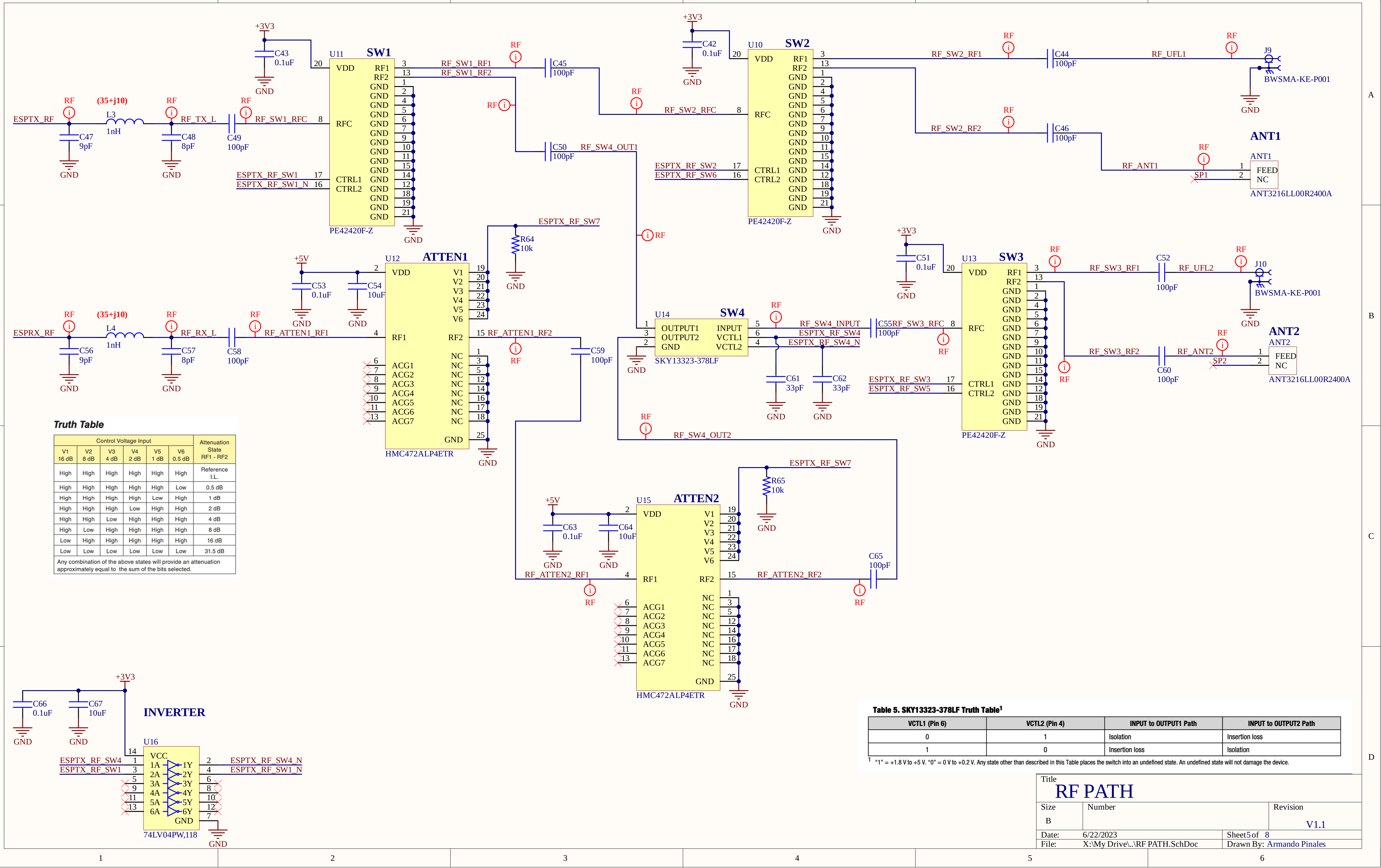

Truth Table

Control Voltage Input						Attenuation State RF1 - RF2
V1 16 dB	V2 8 dB	V3 4 dB	V4 2 dB	V5 1 dB	V6 0.5 dB	
High	High	High	High	High	High	Reference I.L.
High	High	High	High	High	Low	0.5 dB
High	High	High	High	Low	High	1 dB
High	High	High	Low	High	High	2 dB
High	High	Low	High	High	High	4 dB
High	Low	High	High	High	High	8 dB
Low	High	High	High	High	High	16 dB
Low	Low	Low	Low	Low	Low	31.5 dB
Any combination of the above states will provide an attenuation approximately equal to the sum of the bits selected.						

Table 5. SKY13323-378LF Truth Table[1]

VCTL1 (Pin 6)	VCTL2 (Pin 4)	INPUT to OUTPUT1 Path	INPUT to OUTPUT2 Path
0	1	Isolation	Insertion loss
1	0	Insertion loss	Isolation

[1] "1" = +1.8 V to +5 V. "0" = 0 V to +0.2 V. Any state other than described in this Table places the switch into an undefined state. An undefined state will not damage the device.

Title RF PATH		
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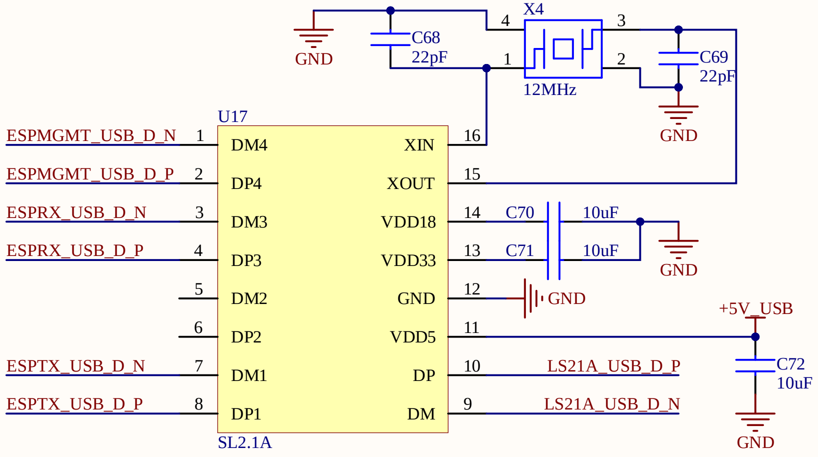
USB 2.0 HUB



Title
PROGRAMMING

Size	Number	Revision
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Drawn By: Armando Pinales

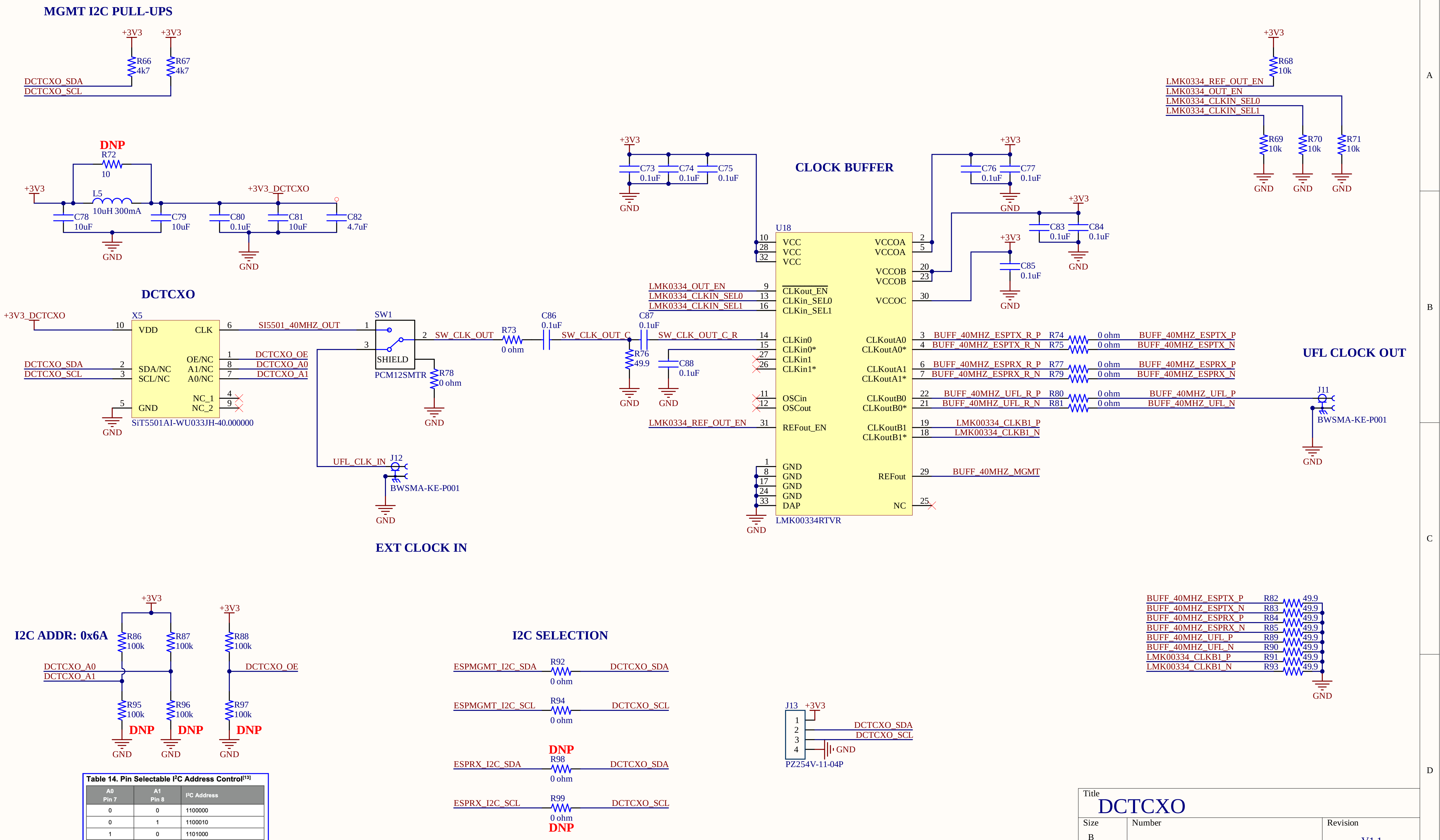

Table 14. Pin Selectable I²C Address Control[13]

A0 Pin 7	A1 Pin 8	I²C Address
0	0	1100000
0	1	1100010
1	0	1101000
1	1	1101010 (Default)

Title DCTCXO		
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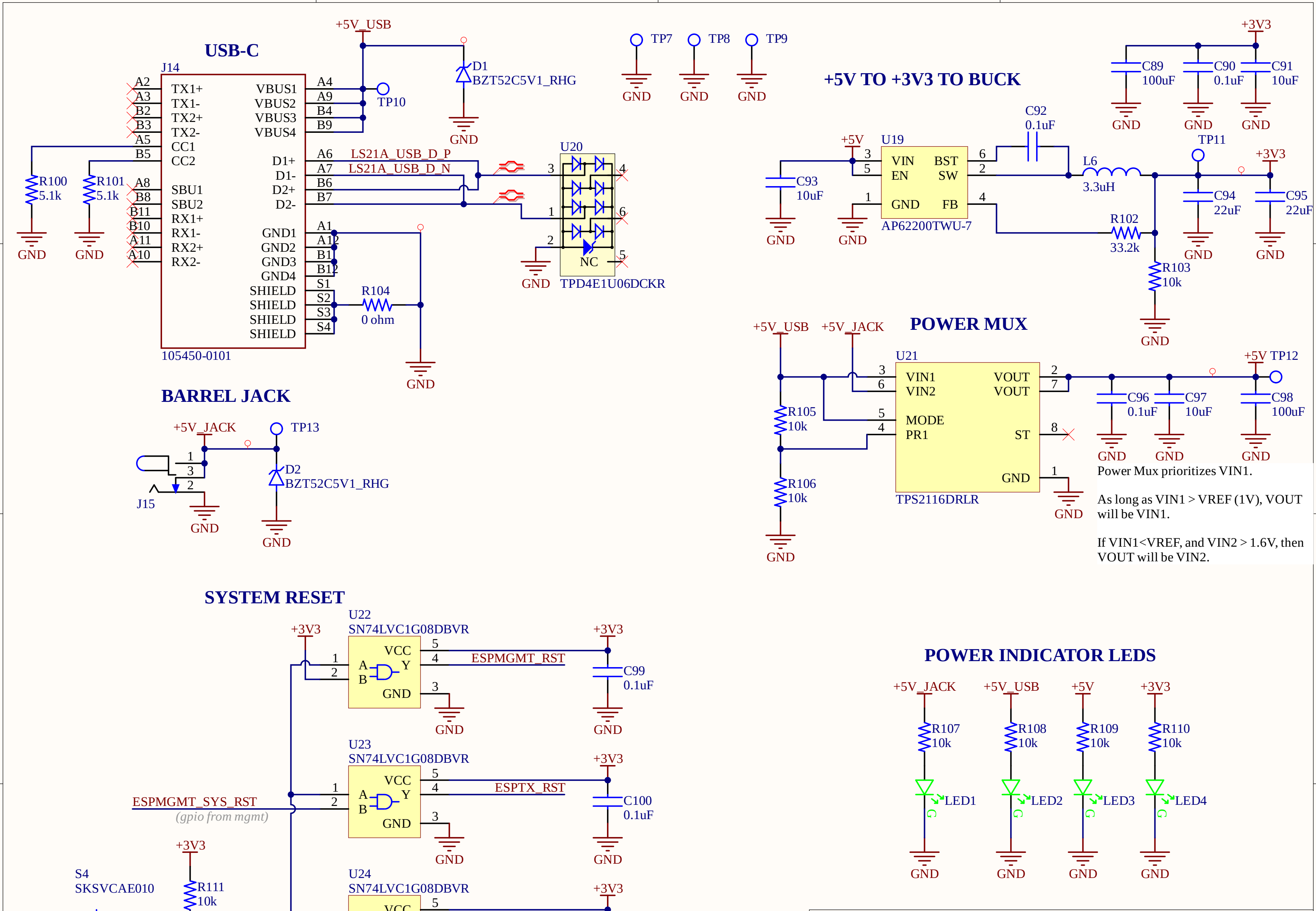

Power Mux prioritizes VIN1.

As long as VIN1 > VREF (1V), VOUT will be VIN1.

If VIN1<VREF, and VIN2 > 1.6V, then VOUT will be VIN2.

Title		
POWER		
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