AZUltra v0.01

(Preliminary) Hardware Review 0002 - Version 0

AZUltra is a microcontroller-enabled buzzer with eight user-programmable modes.

This document is intended as a review of the v0.01 hardware design. It will focus on the successes and failures of the first revision and detail a plan for future improvements if another revision is deemed necessary. This is only a review of the *hardware.*

CURRENTLY UNDER WRITING
EXPECT CHANGES

CONTENTS

CHANGELOG

V0 - Initial Commit - 10/02/26 - AZ

ABBREVIATIONS

INTRODUCTION

Context

The idea for this project arose from the disappointment of a “cricket noise maker” I ordered from aliexpress. This product was comically quiet and when placed into the intended environment - could be barely heard. Additionally, it used soldered batteries meaning it was difficult if not impossible to repair. It also was not user programmable due to a one time programmable microcontroller being used. This greatly inspired me to make my own improved version.

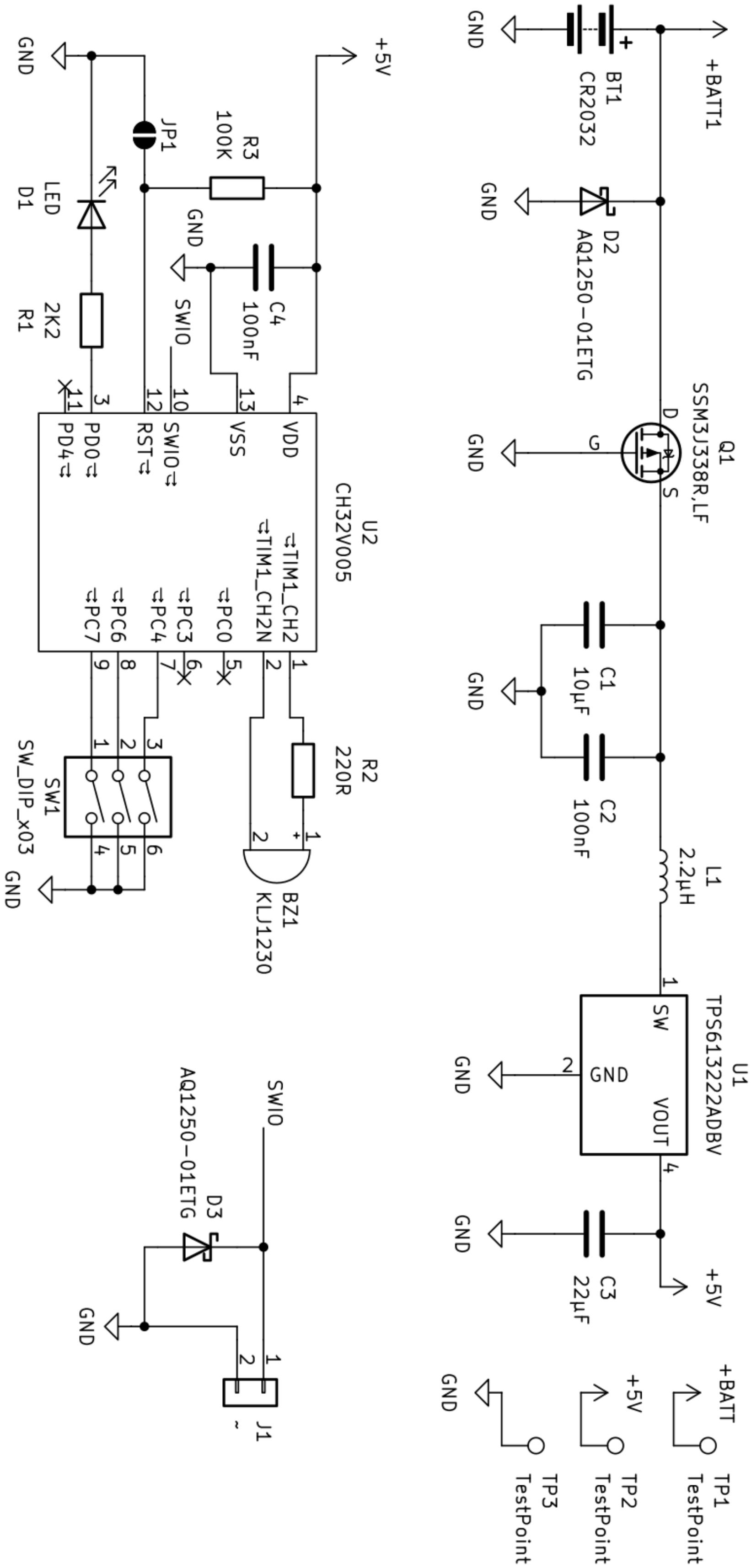
Design Intent

To design a reliable, noticeable and repairable sound maker that would surpass options currently available on the market while maintaining a small form factor and low cost of manufacture.

Specification

- At least 28 days of runtime on a single battery given one 200ms sound every 30 minutes.
- Programming capability without expensive equipment
- Versatile I/O allowing for connection to other equipment
- Peak sound loudness of at least 80dB at 10cm
- A maximum PCB size of 50mmx50mm
- All small surface mount components on one side of the board (smaller than 15x15mm)

Schematic



+BATT1
BT1
CR2032
GND
D2
AQ1250-01ETG
Q1
SSM3J338R,LF
D
G
S
C1
10µF
C2
100nF
L1
2.2µH
U1
TPS613222ADBV
SW
GND
VOUT
C3
22µF
+5V
+BATT
TP1
TestPoint
TP2
TestPoint
TP3
TestPoint
R3
100K
JP1
C4
100nF
SWIO
LED
D1
2K2
R1
U2
CH32V005
VDD
VSS
SWIO
RST
PD0
PD4
TIM1_CH2
TIM1_CH2N
PC0
PC3
PC4
PC6
PC7
R2
220R
BZ1
KLJ1230
SW1
SW_DIP_x03
D3
AQ1250-01ETG
J1

REVIEW

Schematic

The circuit (as seen in the previous page) is fairly simple. I chose the power source to be a CR2032 as they are fairly power dense, commonly available, do not require charging circuitry and the holders come in a small footprint. An alternative would be the CR2450 as it has a much larger capacity, although they are more expensive, larger, and less common.

The battery (and programming I/O) has a TVS diode to prevent damage from ESD events. I also implemented the Q1 PMOS for low loss reverse polarity protection. The specified part should be substituted for a cheaper alternative if absolute cost-saving is a priority. I used the specified PMOS mostly due to the fact that I already had stock and that it had good properties. Namely, low RDS_on and gate voltage.

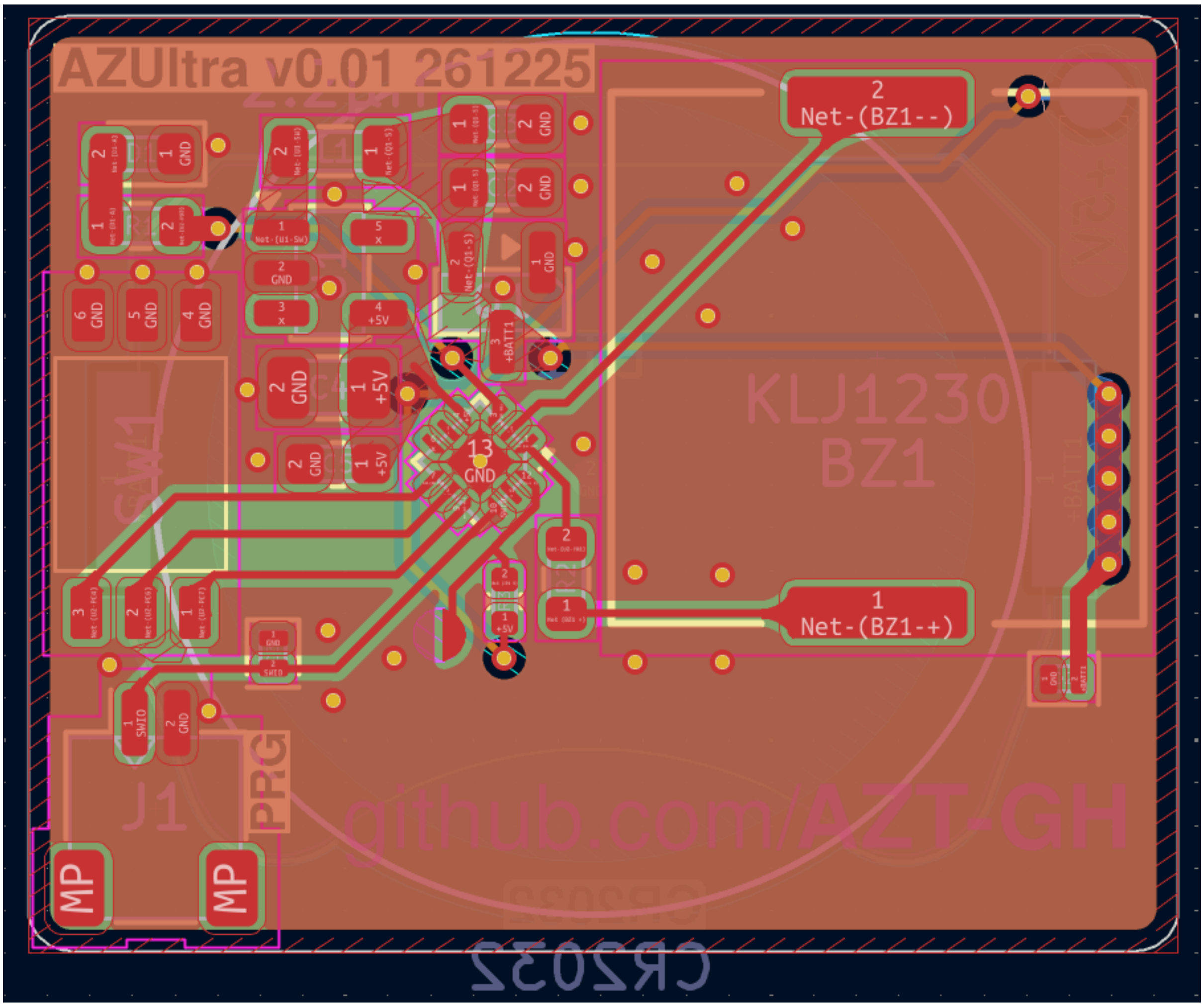
This feeds into a boost converter boosting to 5V. The input caps are fairly standard and a low ESR power inductor with sufficient saturation current should be used for L1. In worst-case scenarios (low ambient temp and low quality, old cells) the converter would switch into PFM mode and leak noise through the uc timer peripheral into the piezo causing unwanted noise. For this reason a relatively large 22uf output cap was chosen in an attempt to mitigate the noise while keeping a relatively small package and cost. The boost converter was chosen as it had acceptable quiescent current and high efficiency . All with a very low bom count and implementation difficulty. A system voltage of 5V was chosen as it was near the maximum acceptable operating voltage for the uc and enabled the piezo to stay at a consistent (and higher) loudness. There are also three test points to easily measure if the power is working.

Another reason for the development of this board was to test the CH32v00x platform of very cost efficient, feature-packed microcontrollers. The CH32v005 is one of the newer revisions and costs close to £0.25 per unit. They are available in a large range of packages, in this case I chose a very small QFN-12 unit. As for passives all it requires is a 100nF decoupling cap and a pull up on the reset pin. I chose a 100k pull up to balance noise immunity and power losses, I implemented the reset as an unlabelled jumper that can be bridged with a screwdriver or similar. This is so that the device cannot be reset by third parties (without operating knowledge) in operation which is very useful for my use case. Ironically, this document removes that layer of security but if whoever finds this goes to the length of reading this then they deserve to know how to reset the board.

I dedicated one GPIO pin to an LED that can be used to verify programming and then can be used by the user for whatever purposes they desire. I also used three GPIO pins for a 2 way dip switch to enable the user to switch between up to 8 modes on the fly. This could very easily be switched out for a female connector to enable for the device to act on information from other devices (or expansion boards). As for connectors, the programming connector is a common JST-SH, Only two terminals are required due to the unique single wire programming process (data and ground).

Finally, I chose a piezo buzzer due to their low current consumption and high loudness. The buzzer is connected push-pull to the complementary outputs of the advanced timer, timer1 on the ch32. This doubles the peak to peak potential difference compared to single end drive which greatly increases volume (~1.4x). I also added a series resistor to reduce ringing and noise (like aforementioned) while not drastically reducing volume.

Layout



Layout PLACEHOLDER TEXT

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