

Satisfiability Checking

01 Overview

Prof. Dr. Erika Ábrahám

RWTH Aachen University
Informatik 2
LuFG Theory of Hybrid Systems

WS 23/24

01 Overview

1 Organizational

2 What is this lecture about?

- **Language:** English
- **Lecture (V3+Ü1):**
Monday 08:30-10:00 (AH III) and
Friday 08:30-10:00 (AH II)
- **Exercise (Ü1):**
Tuesday 10:30-11:15 (AH VI)
- **Room or schedule changes:**
communicated via Moodle
- **Assistants:**
Jasper Nalbach
Valentin Promies
- **Contact:**
`teaching@ths.rwth-aachen.de`

- **Weekly exercise sheets.** Not mandatory (no submission) but strongly recommended.
- **3 mandatory eTests in Moodle:** at least 8 out of $3 \times 5 = 15$ eTest points are needed for exam admission.
- **Exam:** written, 120 minutes, 120 exam points
- During lectures, **you can earn up to 12 bonus exam points!** We pose 36 questions during the lectures over the whole semester, which you can answer in Moodle. Each correct answer brings you $1/3$ exam point.
Submitting answers is possible only during the lecture!

Question board in Moodle



Question Board

✓Propositio...
Logic



✓SAT solving



✓First-order
Logic



✓Eage
solvi
Equa
Bitve

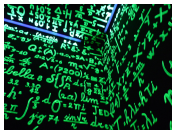
- Daniel Kroening and Ofer Strichman.
Decision Procedures: An Algorithmic Point of View.
Springer-Verlag, Berlin, 2008.
- Slides (grateful for parts from
www.decision-procedures.org/slides/)
- Selected papers and other materials (especially recordings from a
previous year's lecture) in Moodle

01 Overview

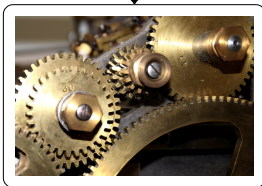
1 Organizational

2 What is this lecture about?

What is this lecture about?



Quantifier-free
logical
formula

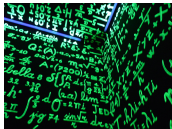


Solver

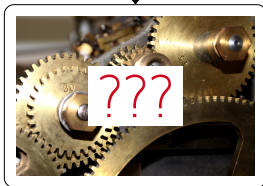


Satisfiability of the
input formula

What is this lecture about?



Quantifier-free
logical
formula

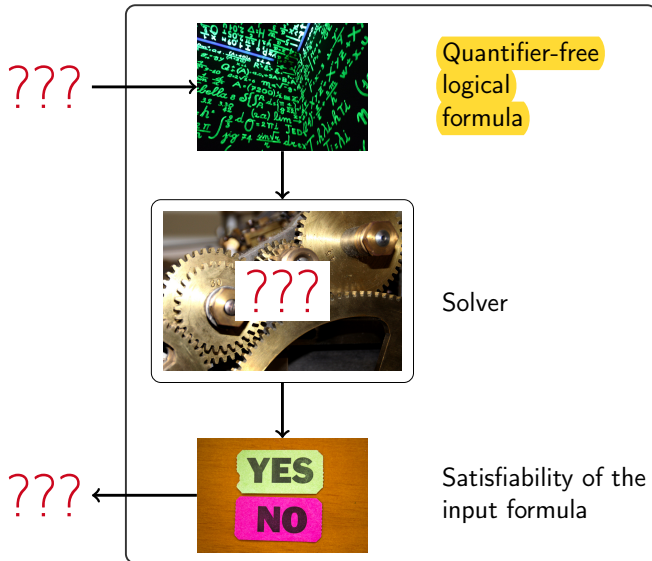


Solver



Satisfiability of the
input formula

What is this lecture about?



命题逻辑

Satisfiability problem for propositional logic

Given a formula combining some atomic propositions using the Boolean operators “and” (\wedge), “or” (\vee) and “not” (\neg), decide whether we can substitute truth values for the propositions such that the formula evaluates to true. 公式计算为真

Example

Formula: $(a \vee \neg b) \wedge (\neg a \vee b \vee c)$

Satisfying assignment: $a = \text{true}, b = \text{false}, c = \text{true}$

It is the perhaps most well-known NP-complete problem [Cook, 1971] [Levin, 1973].

SMT就是SAT (布尔表达式可满足性理论) 在布尔表达式的基础上拓展到其他理论, 比如加上实数理论

Satisfiability modulo theories problem (informal)

Given a Boolean combination of constraints from some theories, decide whether we can substitute (type-correct) values for the (theory) variables such that the formula evaluates to true.

A non-linear real arithmetic example

Formula: $(x - 2y > 0 \vee x^2 - 2 = 0) \wedge x^4 y + 2x^2 - 4 > 0$

Satisfying assignment: $x = \sqrt{2}, \quad y = 2$

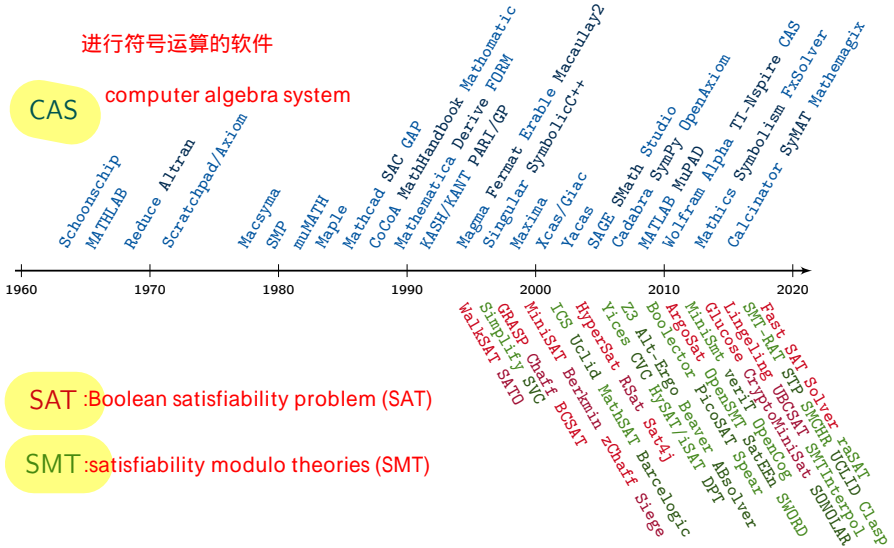
Hard problems... non-linear integer arithmetic is even undecidable.

Tool development (not exhaustive)

进行符号运算的软件

CAS

computer algebra system



Satisfiability checking for propositional logic

Success story: SAT-solving

- Practical problems with millions of variables are solvable.
- Frequently used in different research areas for, e.g., analysis, synthesis and optimisation.
- Also massively used in industry for, e.g., digital circuit design and verification.

Satisfiability checking for propositional logic

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Community support:

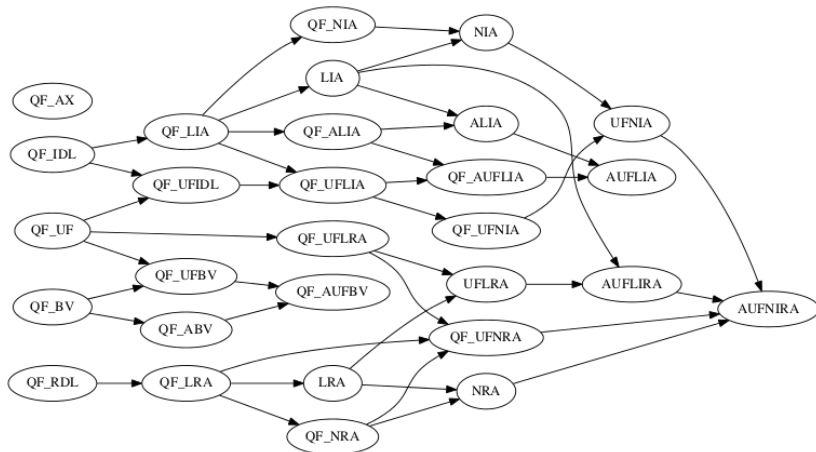
- **Standardised input language**, lots of **benchmarks** available.
- **Competitions** since 2002.
2021: 4 tracks, 45 versions of 18 solvers in main track
SAT Live! forum as community platform, dedicated conferences, journals, etc.

Satisfiability modulo theories solving

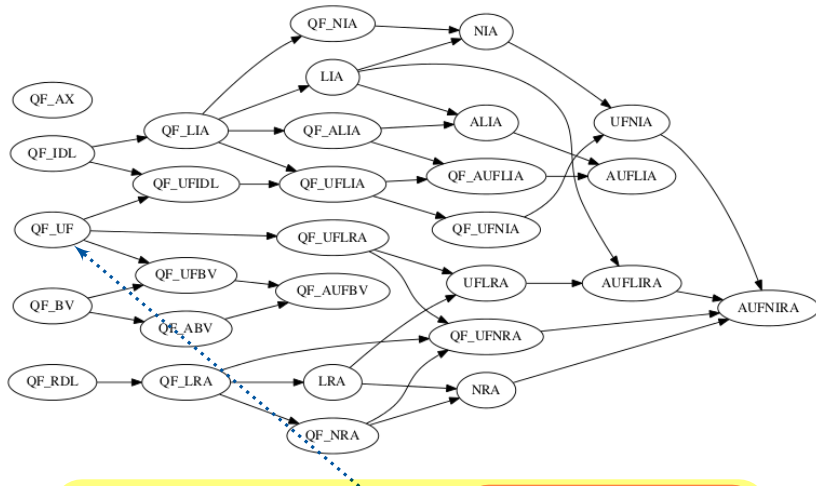
- Propositional logic is sometimes too weak for modelling.
- We need more expressive logics and decision procedures for them.
- Logics:
quantifier-free fragments of first-order logic over various theories.
- Our focus: SAT-modulo-theories (SMT) solving.

- Propositional logic is sometimes too weak for modelling.
- We need more expressive **logics** and **decision procedures** for them.
- Logics:
quantifier-free fragments of first-order logic over various theories.
- Our focus: **SAT-modulo-theories (SMT)** solving.
- **SMT-LIB** as **standard input language** since 2004.
- **Competitions** since 2005.
- **2021 SMT-COMP** competition: 25 solvers

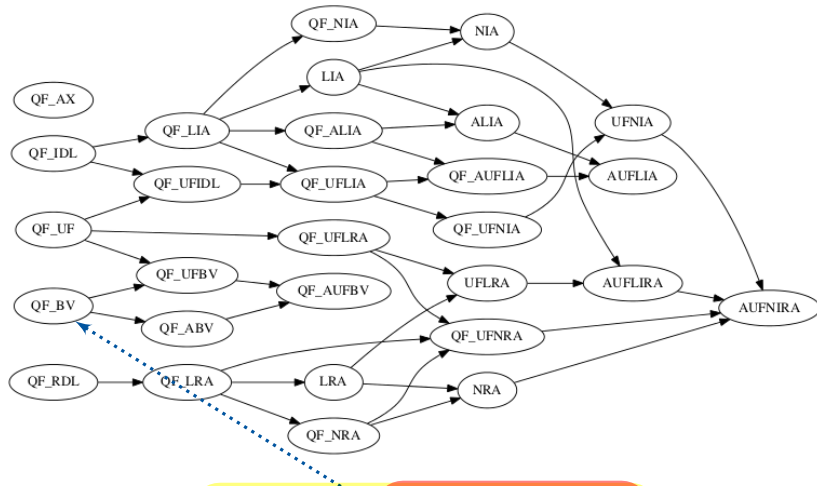
SMT-LIB theories



Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

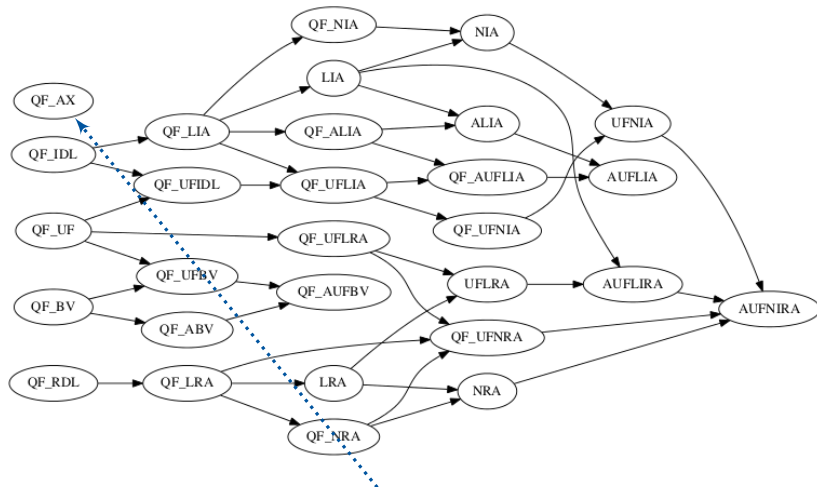


Quantifier-free equality logic with uninterpreted functions
 $(a = c \wedge b = d) \rightarrow f(a, b) = f(c, d)$



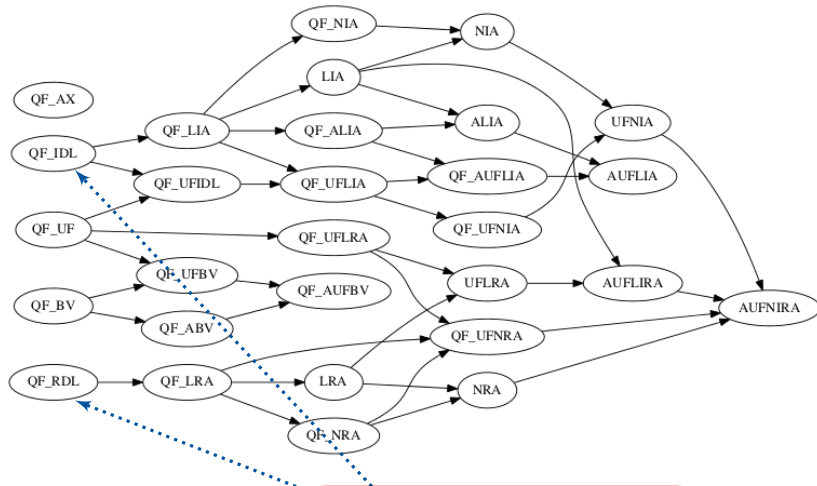
Quantifier-free bit-vector arithmetic
 $a + b \geq 0 \wedge (a|b) \leq (a \& b)$

SMT-LIB theories



Quantifier-free array theory
 $i = j \rightarrow \text{read}(\text{write}(a, i, v), j) = v$

Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

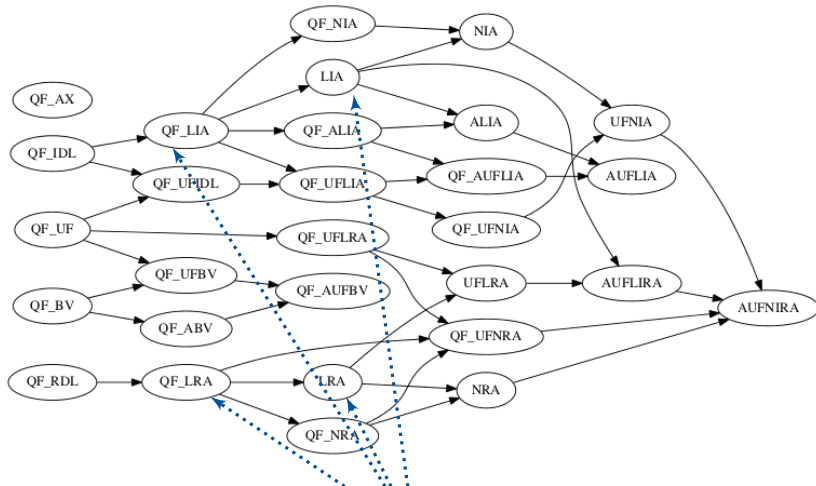


Quantifier-free integer/rational difference logic

$$x - y \geq 0 \vee x - z < 0$$

Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

SMT-LIB theories

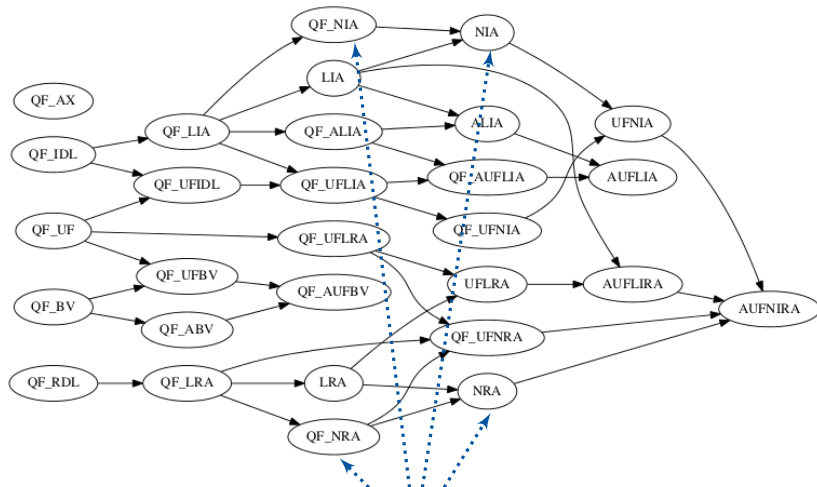


(Quantifier-free) real/integer linear arithmetic

$$4x + 7y = 8 \wedge (y = 0 \vee x > y)$$

Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

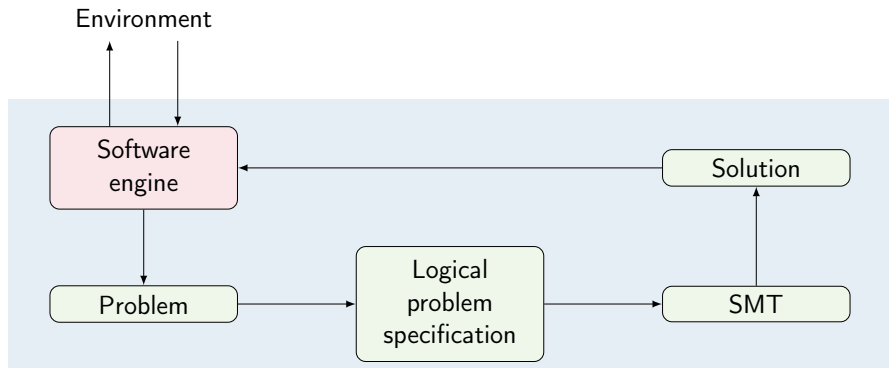
SMT-LIB theories



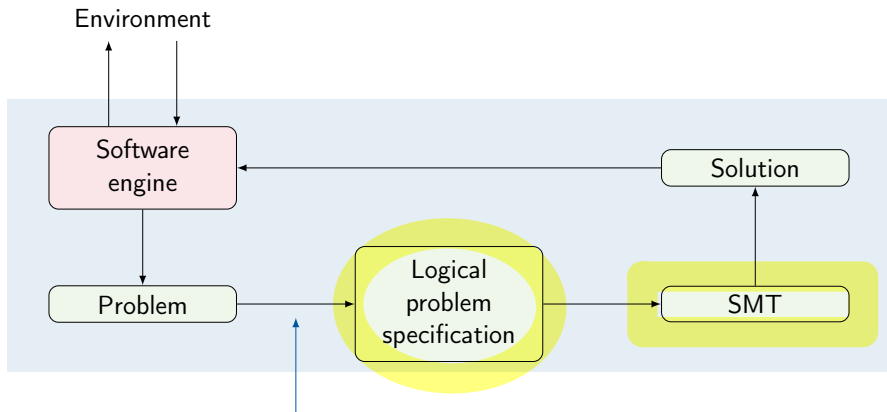
(Quantifier-free) real/integer non-linear arithmetic
 $x^2 + 2xy + y^2 > 0 \vee (x \geq 1 \wedge xz + yz^2 = 0)$

Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

SAT/SMT embedding structure

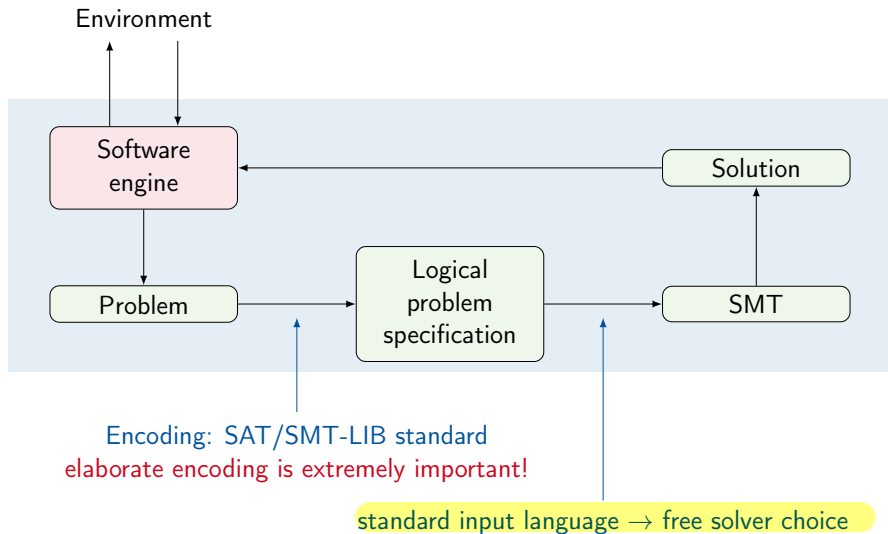


SAT/SMT embedding structure



Encoding: SAT/SMT-LIB standard
elaborate encoding is extremely important!

SAT/SMT embedding structure



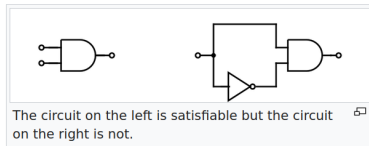
Application example: Circuit satisfiability

Circuit satisfiability problem

From Wikipedia, the free encyclopedia

In [theoretical computer science](#), the **circuit satisfiability problem** (also known as **CIRCUIT-SAT**, **CircuitsAT**, **CSAT**, etc.) is the [decision problem](#) of determining whether a given [Boolean circuit](#) has an assignment of its inputs that makes the output true.^[1] In other words, it asks whether the inputs to a given Boolean circuit can be consistently set to **1** or **0** such that the circuit outputs **1**. If that is the case, the circuit is called *satisfiable*. Otherwise, the circuit is called *unsatisfiable*. In the figure to the right, the left circuit can be satisfied by setting both inputs to be **1**, but the right circuit is unsatisfiable.

CircuitSAT is closely related to [Boolean satisfiability problem \(SAT\)](#), and likewise, has been proven to be [NP-complete](#).^[2] It is a prototypical NP-complete problem; the [Cook-Levin theorem](#) is sometimes proved on CircuitSAT instead of on the SAT and then reduced to the other satisfiability problems to prove their NP-completeness.^{[1][3]} The satisfiability of a circuit containing m arbitrary binary gates can be decided in time $O(2^{0.4058m})$.^[4]



Source: Wikipedia.

Application example: Symbolic execution

Program 1.2.1 A recursion-free program with bounded loops and an SSA unfolding.

```
int Main(int x, int y)
{
    if (x < y)
        x = x + y;
    for (int i = 0; i < 3; ++i) {
        y = x + Next(y);
    }
    return x + y;
}

int Next(int x) {
    return x + 1;
}
```

```
int Main(int x0, int y0)
{
    int x1;
    if (x0 < y0)
        x1 = x0 + y0;
    else
        x1 = x0;
    int y1 = x1 + y0 + 1;
    int y2 = x1 + y1 + 1;
    int y3 = x1 + y2 + 1;
    return x1 + y3;
}
```

$$\exists x_1, y_1, y_2, y_3 \left((x_0 < y_0 \implies x_1 = x_0 + y_0) \wedge (\neg(x_0 < y_0) \implies x_1 = x_0) \wedge \right. \\ \left. y_1 = x_1 + y_0 + 1 \wedge y_2 = x_1 + y_1 + 1 \wedge y_3 = x_1 + y_2 + 1 \wedge \right. \\ \left. result = x_1 + y_3 \right)$$

Source: Nikolaj Bjørner and Leonardo de Moura. *Applications of SMT solvers to Program Verification*.

Rough notes for SSFT 2014.

Application example: Bounded model checking



**Bounded Model Checking
for Software**



CBMC About CBMC

CBMC is a Bounded Model Checker for C and C++ programs. It supports C89, C99, most of C11 and most compiler extensions provided by gcc and Visual Studio. It also supports [SystemC](#) using [Scoot](#). We have recently added experimental support for Java Bytecode.

CBMC verifies array bounds (buffer overflows), pointer safety, exceptions and user-specified assertions. Furthermore, it can check C and C++ for consistency with other languages, such as Verilog. The verification is performed by unwinding the loops in the program and passing the resulting equation to a decision procedure.



While CBMC is aimed for embedded software, it also supports dynamic memory allocation using `malloc` and `new`. For questions about CBMC, contact [Daniel Kroening](#).

CBMC is available for most flavours of Linux (pre-packaged on Debian, Ubuntu and Fedora), Solaris 11, Windows and MacOS X. You should also read the [CBMC license](#).

CBMC comes with a built-in solver for bit-vector formulas that is based on MiniSat. As an alternative, CBMC has featured support for external SMT solvers since version 3.3. The solvers we recommend are (in no particular order) [Boolector](#), [MathSAT](#), [Yices 2](#) and [Z3](#). Note that these solvers need to be installed separately and have different licensing conditions.

Source: D. Kroening. **CBMC home page**. <http://www.cprover.org/cbmc/>

Application example: Bounded model checking



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Logical encoding of finite unsafe paths

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Encoding idea: $Init(s_0) \wedge Trans(s_0, s_1) \wedge \dots \wedge Trans(s_{k-1}, s_k) \wedge Bad(s_0, \dots, s_k)$

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CBMC is a Solaris 11

CBMC co alternative

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Application examples:
Error localisation and explanation
Equivalence checking
Test case generation
Worst-case execution time

location

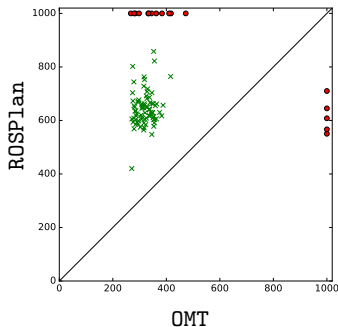
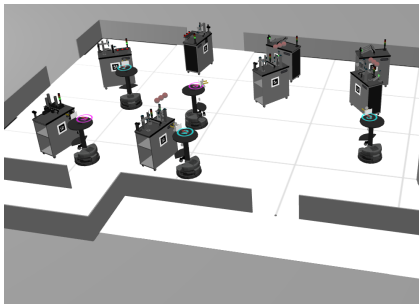
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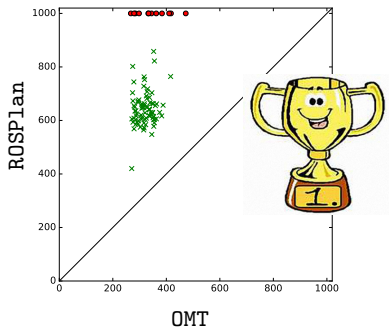
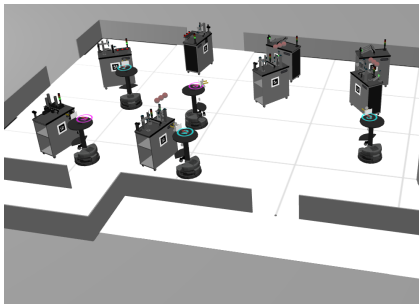
Source: D. Kroening. **CBMC home page**. <http://www.cprover.org/cbmc/>

Planning




Source: F. Leofante, E. Giunchiglia, E. Ábrahám, A. Tacchella. **Optimal Planning Modulo Theories**. Proc. of IJCAI'20.

Planning



Source: F. Leofante, E. Giunchiglia, E. Ábrahám, A. Tacchella. **Optimal Planning Modulo Theories**. Proc. of IJCAI'20.

Application example: Security at Amazon



amazon | science

Research areas ▾ Blog News and features ▾ Publications Conferences Collaborations ▾ Careers ▾

AUTOMATED REASONING

A billion SMT queries a day

CAV keynote lecture by the director of applied science for AWS Identity explains how AWS is making the power of automated reasoning available to all customers.

By [Neha Rungta](#) [Share](#)


August 18, 2022










Conference




[FLoC 2022](#)

At this year's Computer-Aided Verification (CAV) conference — a leading automated-reasoning conference collocated with the Federated Logic Conferences ([FLoC](#)) — Amazon's Neha Rungta delivered a keynote talk in which she suggested that innovations at Amazon have "ushered in the golden age of automated reasoning".

Application example: Z3 from Microsoft



 **Z3Prover / z3** Public

















 **Code**  **Issues** 121  **Pull requests** 2  **Discussions**  **Actions**  **Projects** 1  **Wiki**  **Security**  **Insights**

 **master**  **11 branches**  **35 tags**

Go to file


Code


 **NikolajBjorner** wip - proofs ✖ fcecd6 1 hour ago  **17,096 commits**


| | | |
|--|--|---------------|
|  .github | Update msvc-static-build.yml | 16 days ago |
|  cmake | cmake: Cleanup remnants of workaround for USES_TERMINAL. | 2 months ago |
|  contrib | Remove contrib/cmake. | 2 months ago |
|  doc | include global parameters and fixup for HTML meta-characters | 2 months ago |
|  docker | Update docker-image.yml (#5739) | 10 months ago |
|  examples | revert update to netcoreapp version | 28 days ago |
|  noarch | follow instructions from #1879 | 4 years ago |
|  resources | Publishing SNK file private key for reproducible builds | 3 years ago |
|  scripts | update dependencies for build | 12 days ago |
|  src | wip - proofs | 1 hour ago |
|  .dockerignore | [TravisCI] Implement TravisCI build and testing infrastructure for Linux | 5 years ago |
|  .gitattributes | set text default to auto to try to avoid crlf disasters | 9 years ago |
|  .gitignore | bug fixes to mod/div quantifier elimination features | 2 months ago |
|  CMakeLists.txt | Add option 'MSVC_STATIC' (#6358) | 18 days ago |
|  LICENSE.txt | update license for space/quotes per #982 | 6 years ago |
|  Parameters.md | parameters neatified | 2 months ago |


About


The Z3 Theorem Prover

 [Readme](#)


 [View license](#)

 **8.1k stars**

 **180 watching**


 **1.3k forks**

Releases 33








 **z3-4.11.2** Latest
on Sep 4






[+ 32 releases](#)

Packages 1

 **z3**

Contributors 240

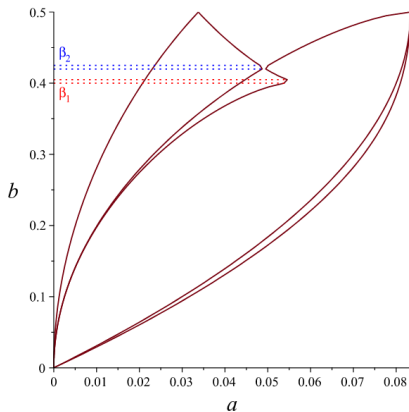
      

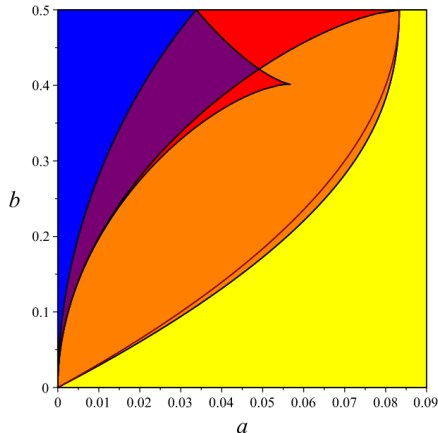
[+ 229 contributors](#)

ps://github.com/Z3Prover/z3/find/master

Application example: Biology



Numeric computation



Symbolic computation

Source: Röst, Gergely, and AmirHosein Sadeghimanesh. 'Exotic Bifurcations in Three Connected Populations with Allee Effect'. *International Journal of Bifurcation and Chaos* 31, no. 13 (October 2021): 2150202. <https://doi.org/10.1142/S0218127421502023>.

- What is the satisfiability checking problem?
- How can SAT/SMT solvers be used in applications?