

Computer Organization of Architecture (KCS-302)

Assignment on UNIT-5 (Input-Output Organization)

S. No.	Assignment	CO's / Bloom's Level
1	What do you mean by vector interrupt? Explain. (2020-21)	(CO5 / K2 Understanding)
2	Write short note on RISC. (2017-18, 2020-21)	(CO3 / K2 Understanding)
3	Explain the term cycle stealing. (2018-19, 2020-21)	(CO5 / K2 Understanding)
4	Differentiate between Daisy chaining and centralized parallel arbitration. (2018-19, 2021-22)	(CO5 / K2 Understanding)
5	What is the transfer rate of an eight-track magnetic tape whose speed is 120 inches per second and whose density is 1600 bits per inch? (2021-22)	(CO5 / K3 Applying)
6	What is I/O control method? (2019-20)	(CO5 / K1 Remembering)
7	What is Bus Arbitration? (2019-20)	(CO5 / K2 Understanding)
8	a. Discuss the design of typical input and output interface. b. What are the interrupt? How they are handled? (2019-20)	(CO5 / K2 Understanding)
9	Explain the difference between vectored and non-vectored interrupt with example of each. (2018-19)	(CO5 / K2 Understanding)
10	Explain all phases of instruction cycle. (2018-19)	(CO5 / K2 Understanding)
11	Why read and write lines in DMA are bidirectional? (2018-19)	(CO5 / K2 Understanding)
12	Write down the difference between isolated I/O and memory mapped I/O. Also discuss the advantages and disadvantages of isolated I/O and memory mapped I/O. (2019-20)	(CO5 / K2 Understanding)

13	Draw and explain the block diagram of typical DMA controller. Describe how DMA is used to transfer the data from peripherals? (2018-19, 2019-20, 2020-21)	(CO5 / K2 Understanding)
14	What do you mean by asynchronous data transfer? Explain strobe control and hand shaking mechanism. (2020-21, 2021-22)	(CO5 / K2 Understanding)
15	Discuss the different modes of data transfer. (2020-21)	(CO5 / K1 Remembering)
16	Explain how the computer buses can be used to communicate with memory and I/O. Also draw the block diagram for CPU-IOP communication. (2021-22)	(CO5 / K2 Understanding)