CSE2005	OPE	RATING SYSTEMS	
			2 0 2 4 4
Pre-requisite	NIL		Syllabus version
~ ~ ~ ~ ~ ~ ~			v1.0
Course Objective			
		ng system concepts an	nd designs and provide the skills
-	implement the services.	CI: 4: 1: 4:	
			in large scale system design.
3. To develop	the knowledge for appli	cation of the various of	design issues and services.
Expected Course	Outcome		
_	ne evolution of OS function	nality structures and	lavers
		•	of various process states.
	nodel scheduling algorith		
			and synchronization techniques.
	page replacement algori	-	•
segmentat	1 0 1	, , , , , , , , , , , , , , , , , , ,	r
_		olying different alloca	ation and access techniques.
	•	•	is Operating system tasks and the
	lgorithms for enumeratin	<u> </u>	1 0 7
Student Learning	g Outcomes (SLO): 2,	14, 17	
Module:1 Intro	duction		2 hour
tayered, modular,	micro-kernel models) -	_	Structuring methods (monolithic ses, and resources - influence of
security, networki	ng, multimedia.	_	ses, and resources - influence o
security, networki Module:2 OS I	ng, multimedia. Principles	Abstractions, proces	ses, and resources - influence of 3 hour
Module:2 OS I System Calls Sys	Principles stem/Application Call In	Abstractions, proces terface - Protection	ses, and resources - influence of the ses, and resources - influence
Module:2 OS I System Calls Sys	ng, multimedia. Principles	Abstractions, proces terface - Protection	ses, and resources - influence of the ses, and resources - influence -
Module:2 OS I System Calls Sy Processes and Thi	Principles Stem/Application Call Interests - Structures (Proces	Abstractions, proces terface - Protection	3 hour User/Kernel modes - Interrupt ly List etc).
Module:2 OS I System Calls Syst	Principles Stem/Application Call Ineads - Structures (Proces	Abstractions, proces terface - Protection Control Block, Read	3 hour User/Kernel modes - Interrupt ly List etc).
Module:2 OS I System Calls Syst	rinciples Stem/Application Call Ineads - Structures (Proceseduling	Abstractions, proces terface - Protection Control Block, Reac	3 hour User/Kernel modes - Interrupt ly List etc).
Module:2 OS I System Calls Syst	Principles Stem/Application Call Ineads - Structures (Proces	Abstractions, proces terface - Protection Control Block, Reac	3 hour User/Kernel modes - Interrupt ly List etc).
Module:2 OS I System Calls Syst	rinciples Stem/Application Call Ineads - Structures (Proceseduling	Abstractions, proces terface - Protection Control Block, Reac	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation an
Module:2 OS I System Calls Syst	rinciples Stem/Application Call Ineads - Structures (Proceseduling Ing - CPU Scheduling - adlocks Deadlock Handling	Abstractions, proces terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms.	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation an
Module:2 OS I System Calls Syst	rinciples Stem/Application Call Ineads - Structures (Proces eduling ling - CPU Scheduling - adlocks Deadlock Handli currency munication Synchroni	Abstractions, proces terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms.	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation an 4 hour ing Synchronization Primitive
Module:2 OS I System Calls Syst	rinciples Stem/Application Call Ineads - Structures (Proces eduling ling - CPU Scheduling - adlocks Deadlock Handli currency munication Synchroni	Abstractions, proces terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms.	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation and and the state of
Module:2 OS I System Calls Sy Processes and Thr Module:3 Scho Processes Schedu management - De Module:4 Con Inter-process cor Semaphores - Mo	rinciples Stem/Application Call Ineads - Structures (Proces eduling ling - CPU Scheduling - adlocks Deadlock Handli currency munication Synchroni	Abstractions, proces terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms.	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation an ing Synchronization Primitive e Locks - Lock-free Coordination
Module:2 OS I System Calls Send Three Calls School Calls Send Calls	rinciples Stem/Application Call Ineads - Structures (Proces eduling ling - CPU Scheduling - adlocks Deadlock Handli currency munication Synchroni nitors - Multiprocessors a nory management anagement Memory allo	Abstractions, proces terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms. ration - Implement nd Locking - Scalable cation strategies Cac	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation an ing Synchronization Primitive Locks - Lock-free Coordination 5 hour hing -Virtual Memory Hardwar
Module:2 OS I System Calls School Calls System Calls System Calls System Calls Calls System Calls System Calls Calls System Calls Ca	rinciples Stem/Application Call Ineads - Structures (Proceseduling ling - CPU Scheduling - adlocks Deadlock Handling - adlocks Deadlock Handling - Multiprocessors a an agement Memory allowed techniques	Abstractions, proces terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms. ration - Implement nd Locking - Scalable cation strategies Cac	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation an ing Synchronization Primitive Locks - Lock-free Coordination 5 hour hing -Virtual Memory Hardwar
Module:2 OS I System Calls School Calls System Calls School Calls System Calls School Calls System Calls System Calls Calls System Calls System Calls Calls System Calls Ca	rinciples Stem/Application Call Ineads - Structures (Proceseduling ling - CPU Scheduling - adlocks Deadlock Handling - adlocks Deadlock Handling - Multiprocessors a an agement Memory allowed techniques	Abstractions, proces terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms. ration - Implement nd Locking - Scalable cation strategies Cac	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation and ing Synchronization Primitive Locks - Lock-free Coordination 5 hour hing -Virtual Memory Hardwar
Module:2 OS I System Calls Senated Interprocess Schedumanagement - De Module:4 Con Inter-process con Semaphores - Mo Module:5 Men Main Memory m TLB - Virtual Memory m TLB - Virt	rinciples Stem/Application Call Interests - Structures (Procesteduling Sing - CPU Scheduling - Adlocks Deadlock Handling Stationary Currency Stationary Communication Synchronications - Multiprocessors and Communication Synchronications - Multiprocessors - Multiproc	Abstractions, proces terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms. ration - Implement nd Locking - Scalable cation strategies Cac	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation and ing Synchronization Primitive Locks - Lock-free Coordination 5 hour hing -Virtual Memory Hardwar Page Faults Page Replacement
Module:2 OS I System Calls	rinciples Stem/Application Call Ineads - Structures (Proceseduling Ling - CPU Scheduling - Adlocks Deadlock Handling - Multiprocessors and Management Memory allocation of techniques and Set. Lemory OS techniques and Set. Lemory OS techniques and Set.	Abstractions, process terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms. Tation - Implement and Locking - Scalable Cation strategies Cac Paging Segmentation	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation an 4 hour ing Synchronization Primitive Locks - Lock-free Coordination 5 hour hing -Virtual Memory Hardwar Page Faults Page Replacemer
Module:2 OS I System Calls	rinciples Stem/Application Call Ineads - Structures (Proceseduling Ling - CPU Scheduling - Adlocks Deadlock Handling - Multiprocessors and Management Memory allocation of techniques and Set. Lemory OS techniques and Set. Lemory OS techniques and Set.	Abstractions, process terface - Protection Control Block, Read Pre-emptive non-pre- ng Mechanisms. Tation - Implement and Locking - Scalable Cation strategies Cac Paging Segmentation (Software, Server, Server, Server)	3 hour User/Kernel modes - Interrupt ly List etc). 5 hour emptive - Resource allocation and ing Synchronization Primitive Locks - Lock-free Coordination 5 hour hing -Virtual Memory Hardwar

3 hours

Module:7 File systems

	system interface - file system implementation File system recovery Journaling	g - Soft updates				
LFS	- Distributed file system.					
N/L-	L-10 C	4 1				
	lule:8 Security Protection and trends	4 hours				
	urity and Protection - Mechanism Vs Policies Access and authentication - mode					
	nory Protection Disk Scheduling - OS performance, Scaling OS - Mobile OS: For directions in Mobile OS / Multi-gore Optimization / Power officient Schedul					
rutu	re directions in Mobile OS / Multi-core Optimization /Power efficient Schedul	ing				
	Total Lecture hours: 30 hours					
	Total Lecture nours. 30 hours					
Toy	t Book(s)					
1.		conts Wiley				
1.	Abraham Silberschatz, Peter B. Galvin, Greg Gagne-Operating System Concepts, Wiley (2012).					
Reference Books						
	Ramez Elmasri, A Carrick, David Levine, Operating Systems, A Spir	ral Approach -				
1.	McGrawHill Science Engineering Math (2009).					
2.	Remzi H. Arpaci-Dusseau, Andrea C. Arpaci-Dusseau, Operating System	ns. Three Easy				
	Pieces, Arpaci-Dusseau Books, Inc (2015).	,,				
Mod	le of Evaluation: CAT / Assignment / Quiz / FAT / Project / Seminar					
	of Challenging Experiments (Indicative)					
1.	Write a boot loader - to load a particular OS say TinyOS/ KolibriOS image	3 hours				
	- code to access from BIOS to loading the OS - involves little assembly					
	code may use QEMU/virtual machines for emulation of hardware.					
2.	Allocate/free memory to processes in whole pages, find max allocatable	3 hours				
	pages, incorporate address translation into the program.					
3.	Create an interrupt to handle a system call and continue the previously	3 hours				
	running process after servicing the interrupt.					
4.	Write a Disk driver for the SATA interface. Take care to check readiness of	3 hours				
	the controller, locked buffer cache, accept interrupts from OS during the					
	period, interrupting the OS again once done and clearing buffers.					
5.	Demonstrate the use of locks in conjunction with the IDE driver.	3 hours				
6.	Run an experiment to determine the context switch time from one process	3 hours				
	to another and one kernel thread to another. Compare the findings.					
7.	Determine the latency of individual integer access times in main memory,	3 hours				
	L1 Cache and L2 Cache. Plot the results in log of memory accessed vs					
0	average latency.	2.1				
8.	Compare the overhead of a system call with a procedure call.	3 hours				
0	What is the cost of a minimal system call?	2.1				
9.	Compare the task creation times. Execute a process and kernel thread,	3 hours				
10	determine the time taken to create and run the threads.	3 hours				
10.	1					
	varying sizes of the files. Take care not to read from cached data - used the raw device interface. Draw a graph log/log plot of size of file vs average per-block time.					
	30 hours					
Mod	Total Laboratory Hours	JO HOUIS				
Mode of assessment: Project/Activity Recommended by Board of Studies 04-04-2014						
Approved by Academic Council No. 37 Date 16-06-2015						
17hh	Toved by Academic Council 110. 37 Date 10-00-2013					