

Analyzing the network traffic in a local area network using the Cisco Packet tracer

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Abstract— [1][2]In our new era PCs become our part of life for every personal and professional requirement. Majority of organizations depend on the finest possible working of their systems for correspondences, organization, mechanization, online business solutions, and so on. LAN is the best fundamental and significant PC system claimed by discrete organizations and might be utilized for interconnection of wide region systems. A LAN provides effective cost sharing of fast processing information handling gear, for example, mass stockpiling media, centralized server PCs or tiny computers and various types of printers. Asset sharing is generally similar as significant where a Local Area Network (LAN) serves as the entrance path for an Internet. In view of this, framework supervisor's requirement professional tools to help them with the motivation of improvement of QoS and maintenance of LANs. So in our project, a LAN system is structured utilizing Cisco Packet Tracer. This project explains just how the apparatus can be used to build up a reenactment model of the Local Area Network (LAN) for College of Engineering which contains a department like Bio Technology (BT), Civil, Mechanical, ECE and EEE or any. The examination gives a knowledge into different ideas such as IP address setup, topology plan and how to send data as packets in a solitary network and for the usage of Virtual Local Area Networks to isolate the heavy traffic produced by various systems.

Index Terms— N-Body, All-Pairs, Barnes-Hut, Parallelization, OpenMP, CUDA

I. INTRODUCTION

[4]The requirement for PC systems administration was an effect of the requirement to use PCs for exchanging information in an association in form of messages or packets, exchanging documents and data bases, etc. Regardless of whether the organization is situated in one structure or spread over a huge grounds, the requirement for systems administration the computers cannot be over underscored. As the name assumes, a Local Area Network (LAN) connects PCs in a limited physical territory . It gives high-data transfer capacity correspondence over cheap

transmission media .The corporate LAN has developed from an easy basis business segment to a profoundly vibrant, noticeable core asset that activities depend on to help everyday tasks to their market accomplishment. E-Governance is a system of open segment order and is a significant advance in the adjustment of metropolitan organization, with E-Governance joins the utilization of ICT's by government's association. The anticipated calculation utilizes insight of calculation for security of substance in e-governance executing a standard based methodology from computational Knowledge and client's present purpose of area data. On a work area PC, a recreation model had been actualized and assessment utilizing meandering client's continuous position-based data exhibits that proposed system can capably preserve wandering client position secrecy while giving better execution, ensured position privacy, and better nature of administration in e-Governance.

II. FRAMEWORK

A. Background

Cisco Packet Tracer is designed to be used as multi-tasking, that's been won't to organize and examine varied network exercises like application of dissimilar topologies, development of apt servers, subnetting and study of different network setups, configuration and different troubleshooting defined commands.

To initialize communication among two networking devices i.e., user networking devices and to organize a network, we intend to demand to pick applicable networking devices like switches , routers and interconnecting devices and build physical change of integrity by connecting cables, quick local area network seaports from the module list of packet tracer. Internet working devices square measure costly and thus it's well to perform 1st on the packet tracer to recognize the conception, performance of the designed network.

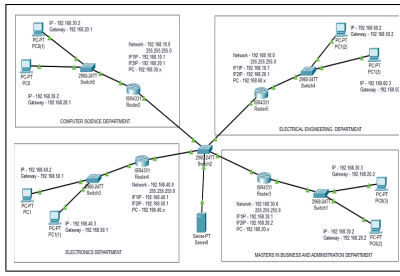


Fig. 1. Barnes-Hut tree structure



Fig. 2. Barnes-Hut tree structure

B. Framework Continued

Framework The graph of Fig. 1 is the finished graph of the LAN and at the center it connected to switch, switch and the servers framing the Network Operating Center and every one of the different departments in College are only a simple expansion of the system at the center. The allotted IP address picked to the inside system is 192.168.0.0 and it has been sub netted to acquire IP address obstructs that are allocated to various divisions and segments of this prescribed LAN.

III. LAN SIMULATION MODEL

[4]We require at least 252 hosts for every subnet the quantity of unmasked bits in the subnet mask is 8. Which infers that the amount of masked bits are 8.

A. Create and assign IP/subnet mask for VLANs:

In this VLAN, we are assigning the below gate ways to all the VLANs with ip address and subnet mask (255.255.255.0). Which is configured in the main switch of VLAN.

- ena .
- config t .
- VLAN 2 .
- VLAN 3 .
- VLAN 4.
- int VLAN 1 .
- ip address 192.168.20.1(Network ID) 255.255.255.0 (Host ID)
- int VLAN 2
- ip address 192.168.50.2(Network ID) 255.255.255.0 (Host ID)
- int VLAN 3
- ip address 192.168.20.2(Network ID) 255.255.255.0 (Host ID)
- int VLAN 4
- ip address 192.168.50.1(Network ID) 255.255.255.0 (Host ID)

B. The configuration is done between the main switch and the primary switchs of VLANs by using the cable interface we can trunk all the switchs.

- int fa0/2.
- Switchport trunk encapsulation dot1q
- switchport mode trunk.

C. In the primary switch, the interface cable are connect to the laptop and access point. Swich is used to trunk to the PC and access point.

- int fa1/1
- Switchport mode access
- switchport access VLAN 2

IV. TELL PC IN VLANs WHERE TO GET IP:

A. In this VLANs, the switch of different VLAN are getting there IP address from server.

- int VLAN 1 ip helper-address 192.168.10.1
- int VLAN 2 ip helper-address 192.168.50.2

V. TABLE : IP ADDRESS ALLOCATION

Broadcast	First Valid Host	Last Valid Host	Network Address
192.168.1.255	192.168.1	192.168.1.25	192.168.1
192.168.2.255	192.168.2	192.168.1.25	192.168.1
192.168.3.255	192.168.3	192.168.1.25	192.168.1
192.168.4.255	192.168.4	192.168.1.25	192.168.1
192.168.5.255	192.168.5	192.168.1.25	192.168.1
192.168.6.255	192.168.6	192.168.1.25	192.168.1
192.168.7.255	192.168.7	192.168.1.25	192.168.1
192.168.8.255	192.168.8	192.168.1.25	192.168.1

Fig. 3. Barnes-Hut tree structure

VI. CONFIGURING COMPONENTS



Fig. 4. Barnes-Hut tree structure

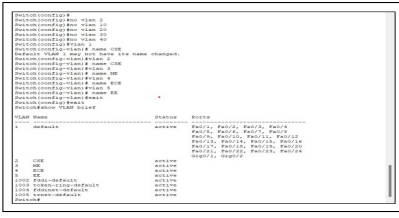


Fig. 5. Barnes-Hut tree structure

A. Fig. 2 shows the created Virtual Local Area Networks working on the switch, corresponding ID and switch ports are connected to every VLAN.

1) Fig.3.1 - 3.4: Static server pools:

B. Parallel Barnes-Hut Algorithm using OpenMP– Mass Distribution is Parallelized (Method-2)

In computing the center of mass of the nodes, even though the presence of data level dependencies restricts parallelizing, some level of parallelism can still be introduced as the computation done for each quad is independent of the other. So, all these computations can occur in parallel; this speeds up the process significantly. So, in the Algorithm-6 described below only the center of mass computation has been parallelized leaving the force computation as it is. The graphs plotted have been shown in the sections that follow.

Parallelization of the Barnes-Hut algorithm has many issues making it more complex than anything so far seen in this project. The main issue is the lack of prescience of the number of calculations done by each process. With the increase in the tree traversal depth, the number of force calculations increase and the exact depth is dependent on the position of the current body, which is random.

Algorithm 1: Parallel Barnes-Hut Algorithm (OpenMP)– Mass Distribution is Parallelized

```

1: Function compute_mass_distribution() is
2:   if new_particles == 1 then
3:     center_of_mass = particle.position
4:     mass = particle.mass
5:   else
6:     #pragma omp parallel for
7:     forall child quadrants with particles do
8:       quadrant.compute_mass_distribution
9:       #pragma omp critical
10:      mass += quadrant.mass
11:      center_of_mass = quadrant.mass *
        quadrant.center_of_mass
12:   center_of_mass /= mass

```

VII. WORK DONE AND RESULTS ANALYSIS

The sequential All-Pairs algorithm is implemented in C++. The parallelization of the algorithm is performed

using OpenMP and CUDA. OpenMP is a usage of multi-threading [11]; an expert string forks a predetermined number of slave strings and the framework separates an errand among them. The strings then run simultaneously, with the runtime environment assigning strings to distinctive processors. The segment of code that is intended to keep running in parallel is stamped likewise, with a preprocessor order that will bring about the strings to shape before the segment is executed. Each string has an *id* appended to it which can be acquired utilizing a method *omp_get_thread_num()*. After the execution of the parallelized code, the strings join over into the master string, which proceeds with forward to the end of the system.

CUDA is an extension of the C that allows the programmer to take advantage of the massive parallel computing power of an Nvidia graphics card in order to do general purpose computation [12]. In order to run efficiently on a GPU, you need to have many hundreds of threads. Generally, the more threads you have, the better. If you can break the problem down into at least a thousand threads, then CUDA probably is the best solution. When something extremely computationally intense is needed, the problem can simply call the CUDA kernel function written by the user. GPUs use massive parallel interfaces in order to connect with it's memory; is approximately 10 times faster than a typical CPU to memory interface.

This section focuses on running each algorithm in serial and in parallel. Testing Speedup, Cost and Efficiency (see equations (1), (2), (3)). All the tests for All-Pairs algorithm (openMP) are ran on nearly identical machines with the following specification:

- Model– Asus
- Processor– i5 7200U @ 4x 3.1GHz
- Memory– 8GB DDR3 at 1333MHz
- Network– 10/100/1000 Gigabit LAN Connection
- Operating System– Arch Linux

All the tests for All-Pairs algorithm (CUDA) are ran on nearly identical machines with the following specification:

- Nvidia Tesla Server
- Operating System– Ubuntu Linux

All the tests for Barnes-Hut algorithm (openMP, both methods) are ran on nearly identical machines with the following specification:

- Model– HP
- Processor– i5 6200U
- Memory– 8GB DDR3 @ 2.8GHz
- Network– 10/100/1000 Gigabit LAN Connection
- Operating System– Ubuntu Linux

$$Speedup(S) = \frac{TimeforSerialExecution}{TimeforParallelExecution} \quad (1)$$

$$Cost(C) = ParallelExecution \times NumberOfProcessors \quad (2)$$

$$Efficiency(E) = \frac{Speedup}{TimeforParallelExecution} \quad (3)$$

A. Result and Analysis of Parallel All-Pairs algorithm in OpenMP and Nvidia CUDA

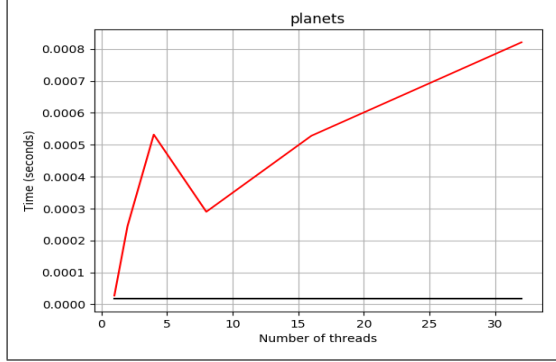


Fig. 6. Serial (black) vs. Parallel (red) execution for *Planets.txt* [4] for 5 bodies using OpenMP (Algorithm 2)

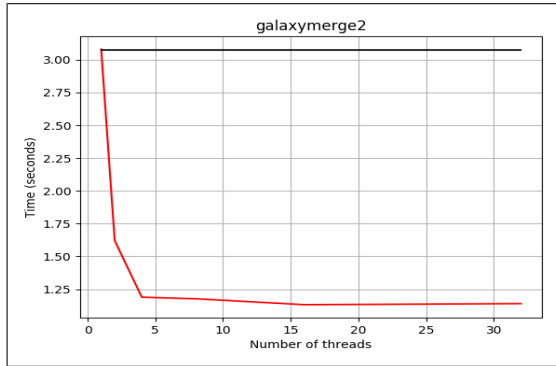


Fig. 7. Serial (black) vs. Parallel (red) execution for *galaxymerge2.txt* [4] for 4000 bodies using OpenMP (Algorithm 2)

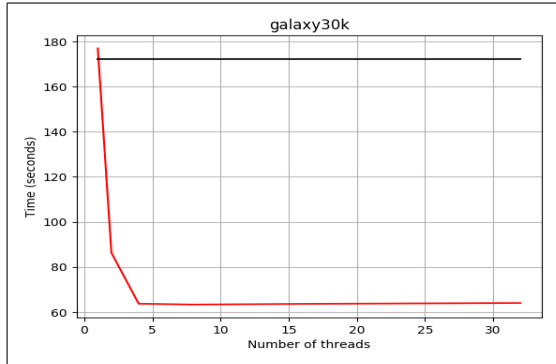


Fig. 8. Serial (black) vs. Parallel (red) execution for *galaxy30k.txt* [4] for 30002 bodies using OpenMP (Algorithm 2)

For inputs with a small number of planets we find sequential execution to be faster than parallelized OpenMP code (Fig. 3). This supports the fact that threads have a high cost of initialization, which outweighs the execution time since it is smaller in comparison. When we increase

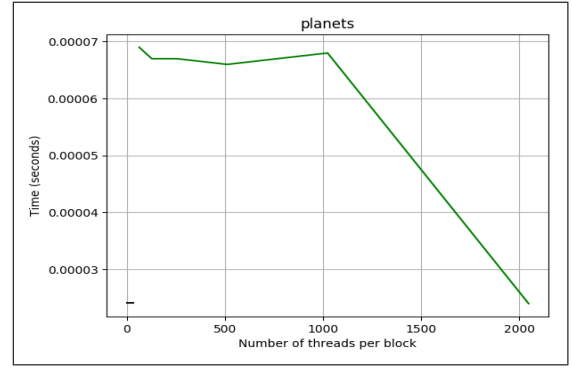


Fig. 9. Serial (black) vs. Parallel (green) execution for *planets.txt* [4] for 5 bodies using CUDA (Algorithm 3)

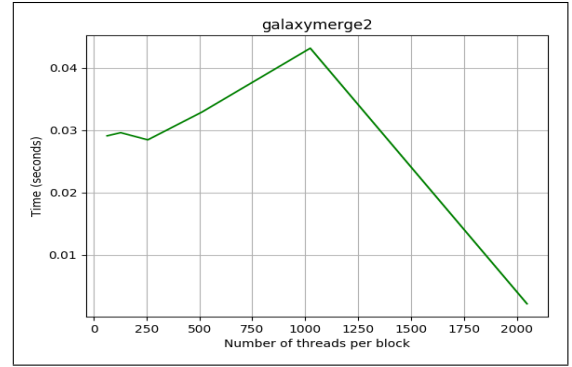


Fig. 10. Serial (black) vs. Parallel (green) execution for *galaxymerge2.txt* [4] for 4000 bodies using CUDA (Algorithm 3)

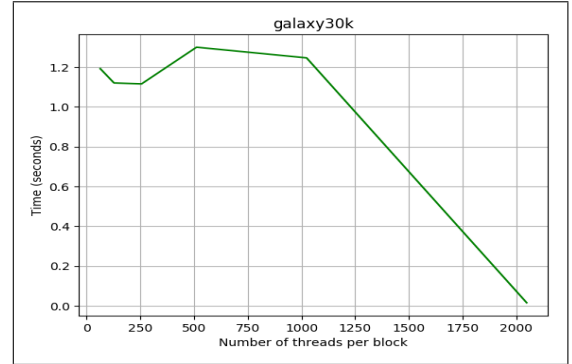


Fig. 11. Serial (black) vs. Parallel (green) execution for *galaxy30k.txt* [4] for 30002 bodies using CUDA (Algorithm 3)

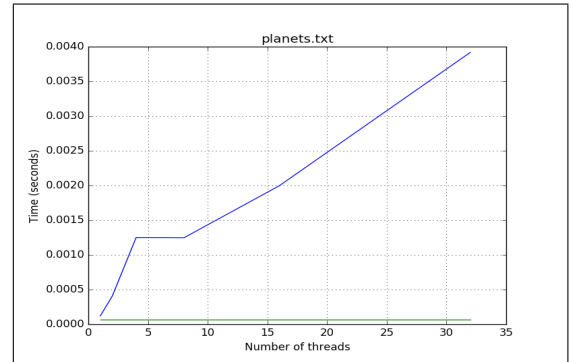


Fig. 12. Serial (green) vs. Parallel (blue) execution for *Planets.txt* [4] for 5 bodies using OpenMP (Algorithm 5)

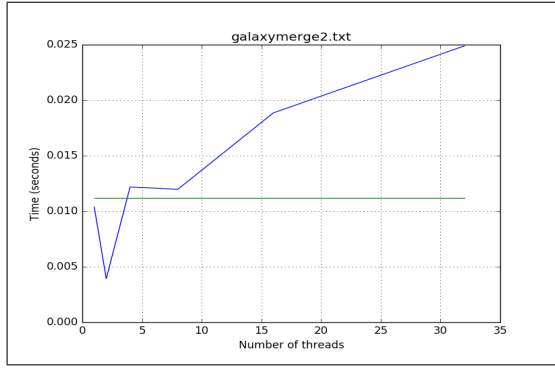


Fig. 13. Serial (green) vs. Parallel (blue) execution for *galaxymerge2.txt* [4] for 4000 bodies using OpenMP (Algorithm 5)

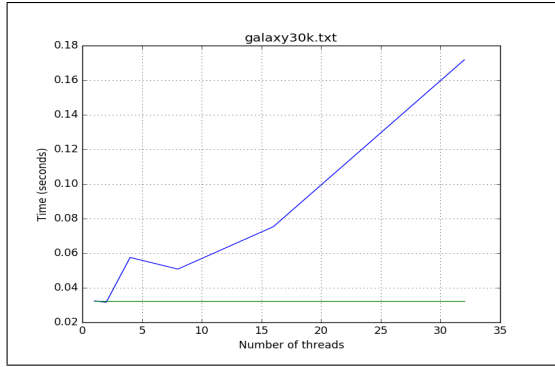


Fig. 14. Serial (green) vs. Parallel (blue) execution for *galaxy30k.txt* [4] for 30002 bodies using OpenMP (Algorithm 5)

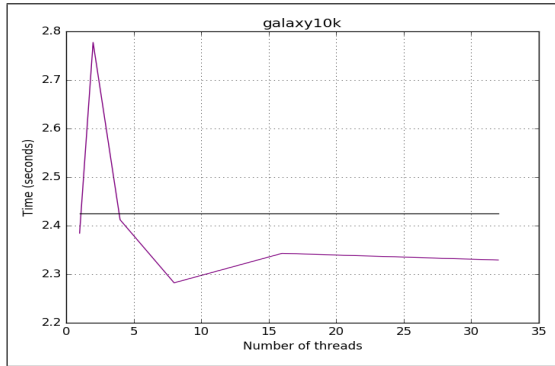


Fig. 15. Serial (black) vs. Parallel (purple) execution for *galaxy10k.txt* [4] for 10001 bodies using OpenMP (Algorithm 6)

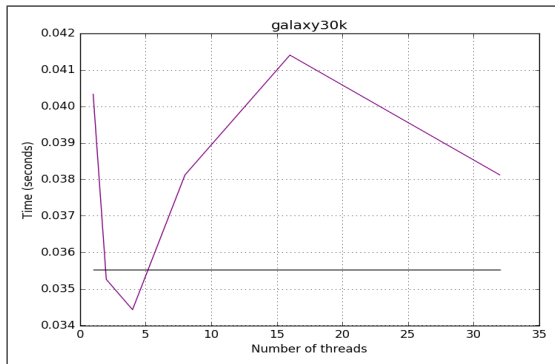


Fig. 16. Serial (black) vs. Parallel (purple) execution for *galaxy30k.txt* [4] for 30002 bodies using OpenMP (Algorithm 6)

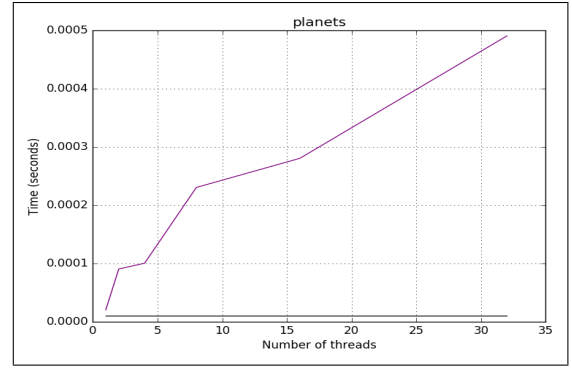


Fig. 17. Serial (black) vs. Parallel (purple) execution for *planets.txt* [4] for 5 bodies using OpenMP (Algorithm 6)

the input size, the parallel code runs much faster than the sequential counterpart. Since the input is large, the time of execution is also larger than the thread spawn overheads. (Fig. 4). However we notice that increasing the threads beyond a certain value does not cause any further decrease. This is because the CPU on the test machine cannot support more than 4 threads. Therefore we see a flat curve after. For an input file with very large inputs the graph remains the same as the previous graph. However there is a larger speedup (Fig. 5).

From Figures 3, 4 and 5 we observe that there is a large speedup for input files with a large size and this speedup is limited to the number of threads on the test machine. For small inputs, sequential execution remains faster than parallelization with OpenMP. In the case of parallelization with CUDA, we see an exponential decrease in CUDA time. This follows from the fact that the GPU has an exponentially larger thread pool as compared to the CPU (Figures 6, 7, 8).

For a very small input the communication over PCI lanes is the bottleneck as execution time is negligible. Hence we see that sequential is comparable to the CUDA program. In the case of Fig. 6 we find parallel execution with CUDA an order of 100 times faster than sequential. As the number of threads per block is increased beyond a certain value the execution time decreases drastically. Fig. 8 shows CUDA execution times for an input file of very large size. The speedup of CUDA with 2048 threads per block over sequential is approximately 350. This means with heavy parallelization of the GPU, we can achieve the results much quicker than doing the same on the CPU. This confirms the fact that GPUs typically have more multiprocessing capabilities than the CPU.

B. Result and Analysis of Parallel Barnes-Hut Algorithm using OpenMP

In Fig.9, the sequential algorithm proved to be efficient as compared to OpenMP. One explanation for the same would include the communication overhead and thread overhead for such small dataset (5 bodies). In Fig. 10, it was observed that the OpenMP implementation performed better in the case of 2 and 4 threads but the over-

heads increased as the number of threads increased from 4. In the case of Fig. 11, the Barnes-Hut implementation in OpenMP algorithm performed better in the case of 2 threads but increased progressively before reducing once in 8 threads and increasing again.

In Fig. 12, the time reduced drastically for 8 threads and above. Although, an anomaly was observed for 4 threads. In case of Fig. 13, it was observed that for *galaxy30k* performed better than the sequential for 4 and 8 threads and then increased linearly with the number threads, again changing the trend from 16 threads onwards. As observed in Fig. 14 for *planets.txt* the overhead increases progressively. Hence, we can infer that the dataset *planets.txt* has data which does not go well with the tree building methodology.

The value of θ determines how deep the Barnes-Hut algorithm traverses the tree. The smaller the value the deeper it goes, increasing accuracy but at the cost of an increased number of calculations and therefore a slower running time. Experimentally it can be seen that as θ tends to 0 the number of calculations increases, as expected. As θ reaches 0.3 the number of calculations begin to rise quickly and at 0.1 the larger problems have to compute many more calculations. When θ is 0 the number of calculations is equal to the number of bodies in the set. This shows the algorithm has become just as complex as the All-Pairs method, with each body needing to calculate forces due to every other star.

VIII. CONCLUSIONS

In this paper, we analyzed two algorithms to solve the classical N-Body problem– the naive All-Pairs Algorithm and quad-tree based Barnes-Hut Algorithm in OpenMP and CUDA. Compared to the sequential execution we noticed a decrease in execution time till a certain level of parallelization, after which the time either remained the same or increased. The performance of these algorithms can be further bettered by running the algorithms on a processor with a higher multiprogramming support.

The parallel direct method scaled linearly with respect to the number of processes. The communication overhead for the parallel direct method is negligible as the number of stars is so small, but as more processes are added the algorithm becomes plausible for larger numbers of N, but with increasing N will come increasing communication overheads. Due to limitations with time this project only implemented a simple parallel version of the Barnes-Hut algorithm that contains no load balancing. The computation time of the Parallel Barnes-Hut scaled almost linearly, but with an increasing number of processes came an increasing communication overhead which soon outweighed the benefit seen due to the increase in computation time. The Barnes-Hut algorithm can offer substantial increases in running time depending on the choice of θ . This shows how well the naive method is improved by parallel computing.

APPENDIX

The appendix shows the analysis of the Barnes-Hut algorithm implemented in Parallel using OpenMP (method-1). The Sequential and Parallel times have been shown in Table 1; for all the galactic datasets [4] with number of bodies ranging from 5 to 30002.

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TABLE I
PERFORMANCE OF SERIAL CODE VS. PARALLEL CODE ON GALACTIC DATASETS [4] OF BARNES-HUT ALGORITHM IN OPENMP (METHOD-1)

Dataset	Number of Particles	Serial Time (seconds)	Parallel Time (seconds)					
			Number of Threads					
			1	2	4	8	16	32
asteroids1000.txt	1000	0.023097	0.020348	0.021905	0.030464	0.063325	0.121116	0.221256
cluster2582.txt	2582	0.004927	0.005837	0.005042	0.011231	0.008328	0.011733	0.014243
collision1.txt	2000	0.004917	0.004829	0.004447	0.006030	0.005751	0.009468	0.012608
collision2.txt	2002	0.006227	0.006008	0.006098	0.006309	0.006821	0.009951	0.013182
galaxy1.txt	802	0.015414	0.015616	0.015217	0.020928	0.045315	0.072090	0.110689
galaxy2.txt	652	0.012274	0.012615	0.014664	0.023931	0.028826	0.040485	0.072064
galaxy3.txt	2001	0.091639	0.087738	0.094466	0.141264	0.264529	0.488200	0.975077
galaxy4.txt	502	0.012875	0.013325	0.010431	0.012065	0.027304	0.037397	0.051786
galaxy10k.txt	10001	2.325312	2.357691	2.422882	3.557520	6.697054	13.312913	27.061886
galaxy20k.txt	20001	13.663441	15.492622	16.259973	23.813991	45.588013	88.301931	160.741782
galaxy30k.txt	30002	0.032405	0.032411	0.031647	0.057545	0.050811	0.075314	0.171779
galaxyform2500.txt	2500	0.007052	0.005922	0.006162	0.006707	0.008641	0.011501	0.016563
galaxymerge1.txt	2000	0.004920	0.005160	0.004812	0.006784	0.006742	0.008701	0.018789
galaxymerge2.txt	4000	0.011205	0.010364	0.003930	0.012193	0.011976	0.018860	0.024891
galaxymerge3.txt	2901	0.009433	0.009095	0.009045	0.015460	0.011692	0.012852	0.019202
planets.txt	5	0.000070	0.000120	0.000406	0.001250	0.001246	0.001997	0.003918
saturnrings.txt	11987	0.024471	0.024749	0.020095	0.025863	0.032043	0.038763	0.064468
spiralgalaxy.txt	843	0.017879	0.017627	0.023605	0.024740	0.052584	0.091534	0.166260