

Computer Organization and Architecture

PRE-REQUISITES FOR THE COURSE PART 1

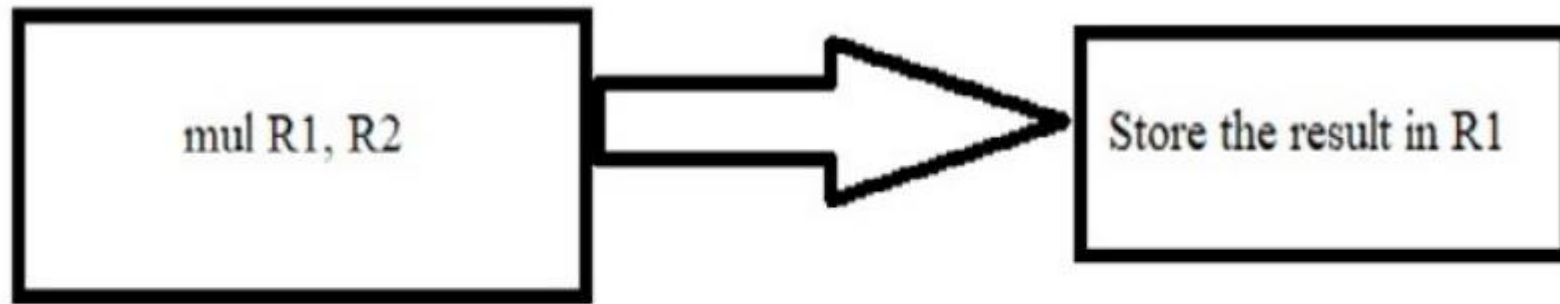
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BASIC TERMINOLOGIES REQUIRED. . . .??

- RISC ARCHITECTURE vs CISC ARCHITECTURE

CISC- Complex Instruction Set Computer

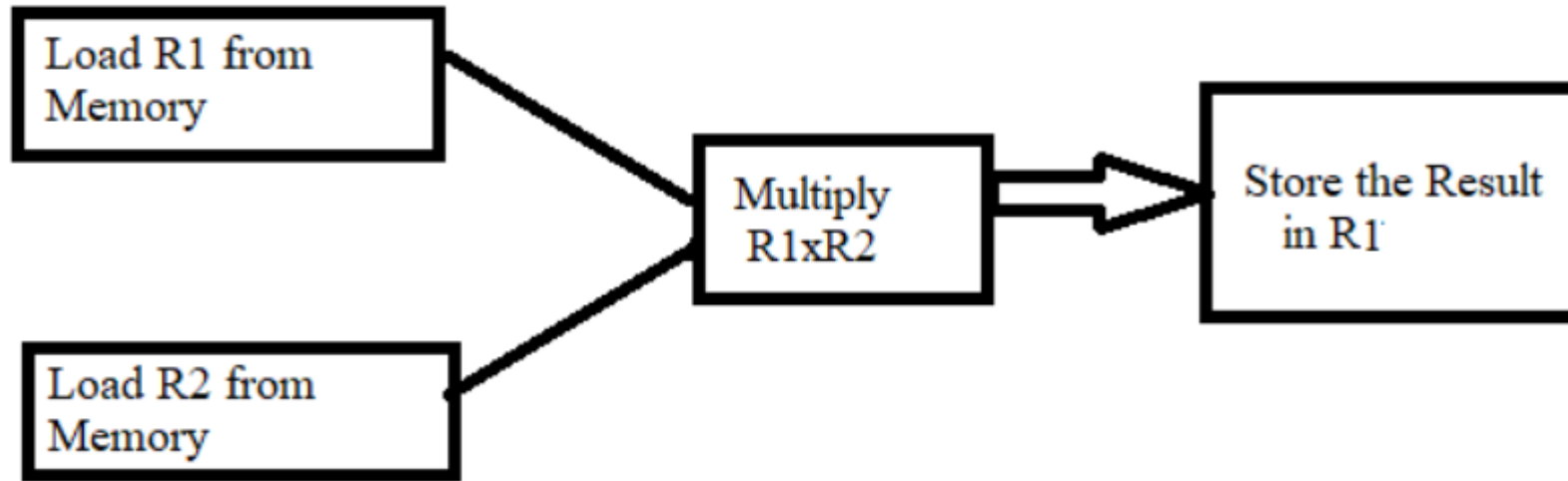
- CISC Approach: Simple Coding, Hardware More Responsibility



Intel Case Scenario

RISC-Reduced Instruction Set Computer

- RISC Approach: Complex Coding, Simple Hardware



[Apple Case Scenario]

CISC Vs RISC

Differences between CISC vs RISC Architectures??

CISC	RISC
More Complexity in hardware	More Complexity in software
CISC Instructions are variable based clock	RISC Instructions are one clock cycle
CISC approach based on limited registers	Since load and store based approach,more registers are there in RISC approach
Numerous addressing modes	Limited addressing modes
Many Instruction format	Limited Instruction formats,with fixed size

Table: CISC Vs RISC